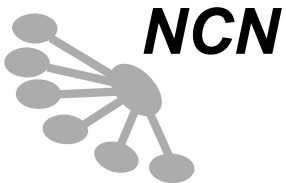


Nanotechnology 101 Series

Transistors 101

Mark Lundstrom
Purdue University
Network for Computational Nanotechnology
West Lafayette, IN USA



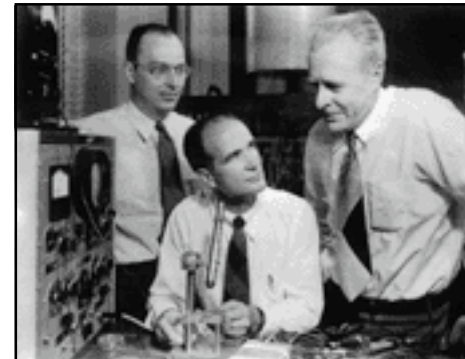
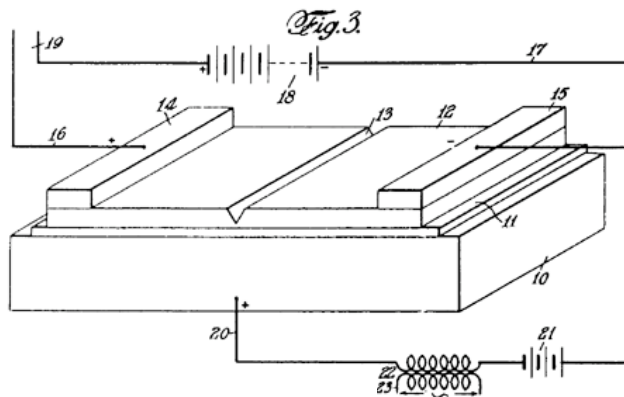
www.nanohub.org

PURDUE
UNIVERSITY

what do transistors do?



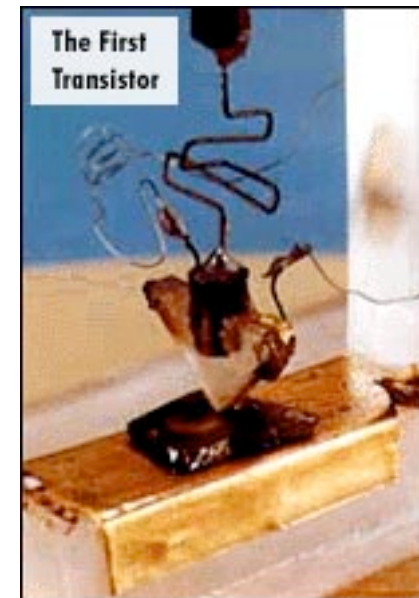
Field-Effect Transistor
Lillienfeld, 1925
Heil, 1935



Bardeen, Shockley,
and Brattain, 1947

“The transistor was probably the most important invention of the 20th century,”

Ira Flatow, Transistorized!
www.pbs.org/transistor



copyright: Lucent / Bell Labs

transistors



- junction transistor, 1951
- silicon BJT, 1954
- MOSFET, 1960

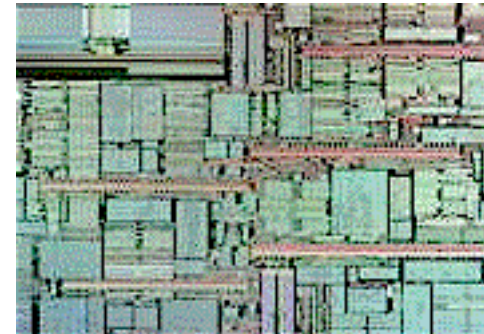
integrated circuit



Kilby and Noyce (1958, 1959)

- commercial IC's, 1961
- PMOS IC's, 1963
- CMOS invented, 1963
- NMOS IC's, 1970

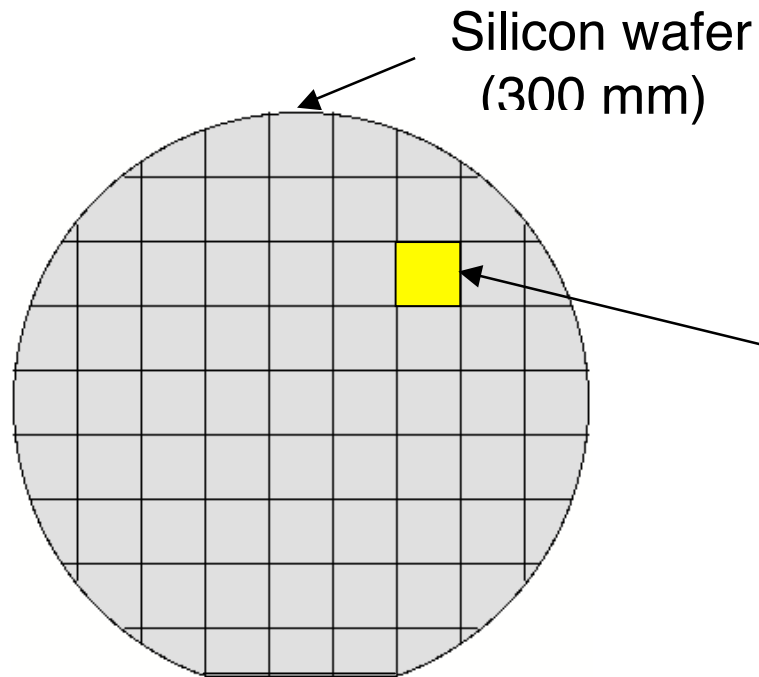
Intel 4004



Hoff (1971)

~2000 transistors

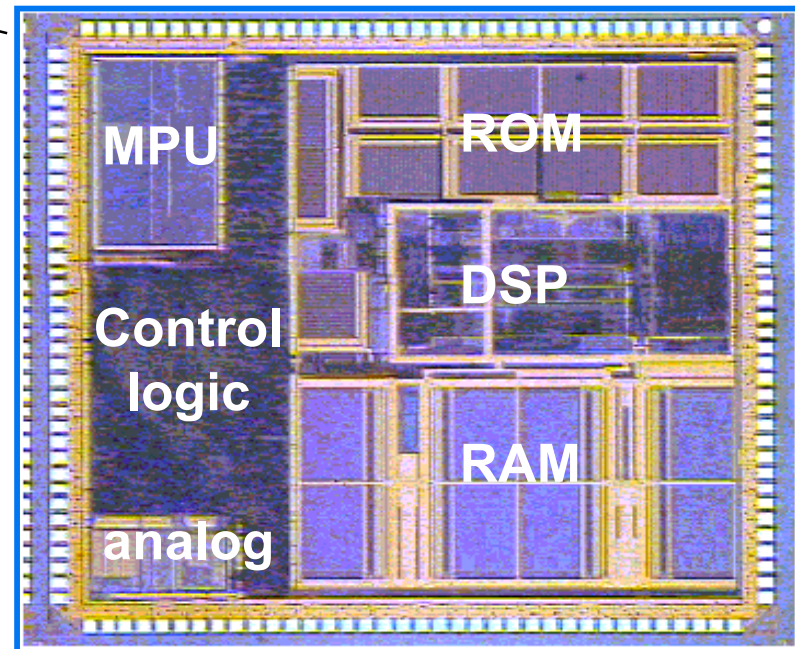
silicon microelectronics



Silicon "chip"
(~ 2 cm x 2 cm)

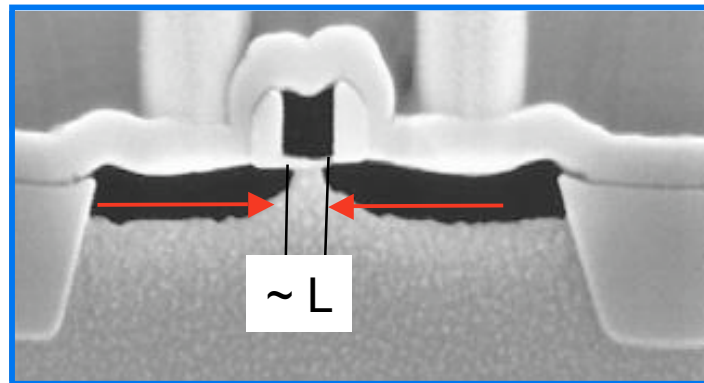


Intel



TI cell phone chip

Transistor scaling



Each technology generation:

(scaling)

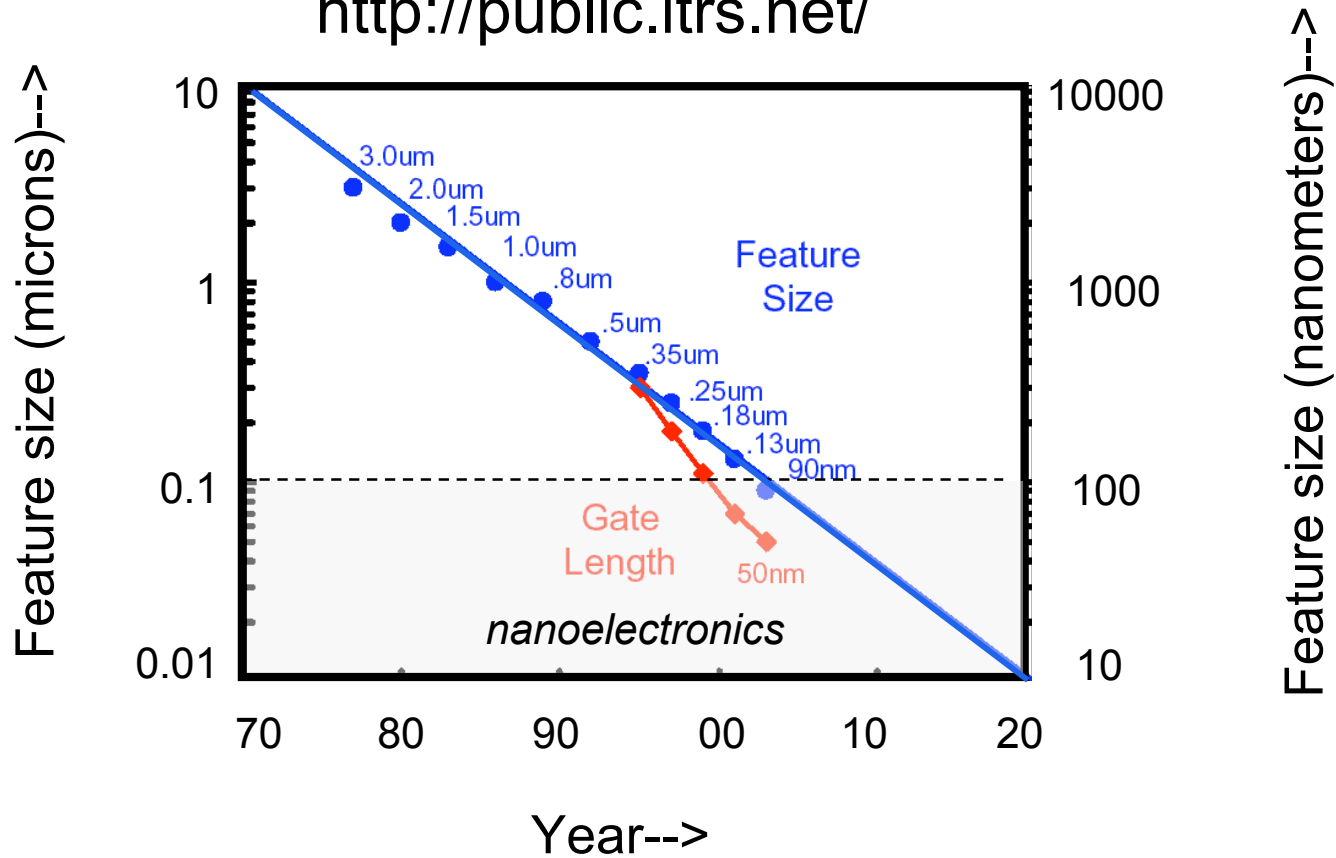
$$L \rightarrow L/\sqrt{2} \quad A \rightarrow A/2$$

Number of transistors per chip doubles

(Moore's Law)

Moore's Law

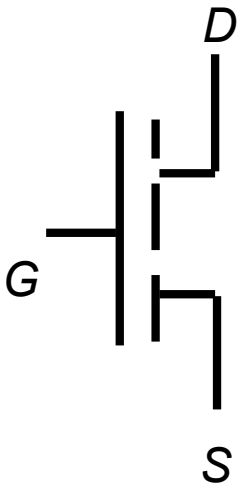
<http://public.itrs.net/>



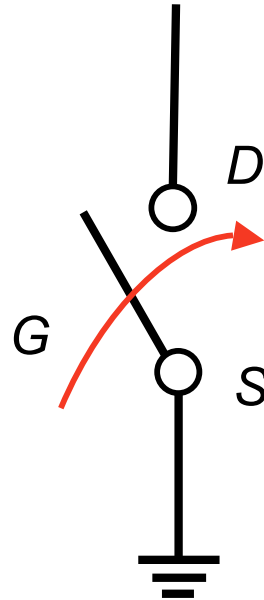
(L = 6 nm (IBM, 2002)
L = 5 nm (NEC, 2003))

applications

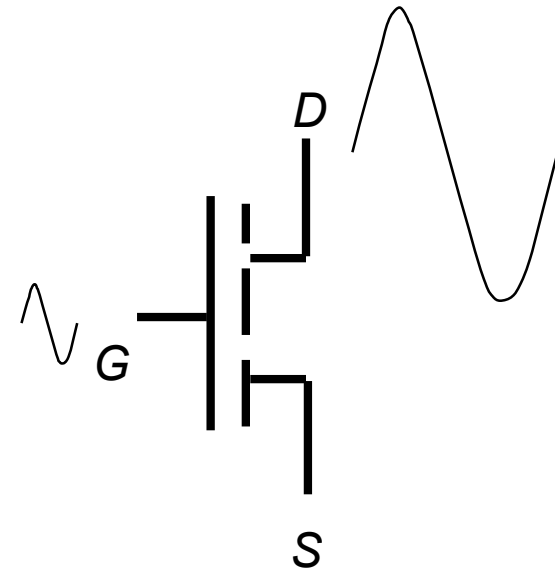
symbol



switch



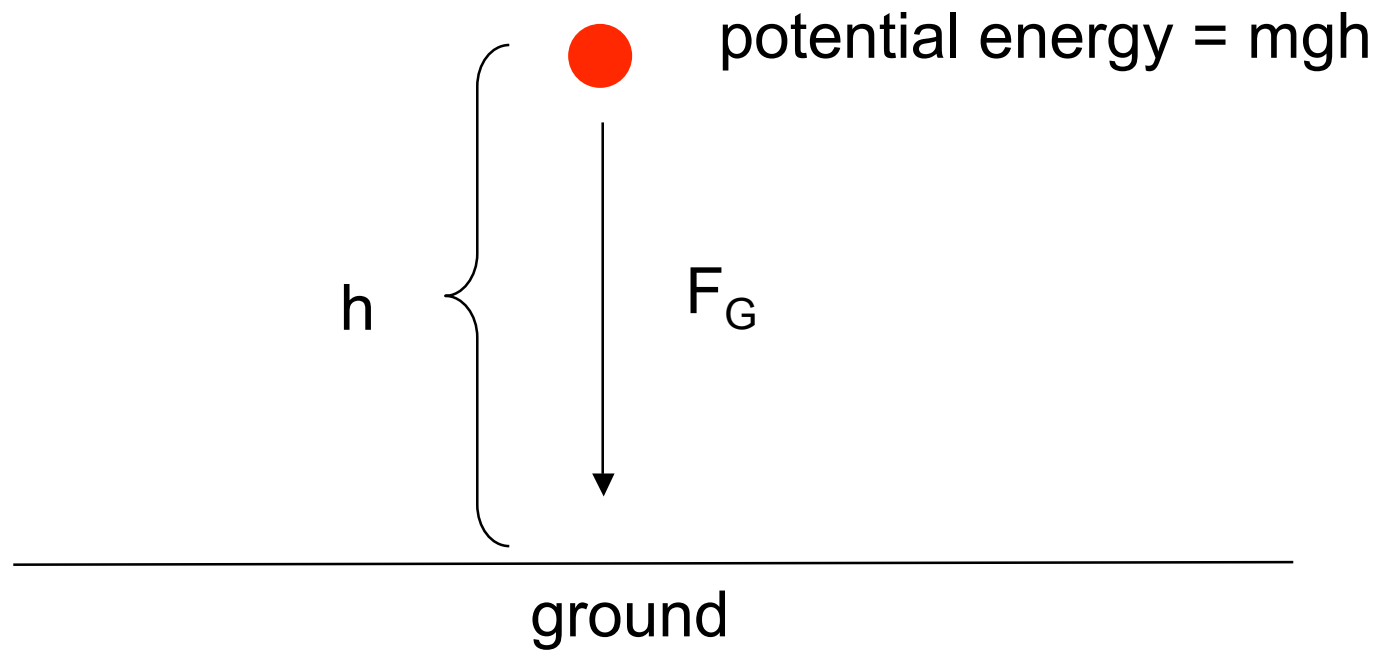
amplifier



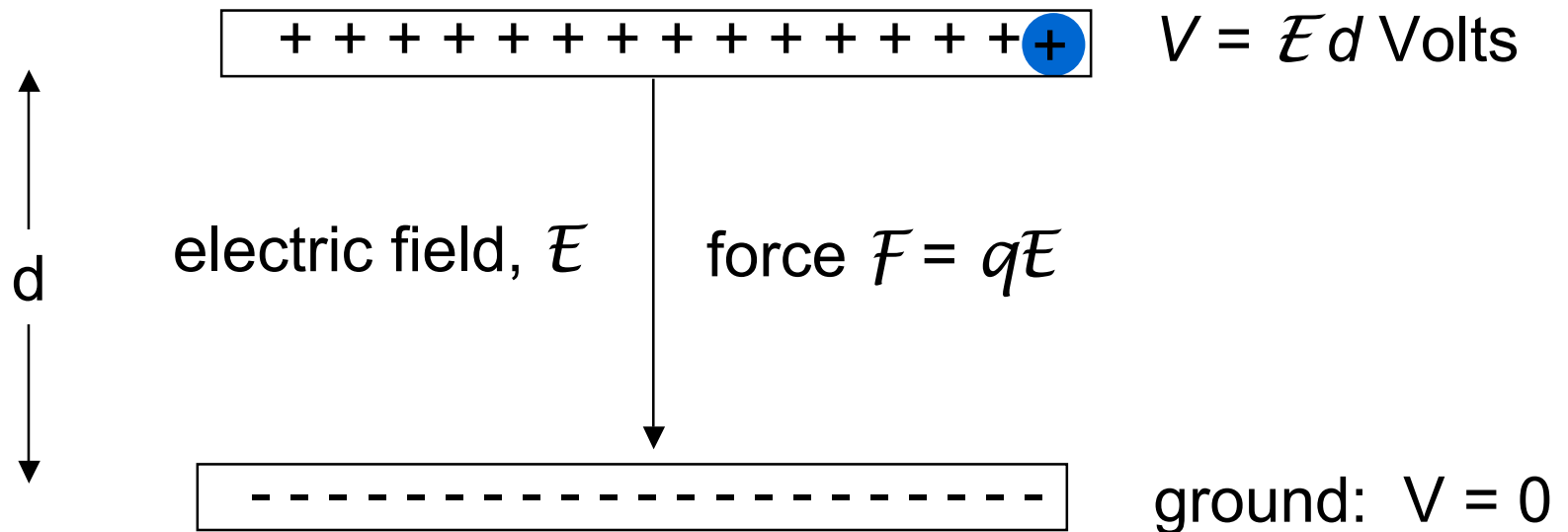
EE fundamentals

- 1) Voltage
- 2) Current
- 3) Resistance
- 4) I-V characteristics
 - resistor
 - voltage source
 - current source
- 5) Metals, insulators, and semiconductors

voltage

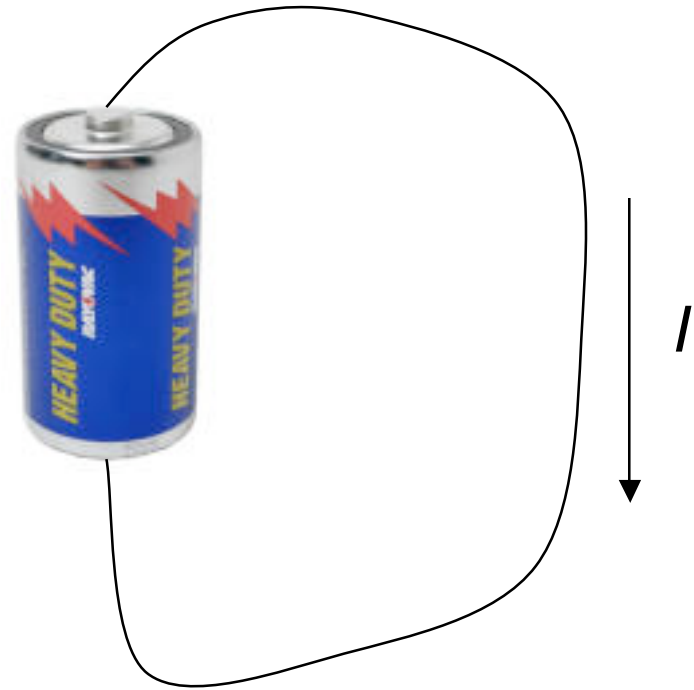
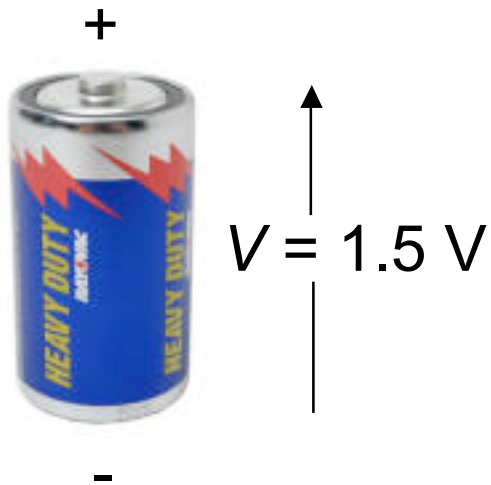


voltage

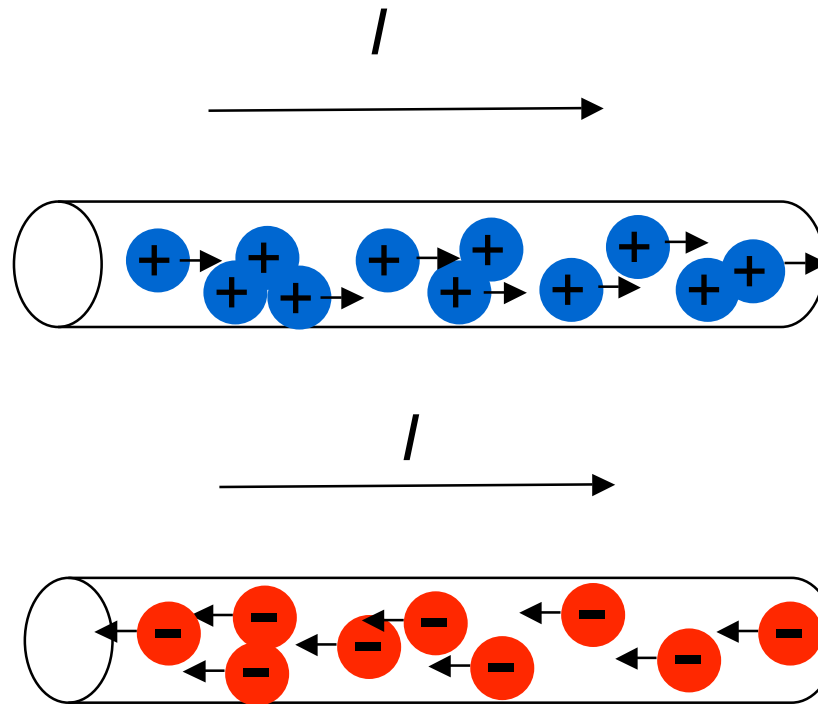


potential energy: $E = q\mathcal{E} d$ Joules

voltage

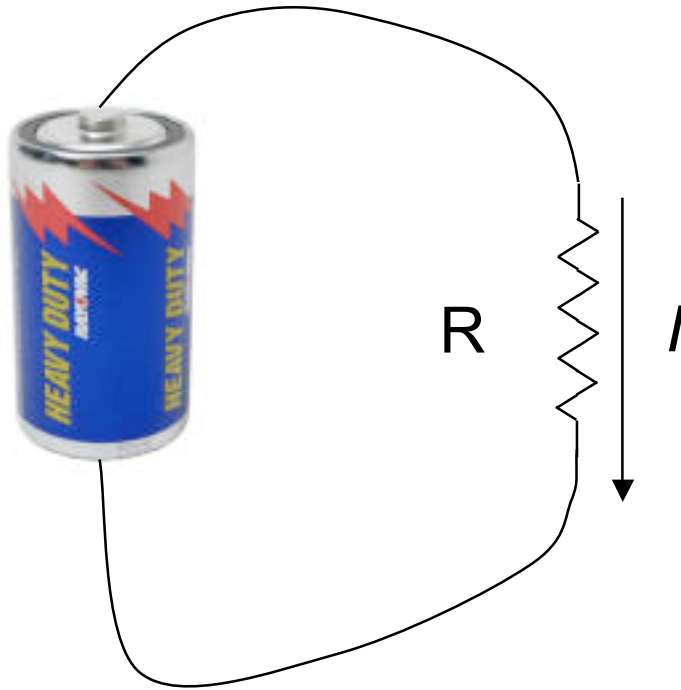


current



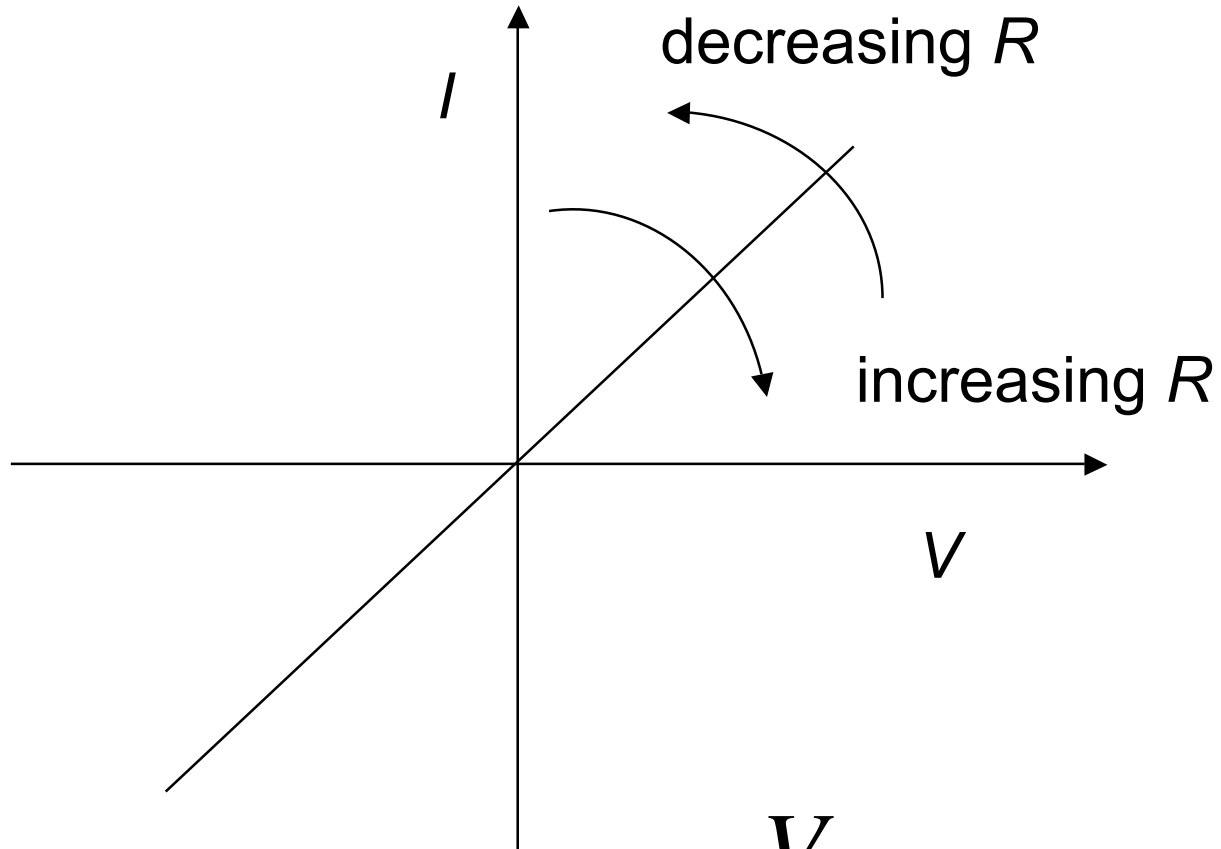
$$I = \frac{Q}{\tau} \quad \text{C/sec} = \text{amperes}$$

resistance



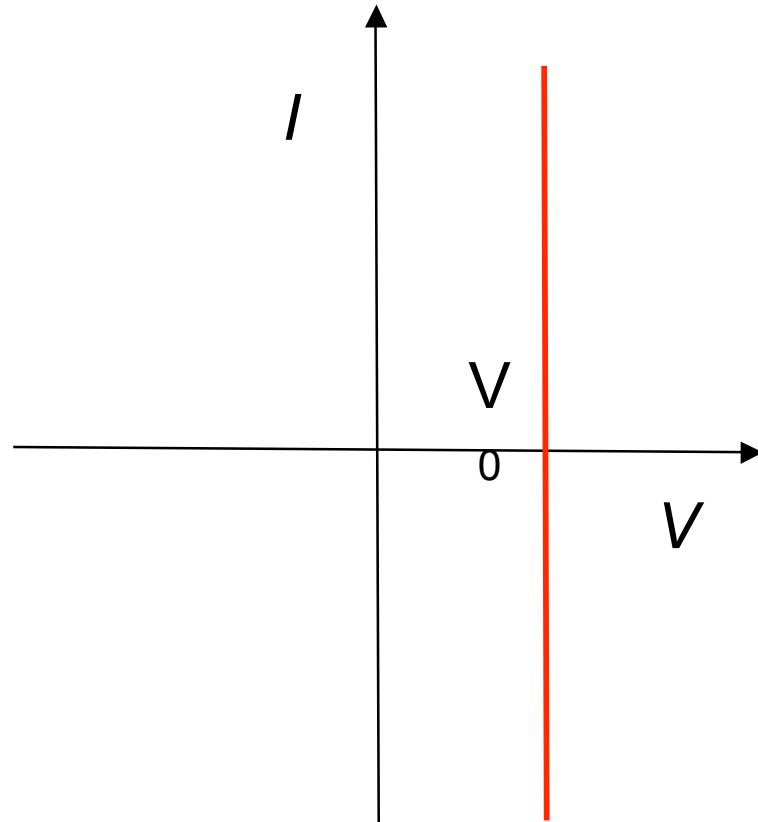
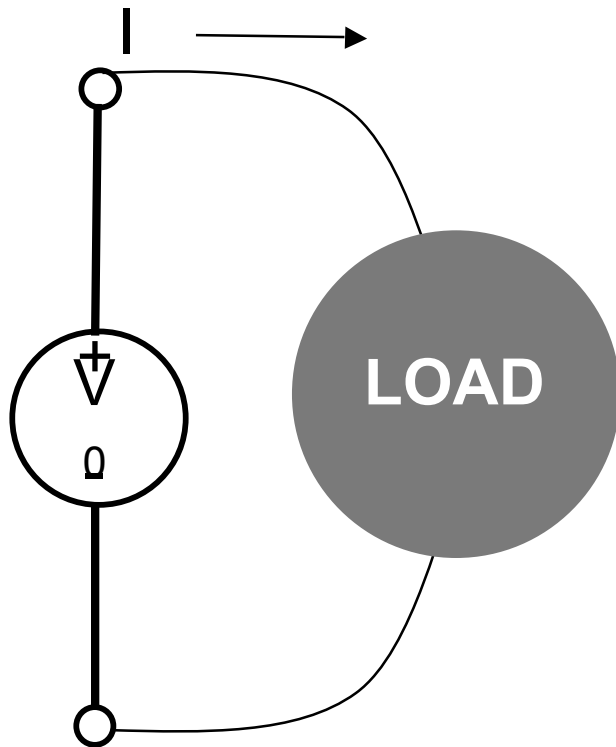
resistance (ohms Ω)

current-voltage characteristic

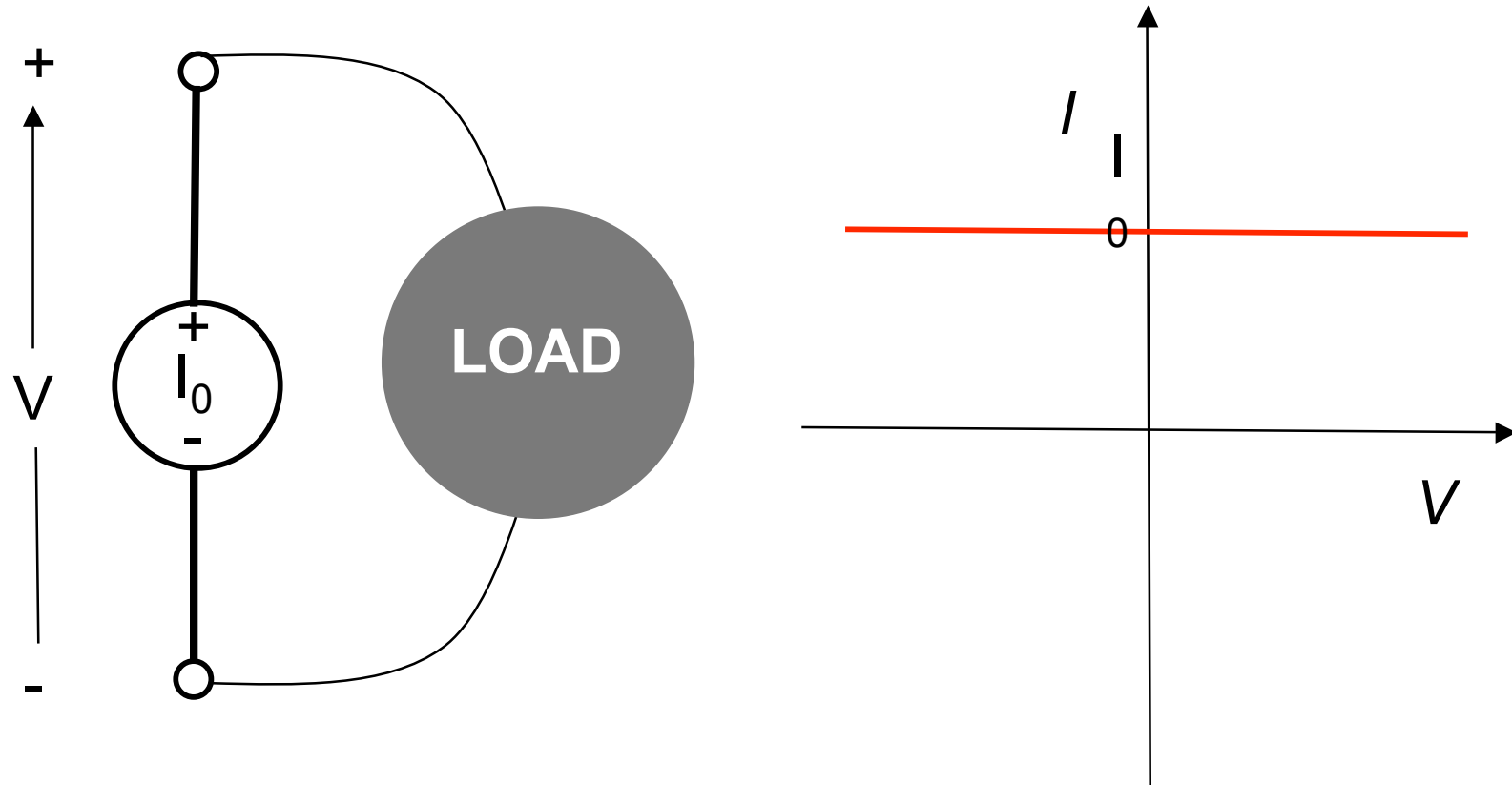


Ohm's Law:
$$I = \frac{V}{R}$$

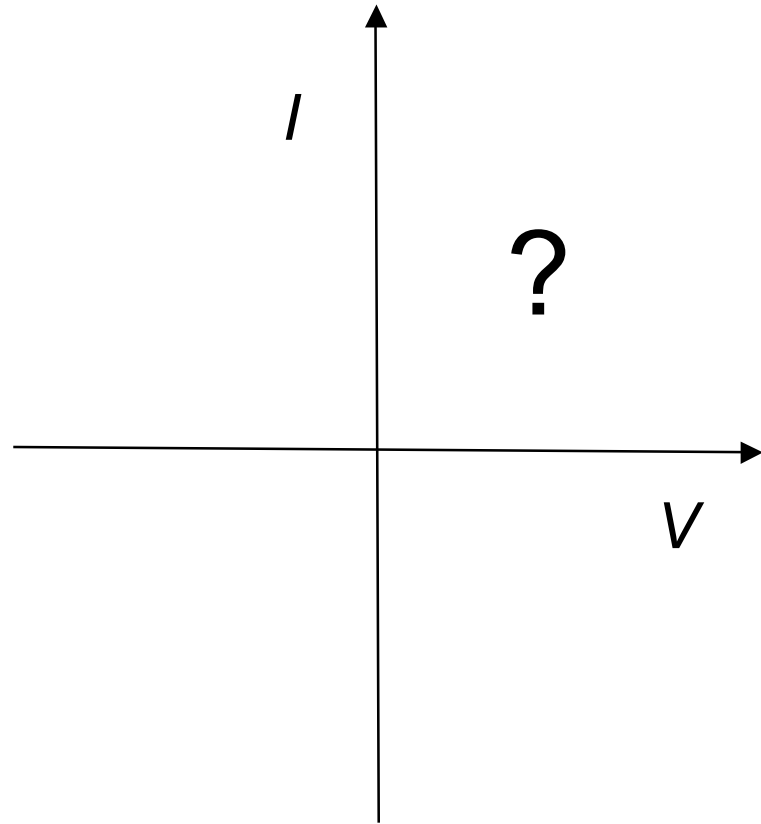
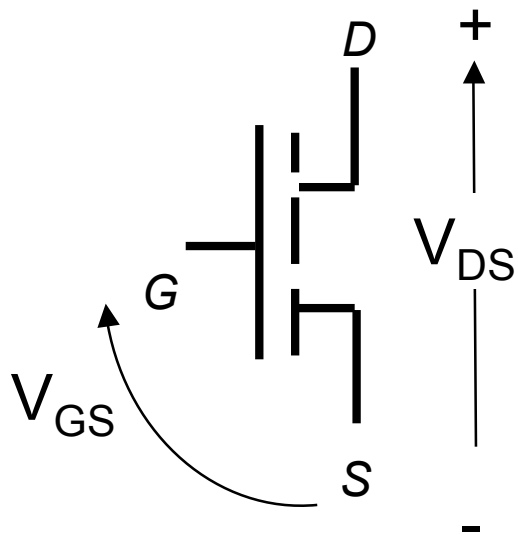
ideal voltage source



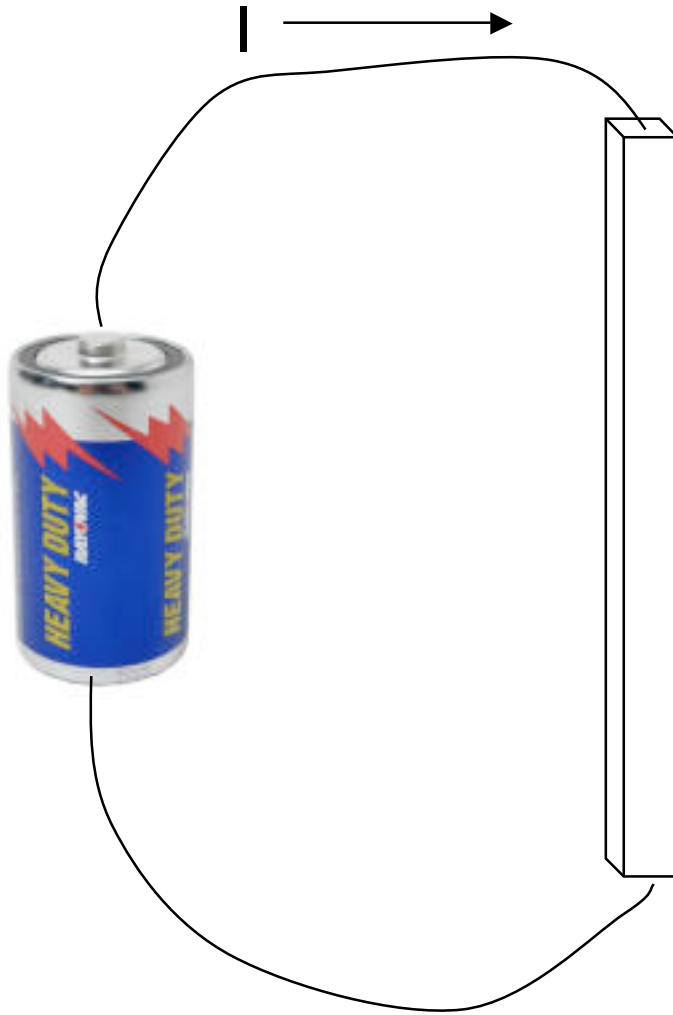
ideal current source



transistor



metal



Au

- good conductors

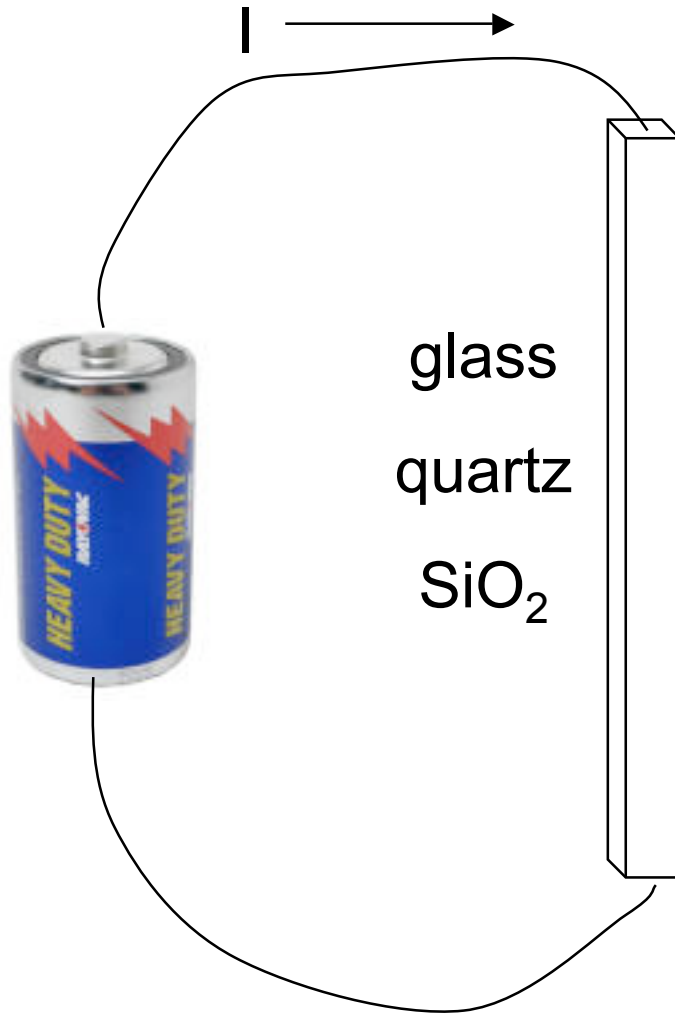
Ag

- resistance low

Cu

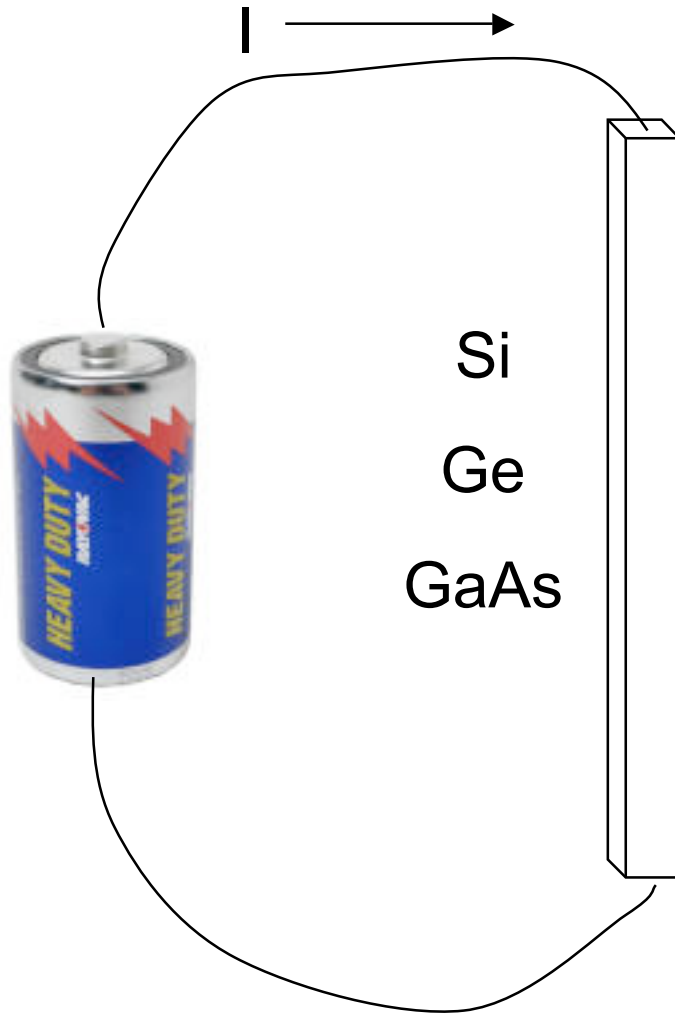
- conduction by electrons

insulator



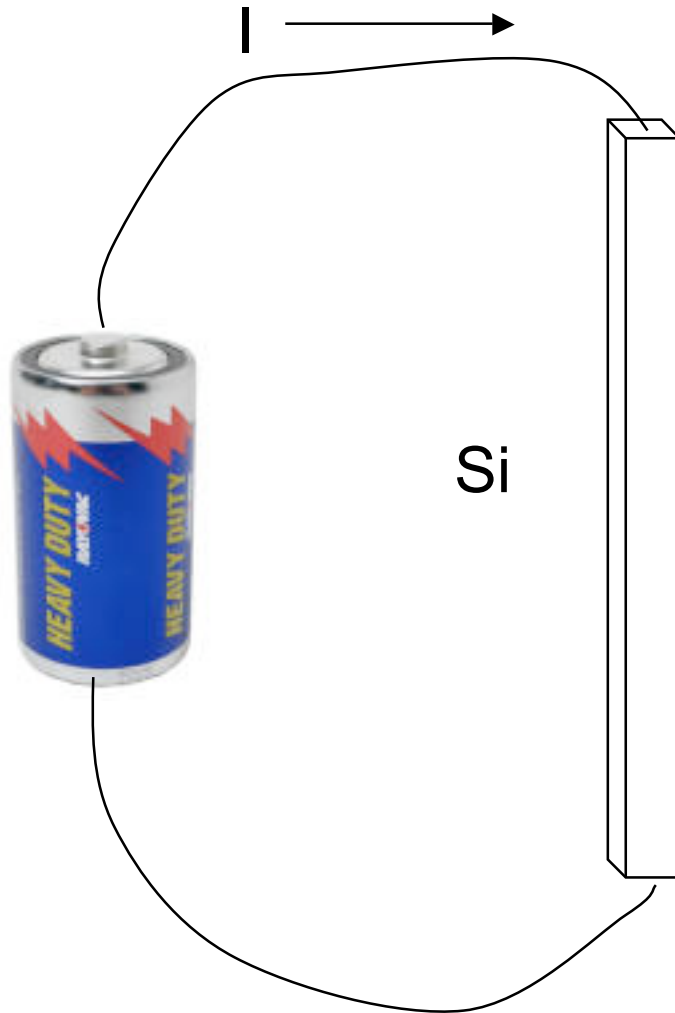
- very poor conductors
- resistance very high

semiconductor



resistance greater than a metal ($\sim 1 \text{ }\Omega$) but less than an insulator ($\sim 10^6 \text{ }\Omega$)

semiconductor doping



intrinsic semiconductor:
e.g. pure Si

doped semiconductor:
< 1% 'impurities'

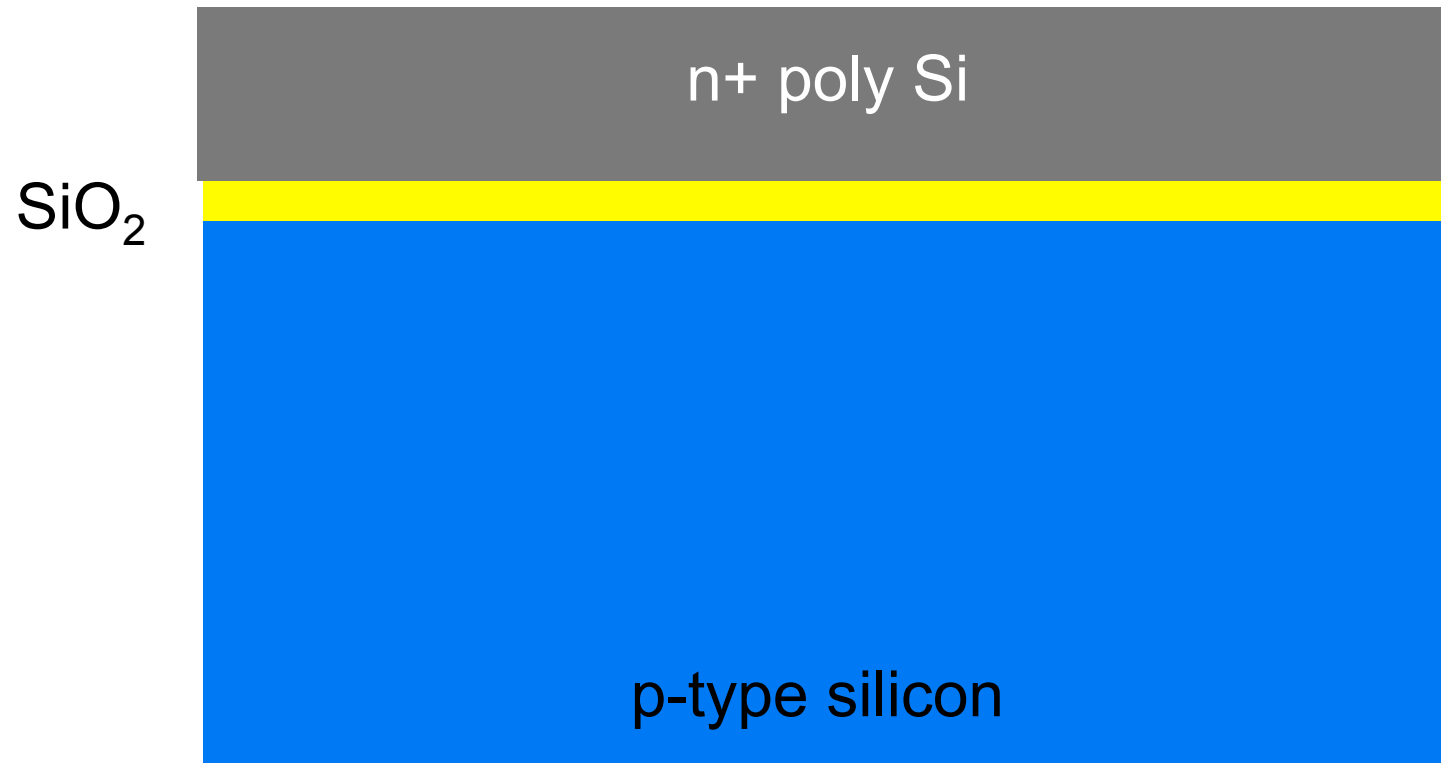
p-type:
boron impurities
conduction by + charges

n-type:
phosphorus impurities
conduction by - charges

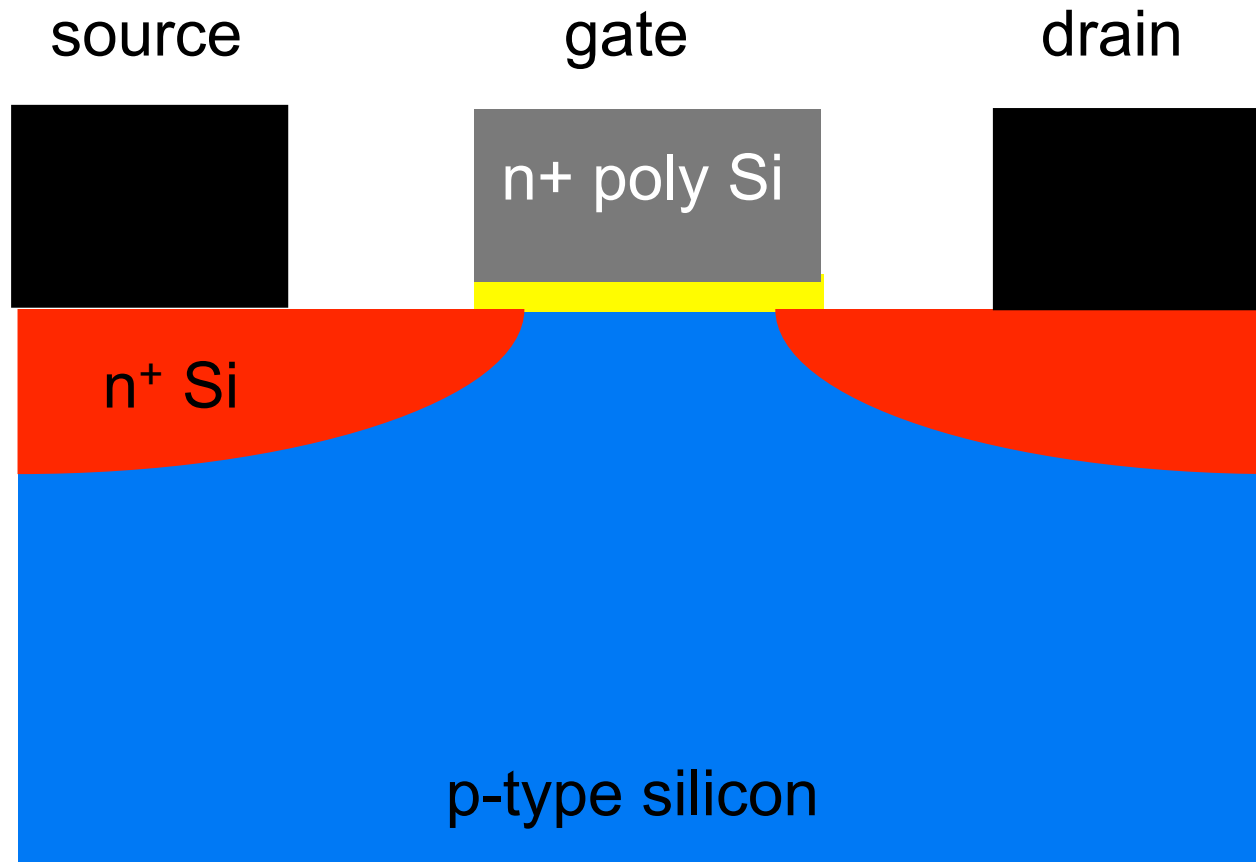
EE fundamentals

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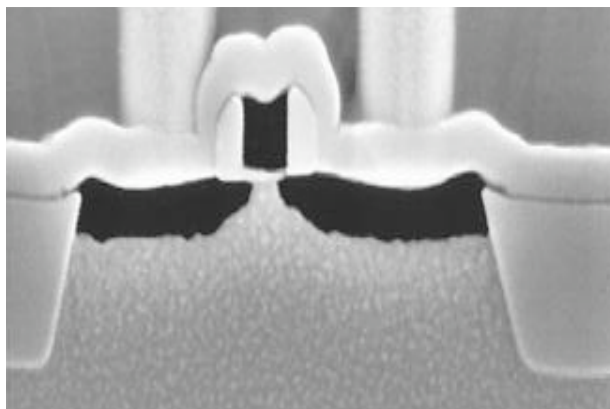
building a transistor



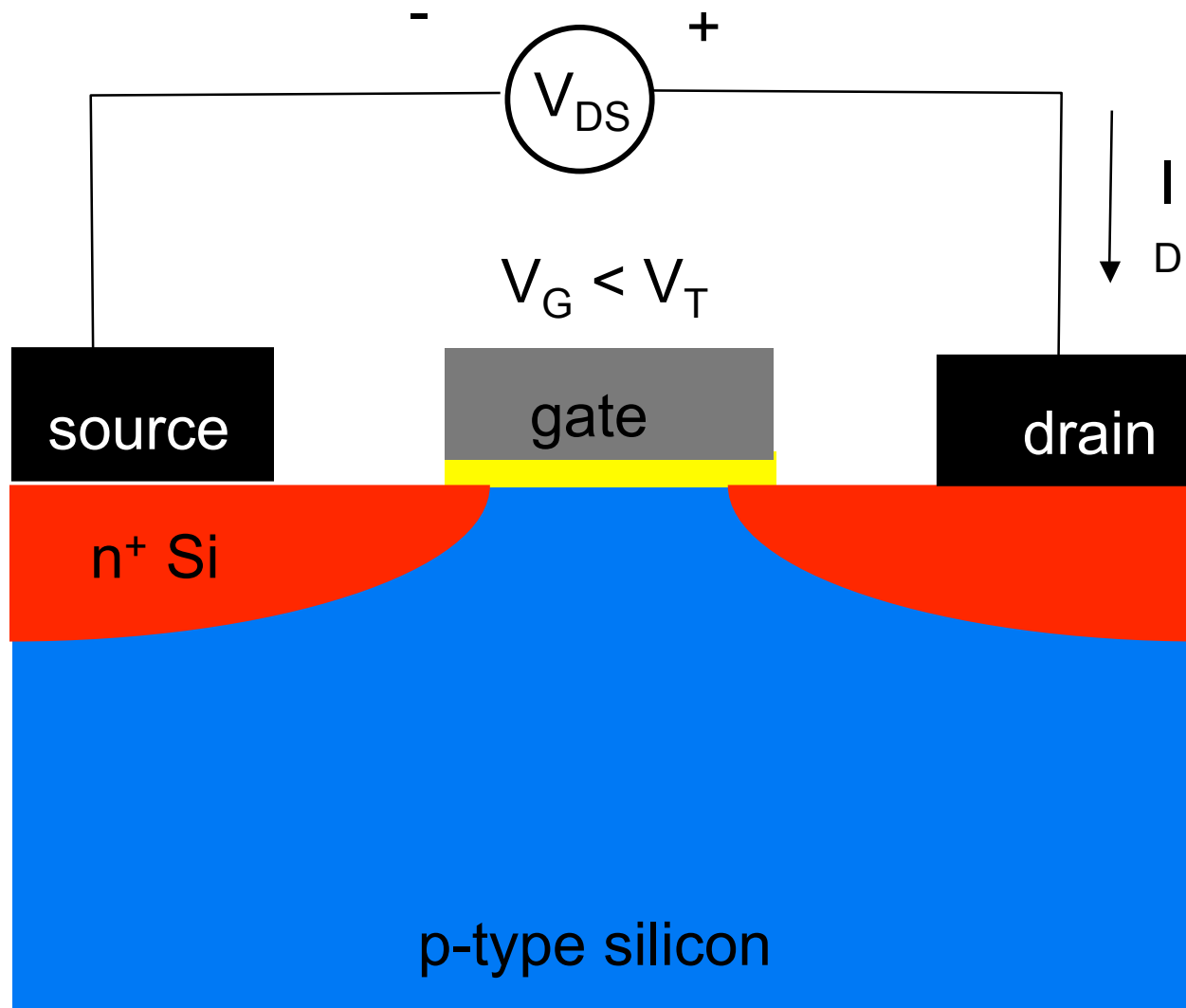
building a transistor



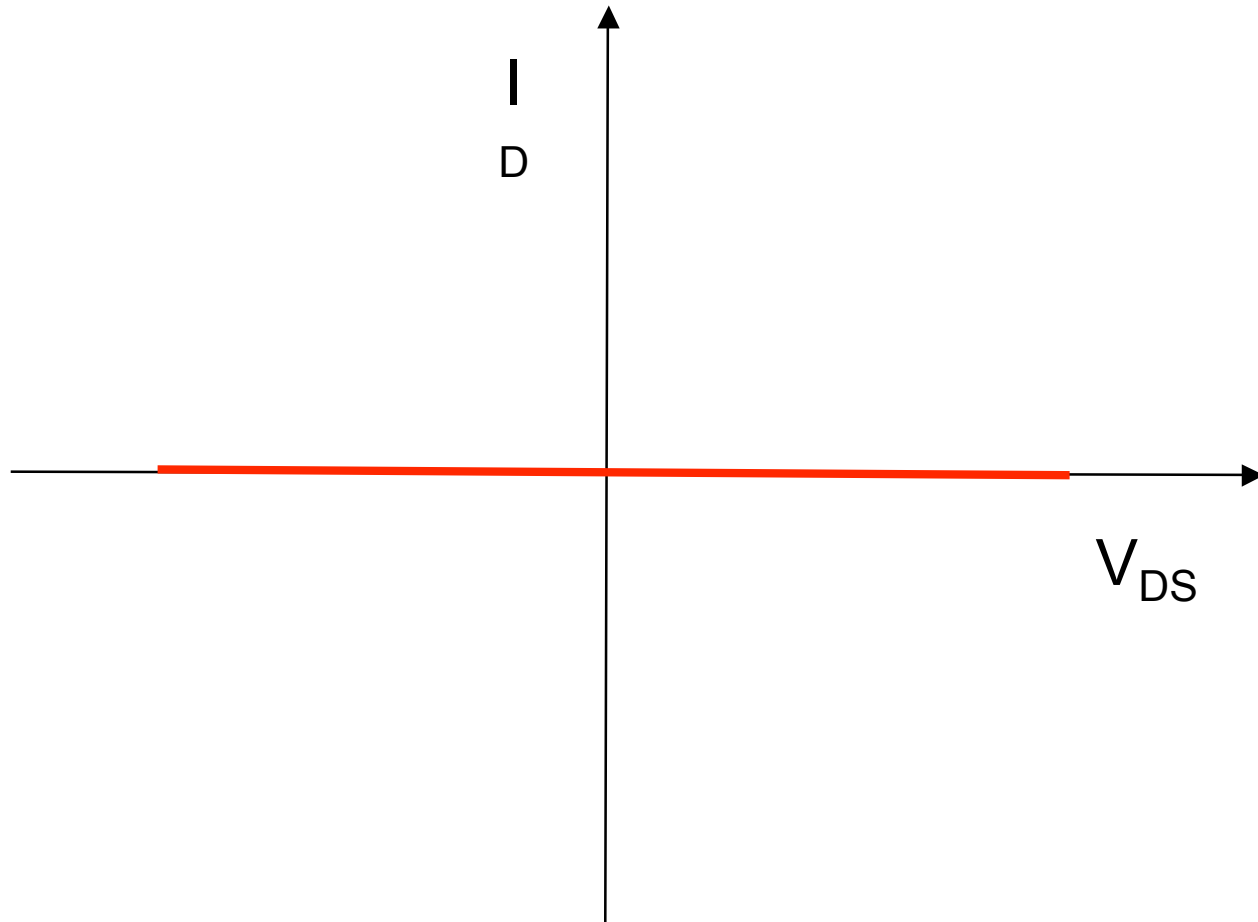
S G D



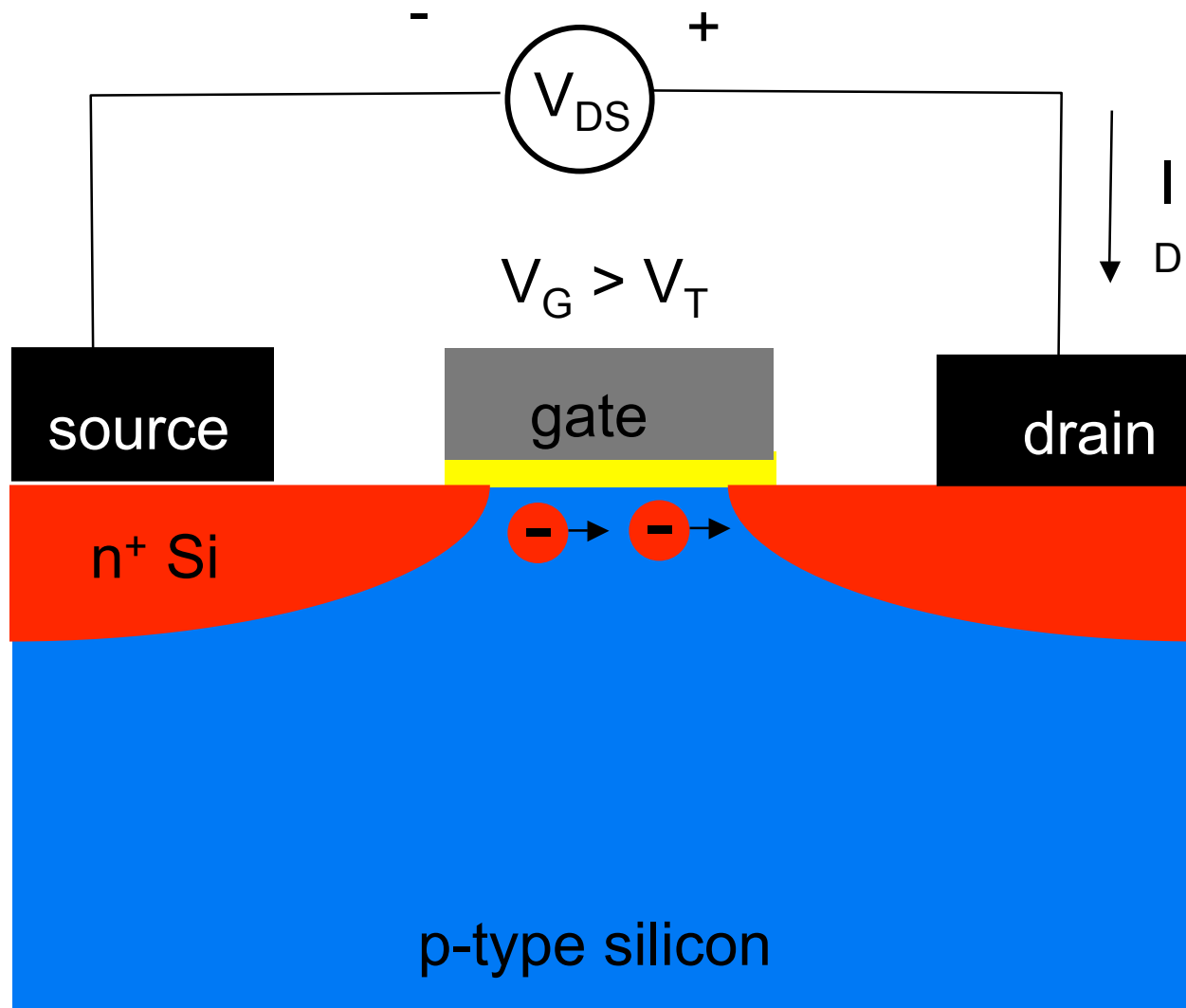
transistor



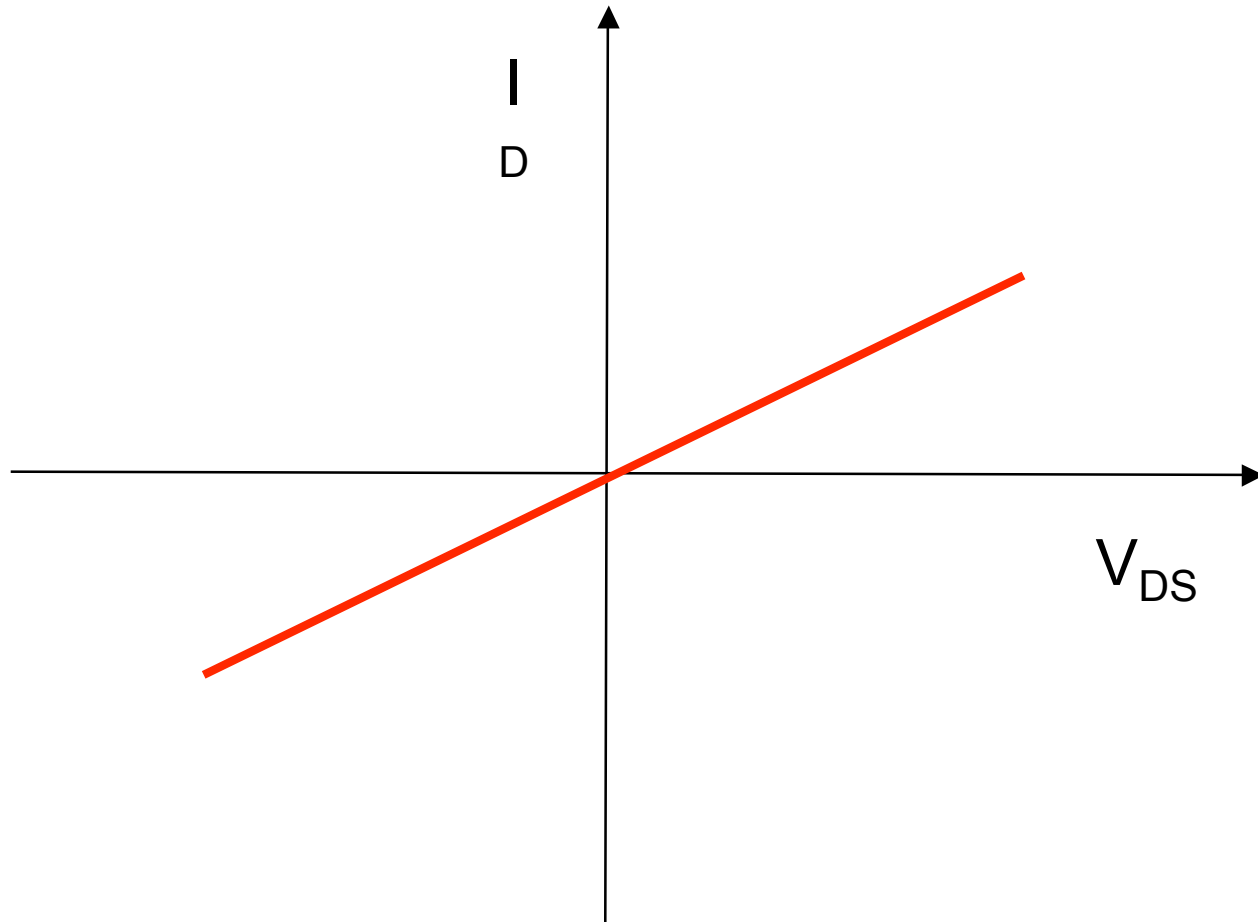
transistor



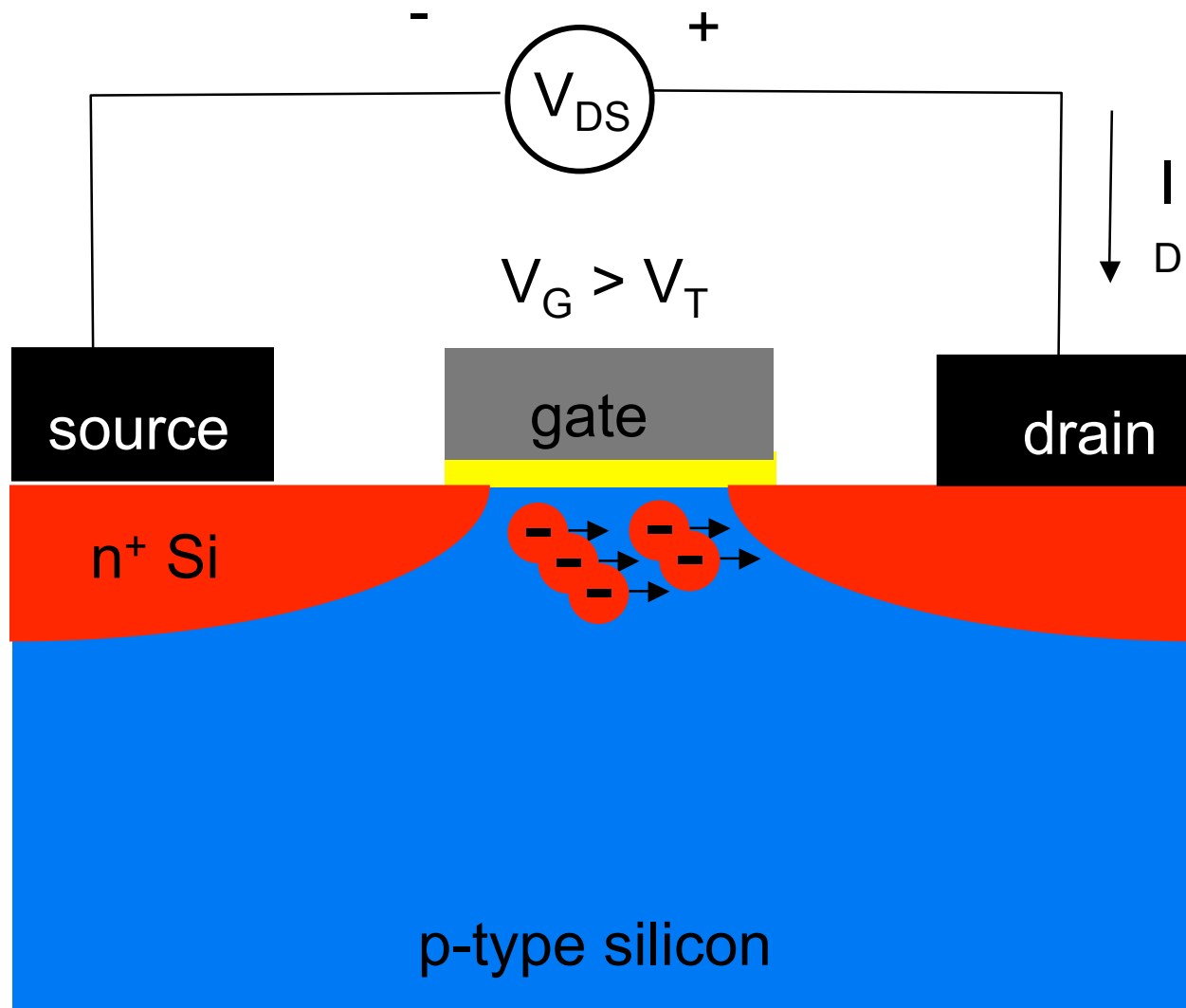
transistor



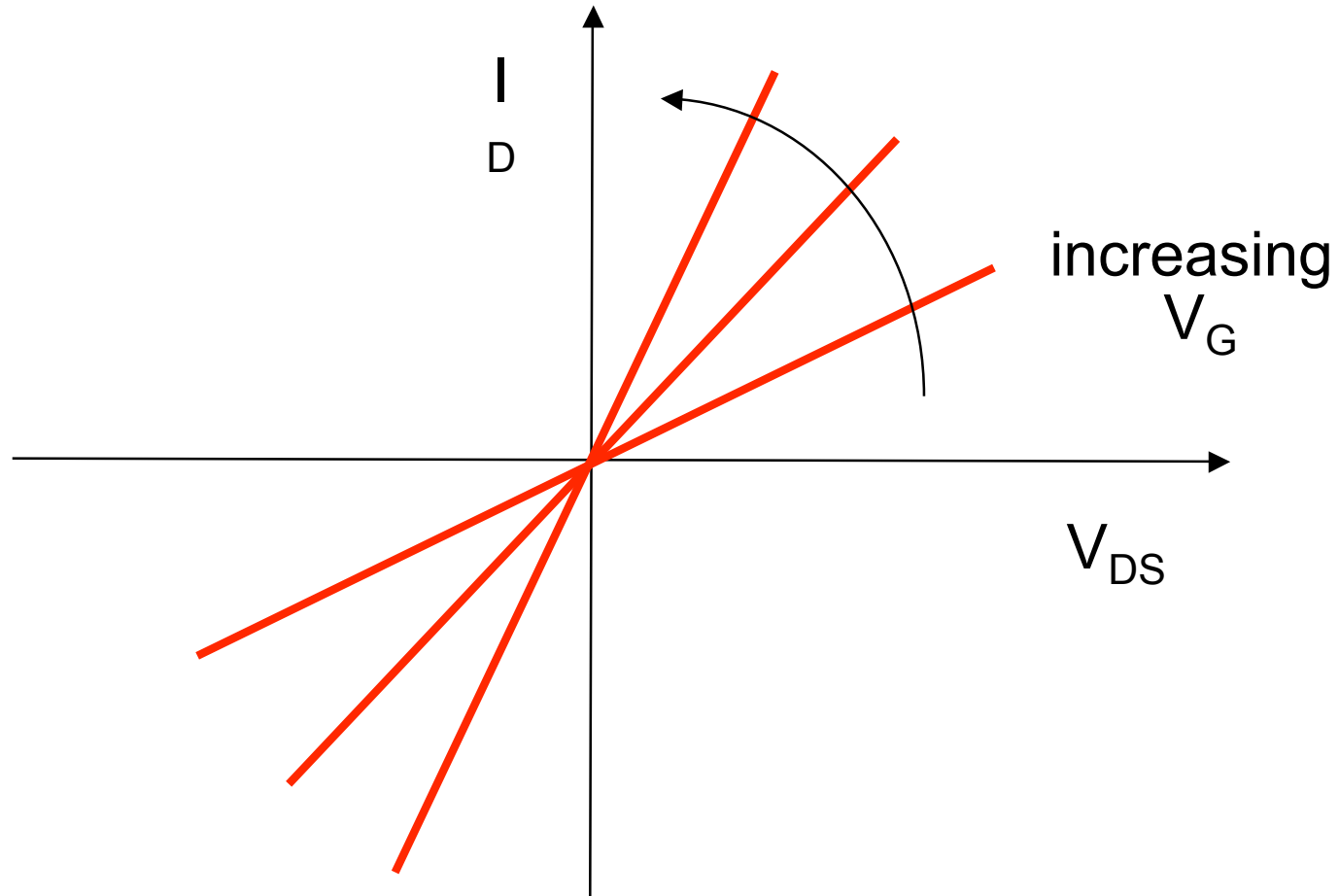
transistor



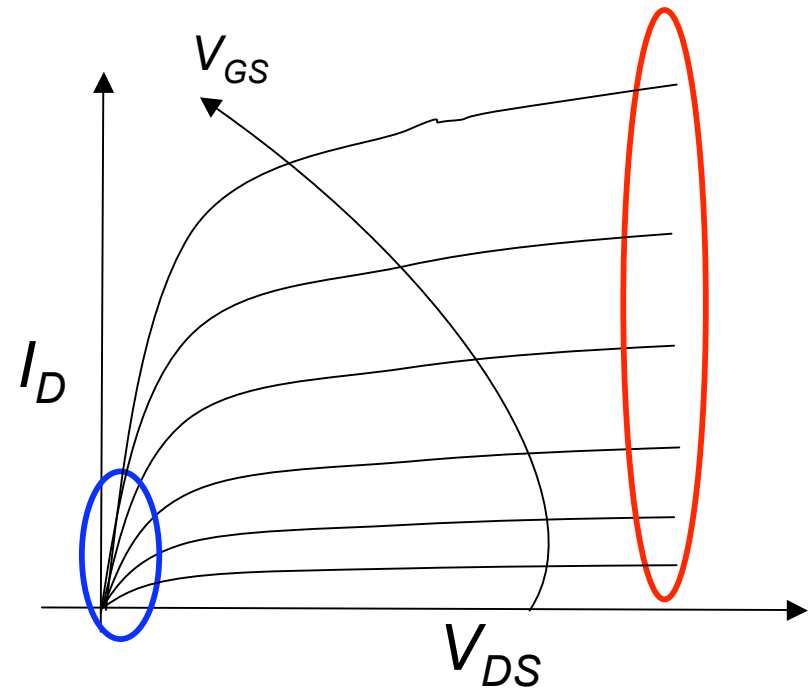
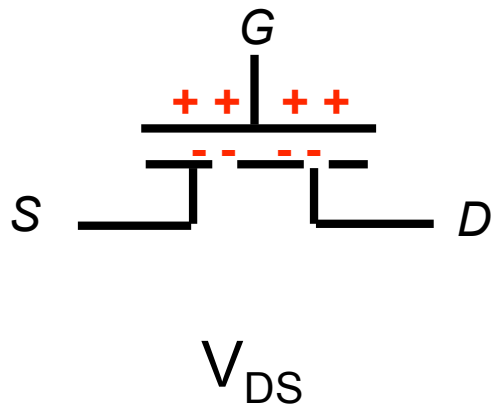
transistor



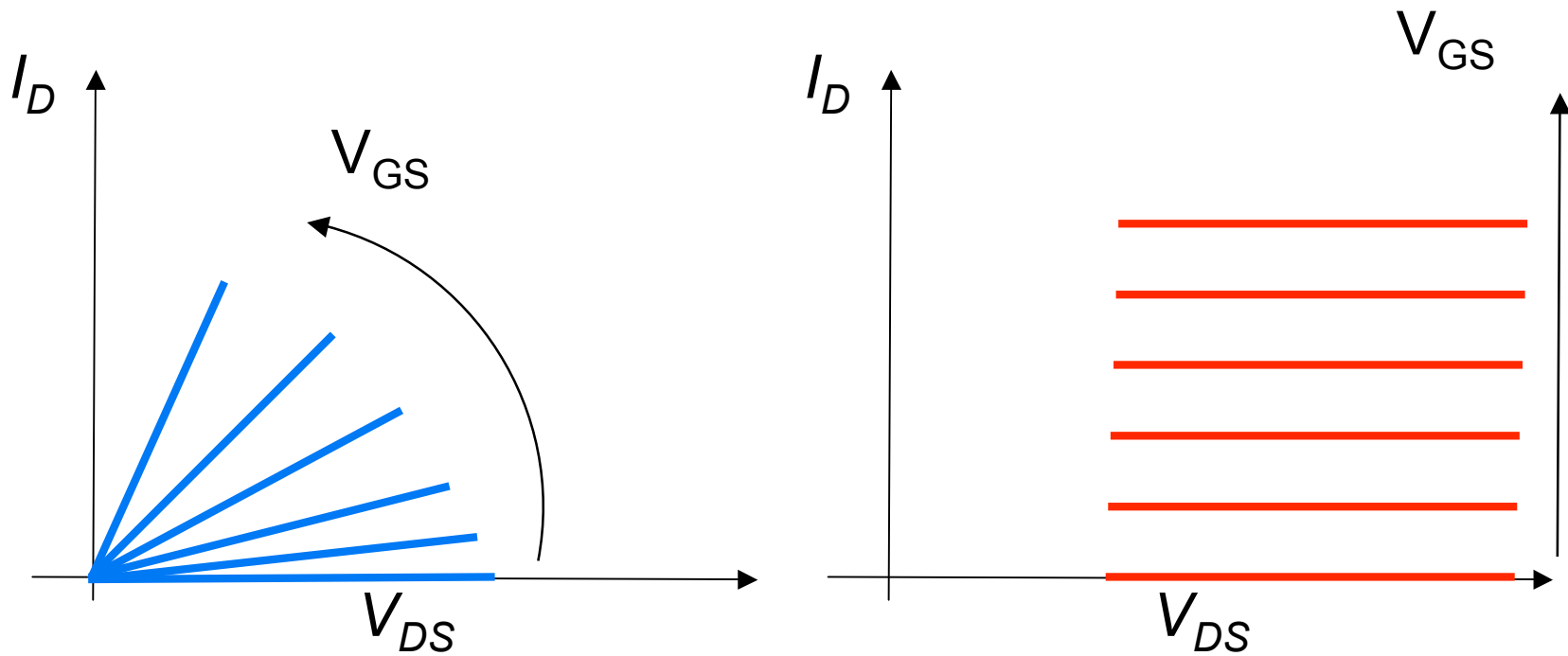
transistor: a voltage controlled resistor?



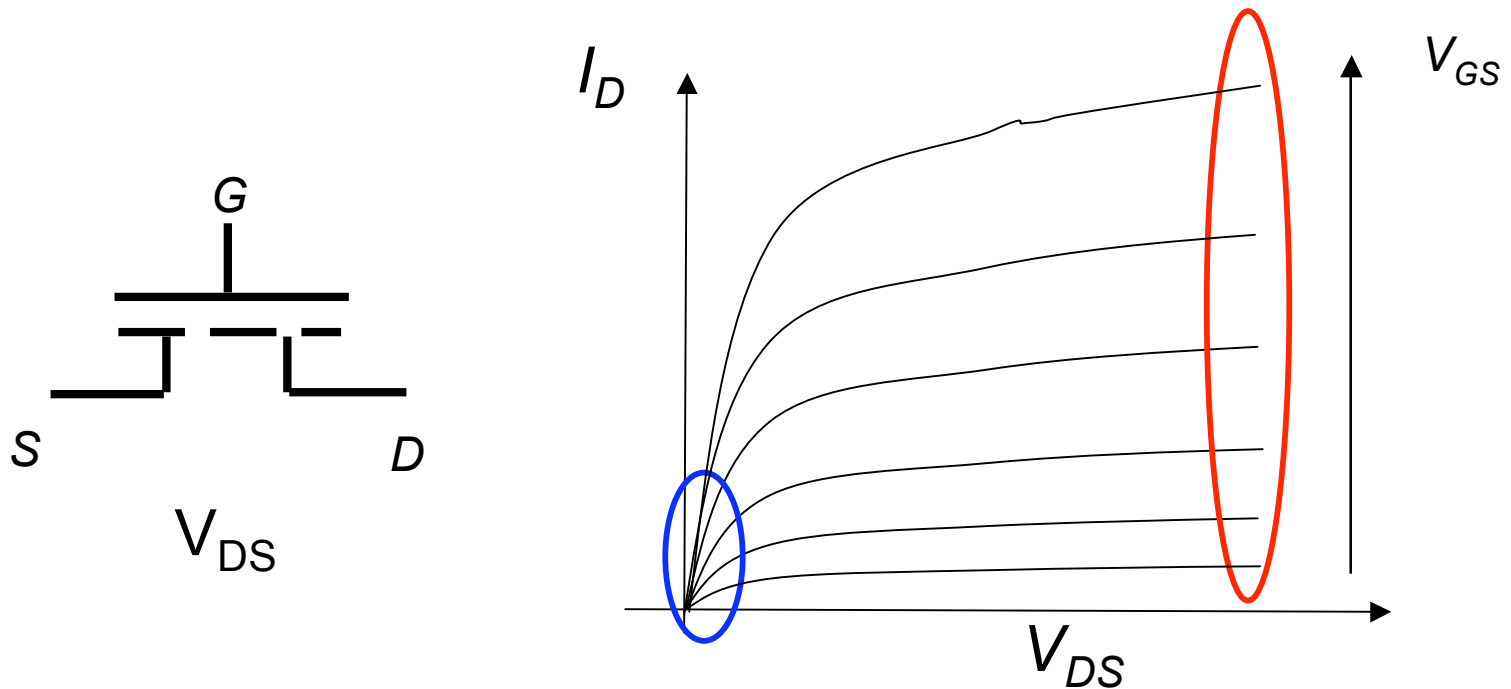
real transistors



real transistors

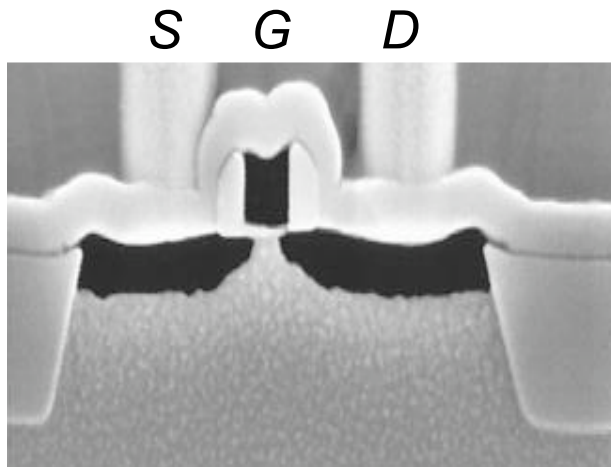


real transistors



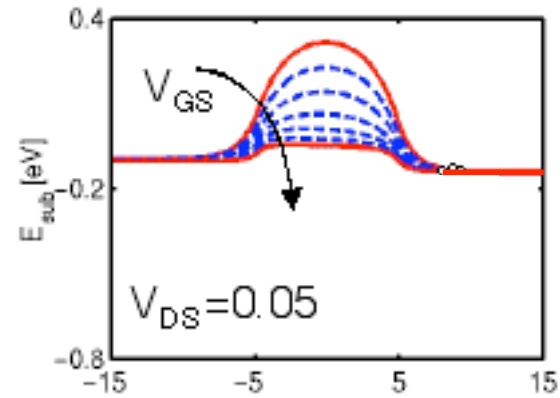
why does the current saturate?

MOSFET energy band diagrams

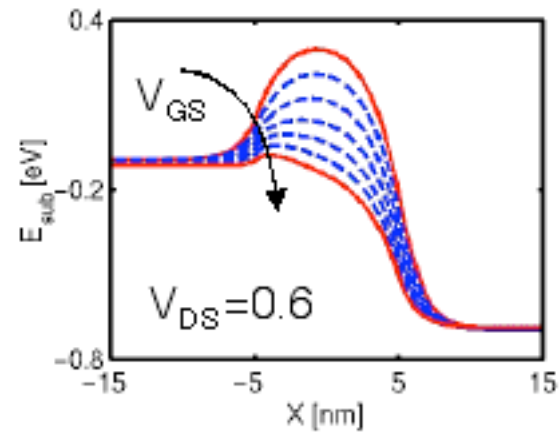


$$E = -q V$$

*electron energy
vs. position*

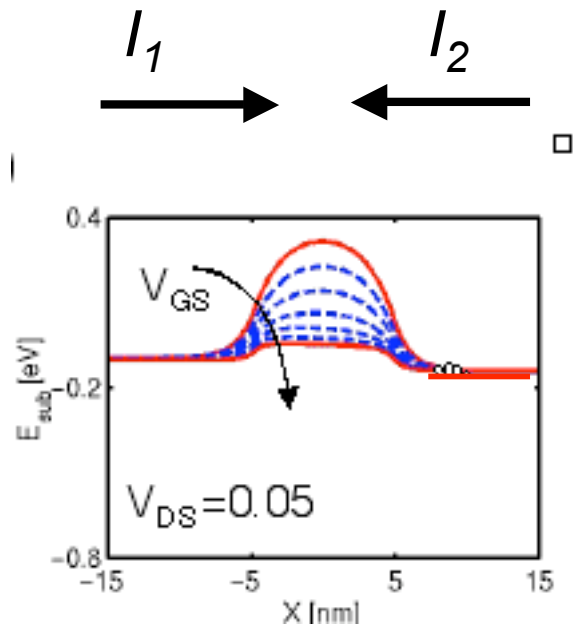


$V_{\text{D}} \approx 0\text{V}$



$V_{\text{D}} = V_{\text{DD}}$

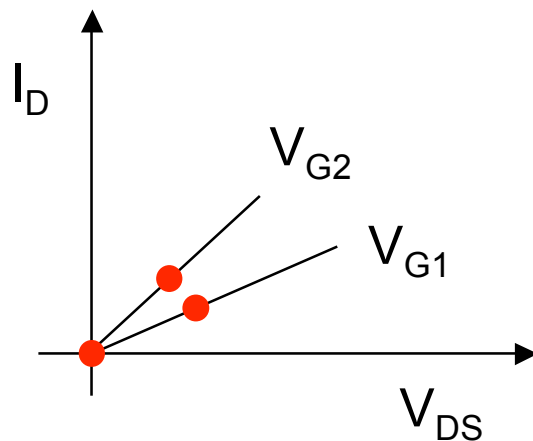
low V_{DS}



$$I_1, I_2 \sim V_G$$

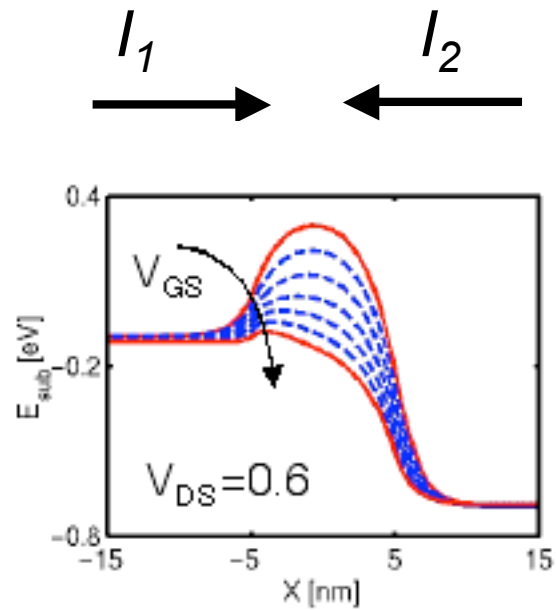
$$I_D = I_1 - I_2$$

$$V_D = 0:$$
$$I_1 = I_2$$
$$I_D = 0$$

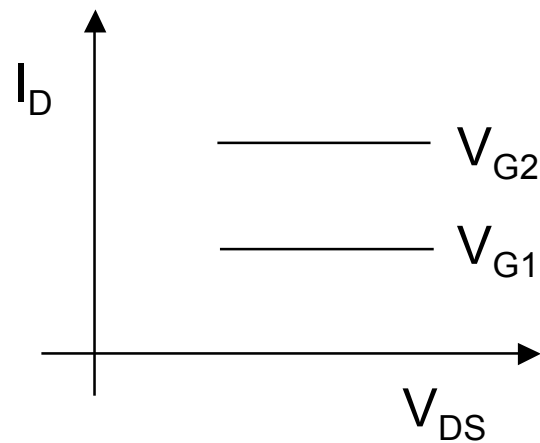


$$V_D > 0:$$
$$I_1 > I_2$$
$$I_D > 0$$

high V_{DS}

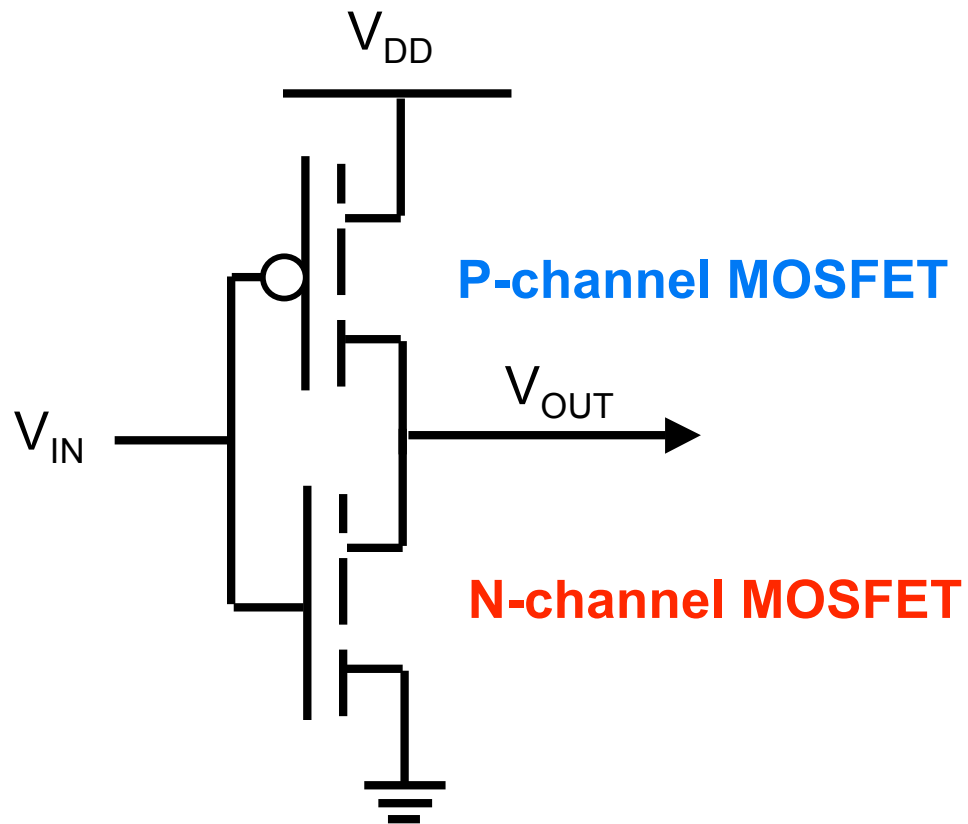


$$I_1 \sim V_G$$
$$I_2 \sim 0$$

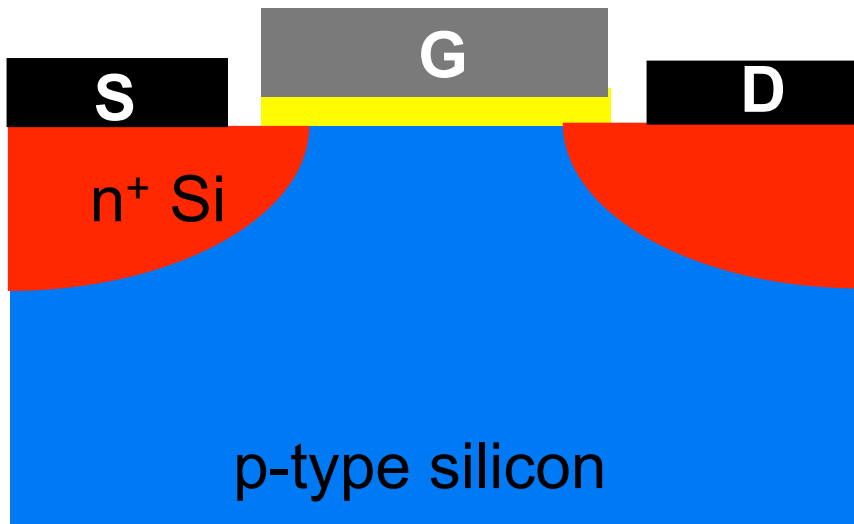


$$I_D = I_1 \sim V_G$$

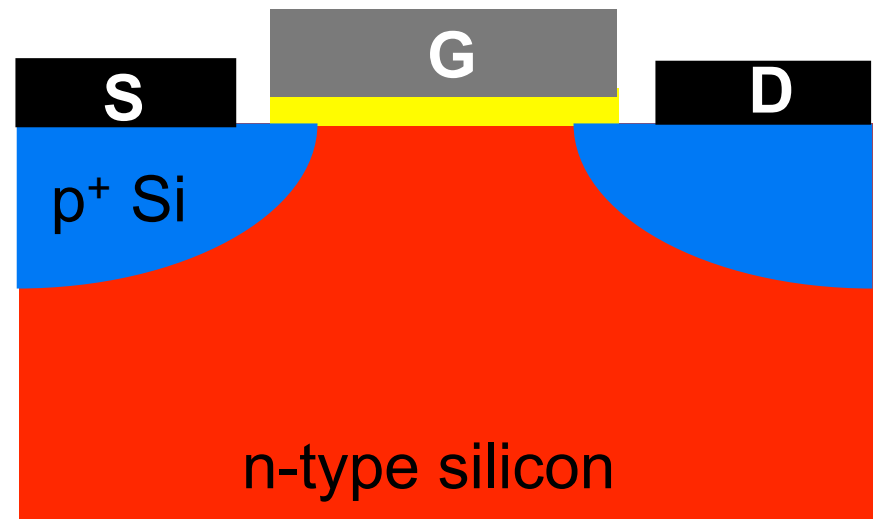
complementary CMOS



CMOS

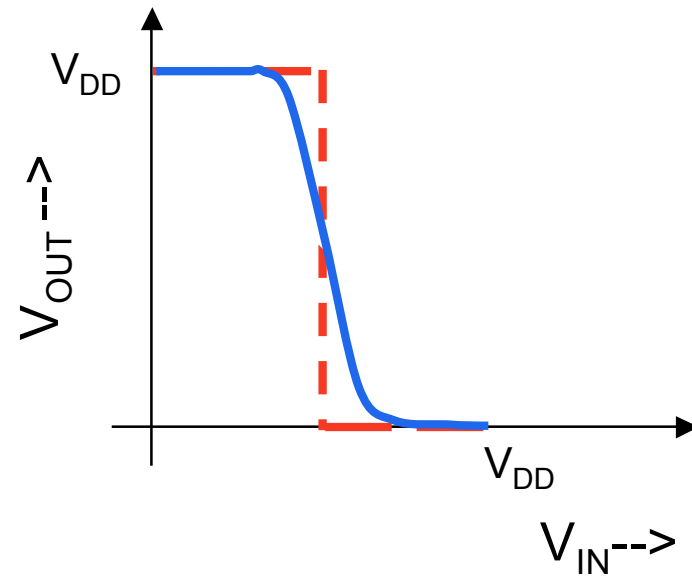
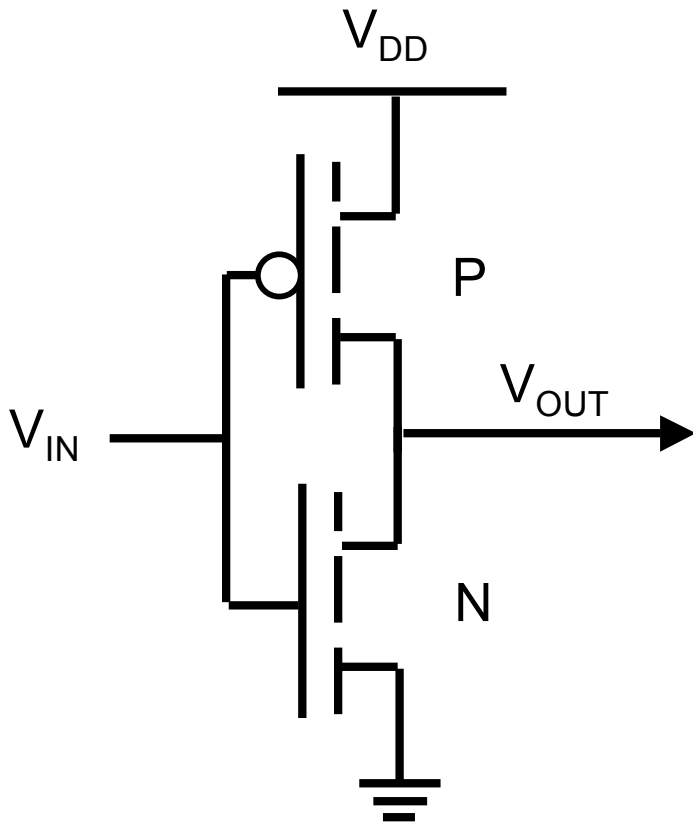


n-MOS: $V_{GS} > 0$



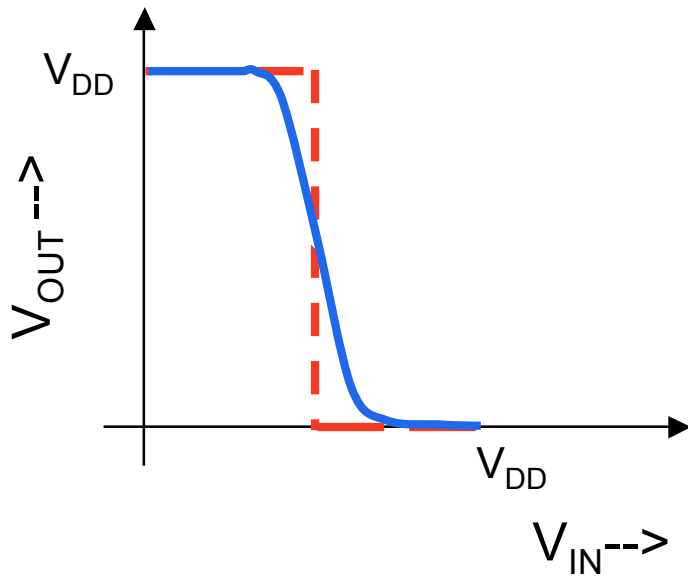
p-MOS: $V_{GS} < 0$

CMOS inverter

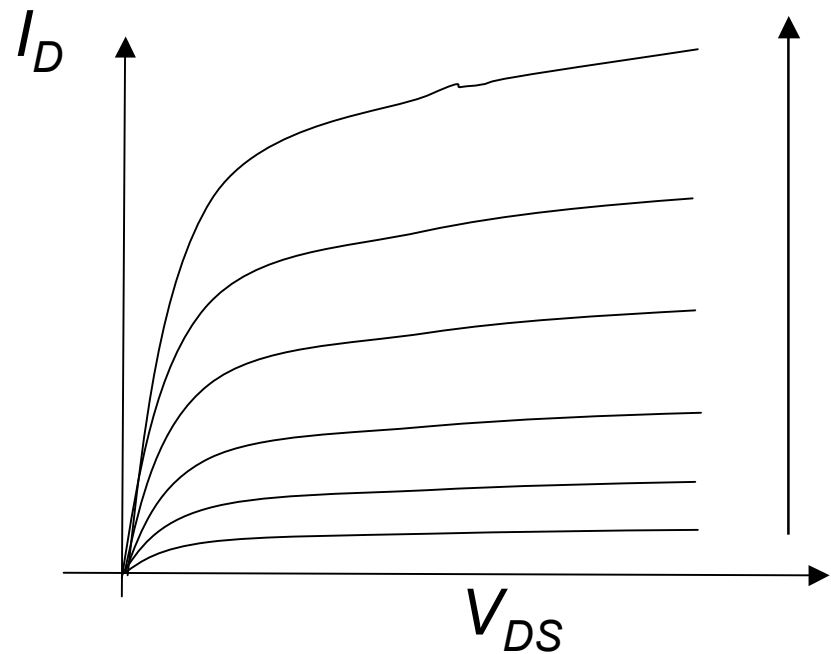


“transfer characteristic”

CMOS inverter



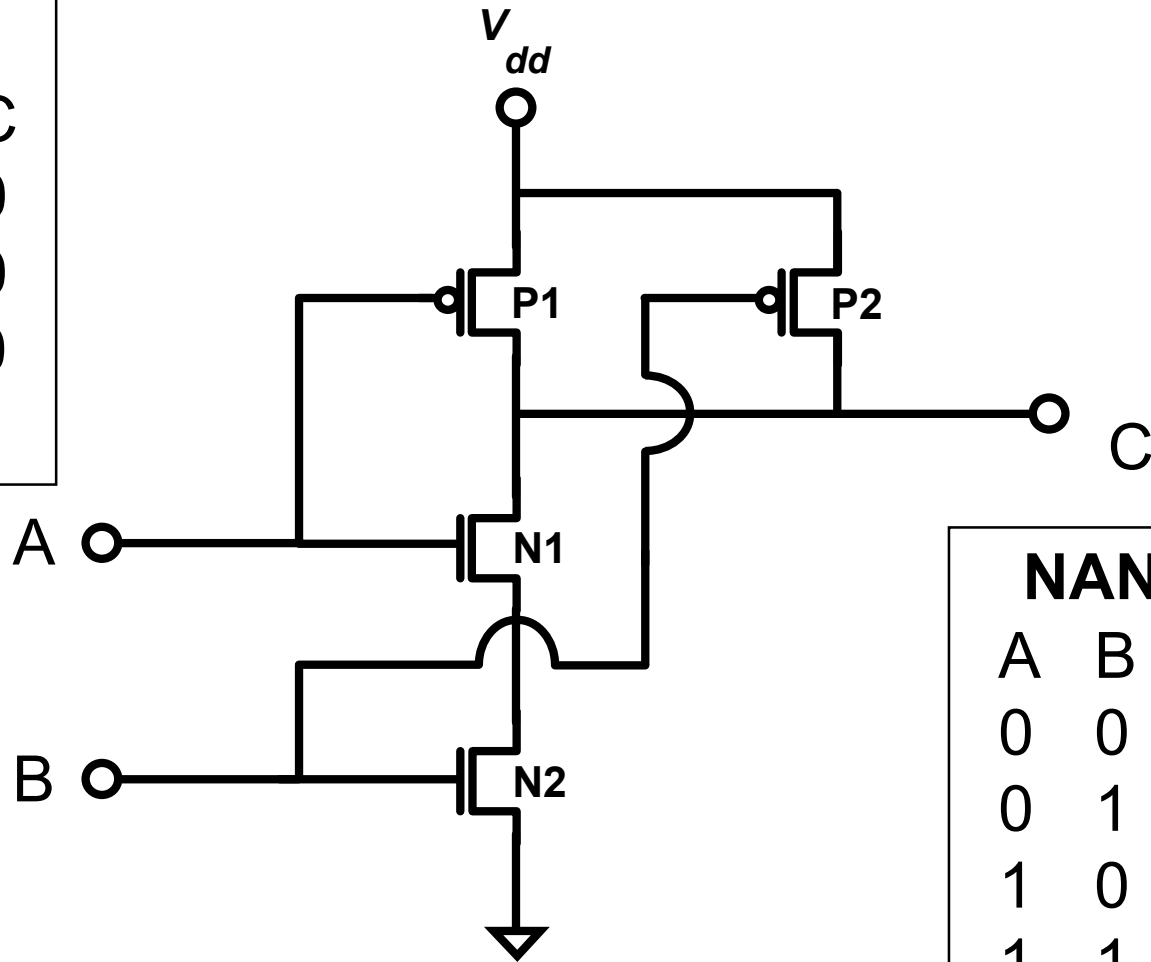
sharp transition
separates zero and one



flatter characteristic
(small dependence of I_D on V_{DS})
gives sharp transition

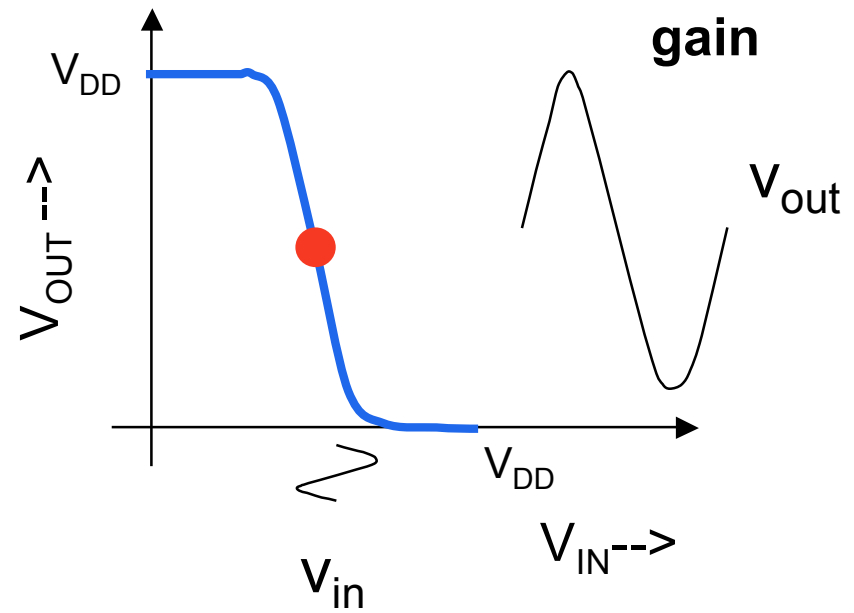
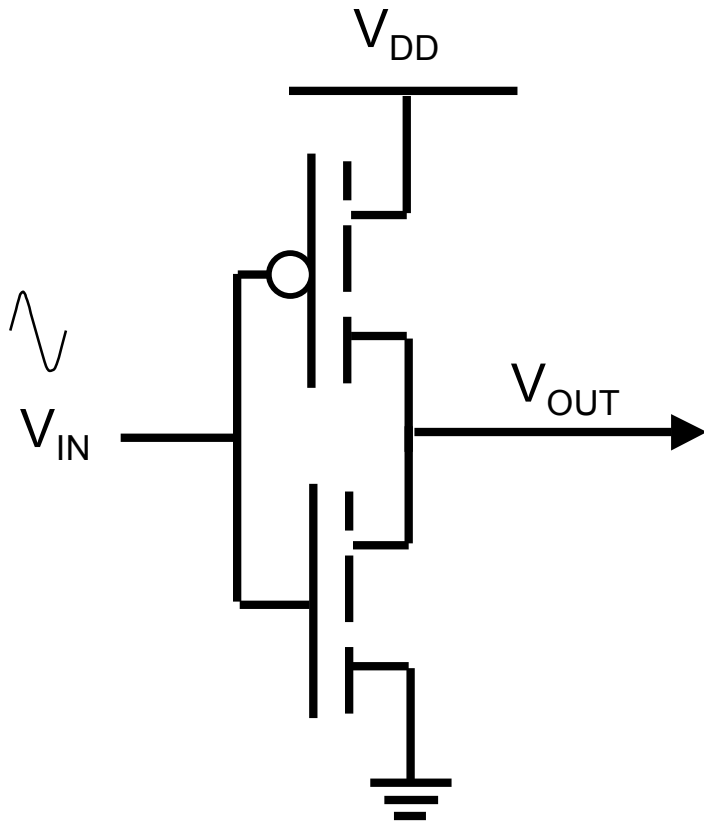
Two input CMOS NAND gate

AND		
A	B	C
0	0	0
0	1	0
1	0	0
1	1	1

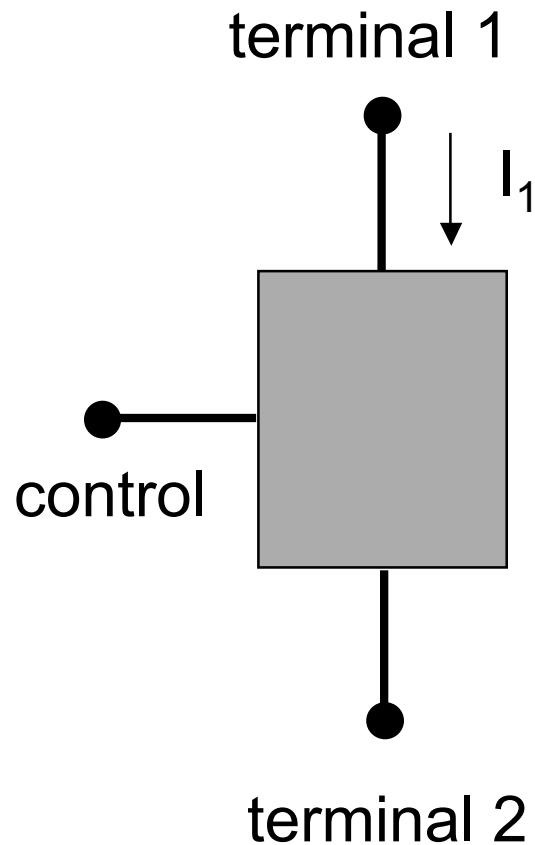


NAND		
A	B	C
0	0	1
0	1	1
1	0	1
1	1	0

CMOS Amplifier



transistors



point contact transistor

bipolar transistor

MOSFET

JFET

SOI MOSFET

FinFET

MODFET (HEMT)

heterojunction bipolar transistor

velocity modulation transistor