Solid State Devices



Section 29 MOS Capacitor Signal Response

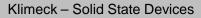
29.2 Small Signal Response

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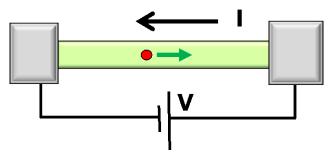
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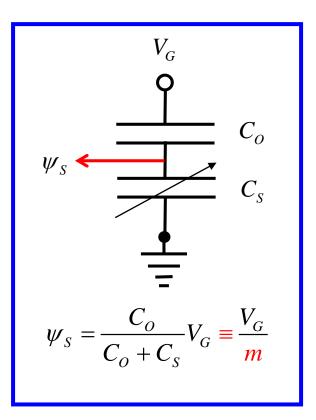
Section 29 MOS Capacitor Signal Response



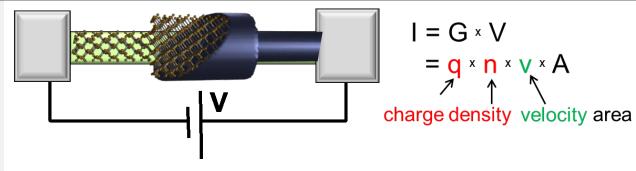
 $I = G \times V$ = q × n × v × A \checkmark \uparrow \checkmark charge density velocity area

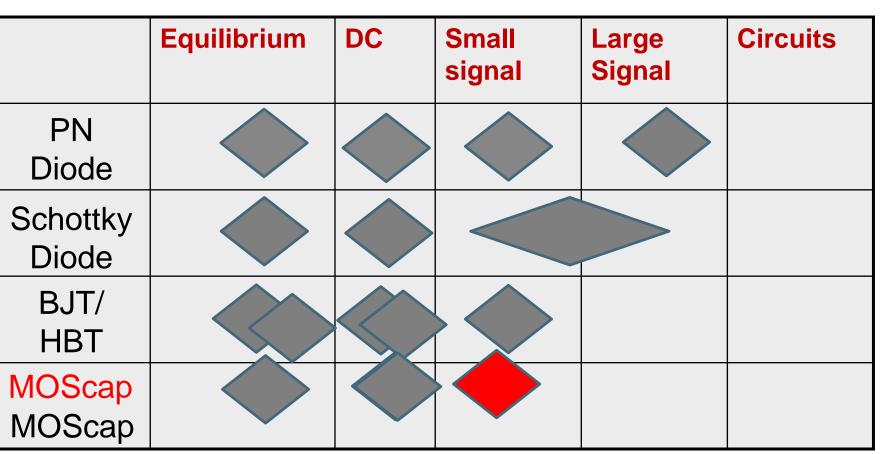
status

- 29.1 Introduction / Background
- 🕨 29.2 Small Signal Response 🛛 🐗
- 29.3 Large Signal Response



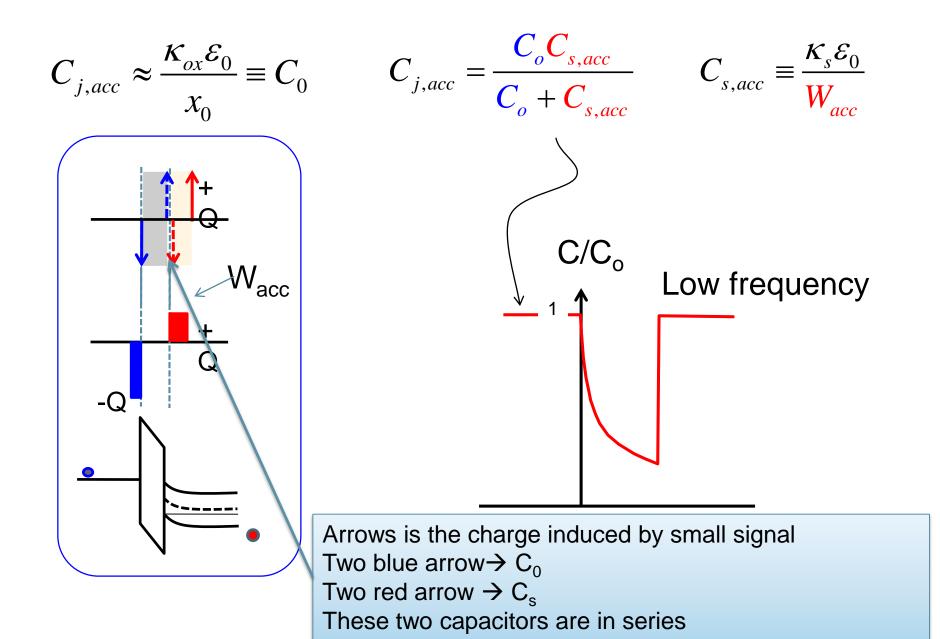
Section 27 Heterojunction Bipolar Transistor





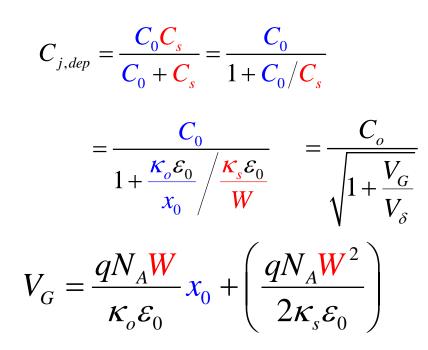
Junction Capacitance in accumulation





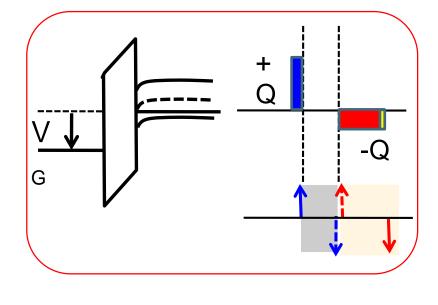
Junction Capacitance in depletion

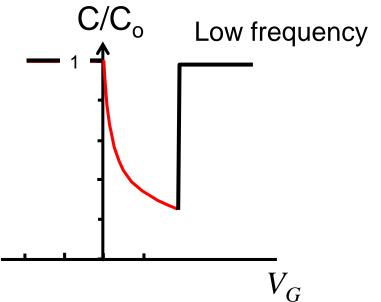




First term is the V drops on oxide Second term is band bending Solve for W V_{δ} is some constant

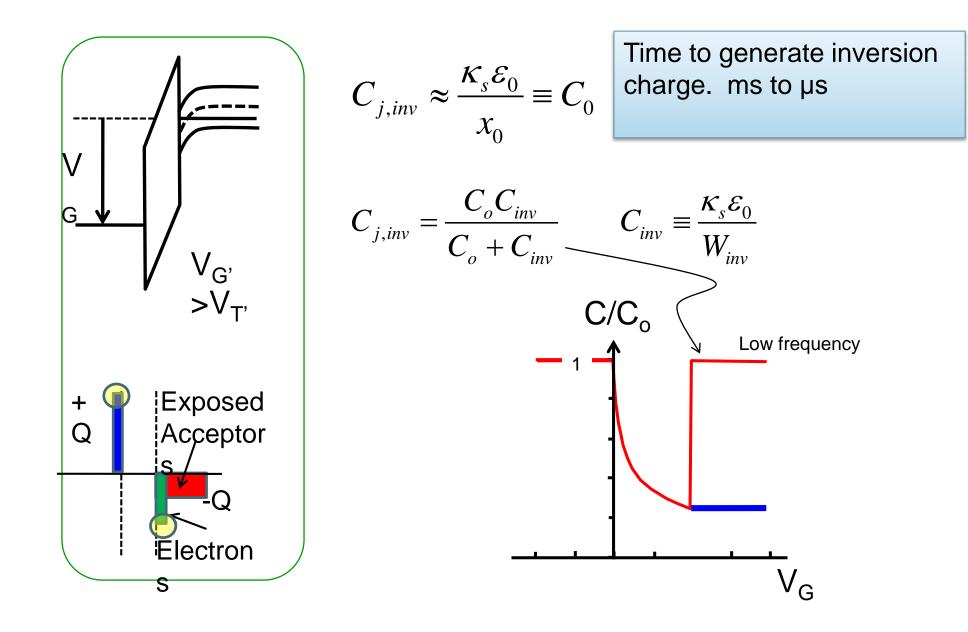
$$\frac{\kappa_o}{\kappa_s} \frac{W}{x_0} = \sqrt{1 + \frac{V_G}{V_\delta}} - 1$$





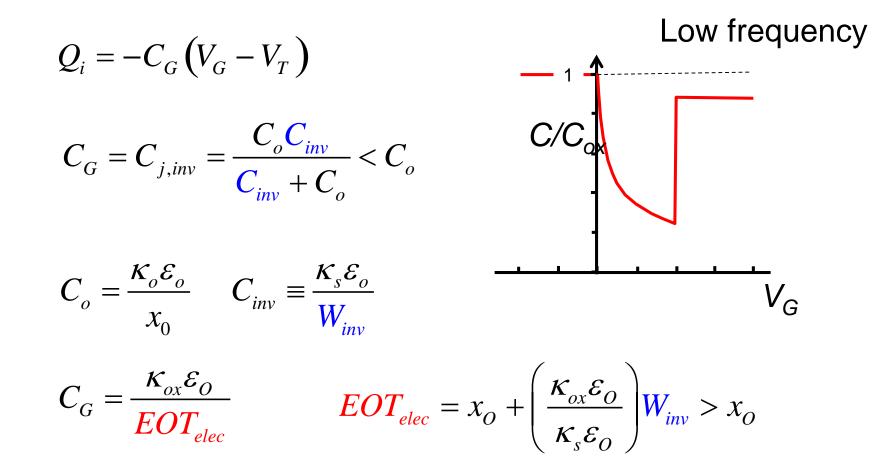
Junction capacitance in inversion





Equivalent Oxide Thickness

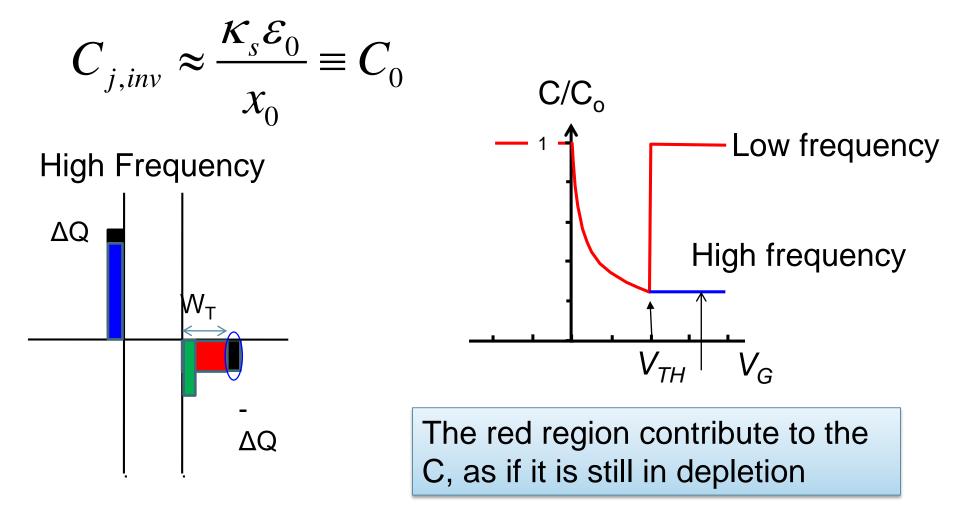




'Equivalent oxide thickness - electrical '

High frequency curve at inversion

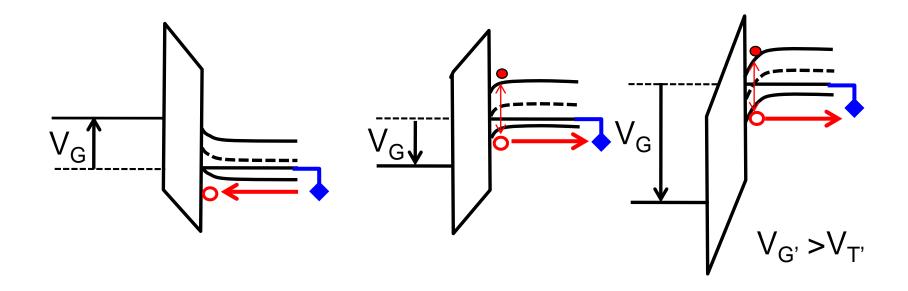




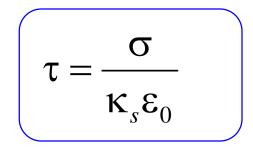
What about high frequency part of the curve?

Response Time





Dielectric Relaxation



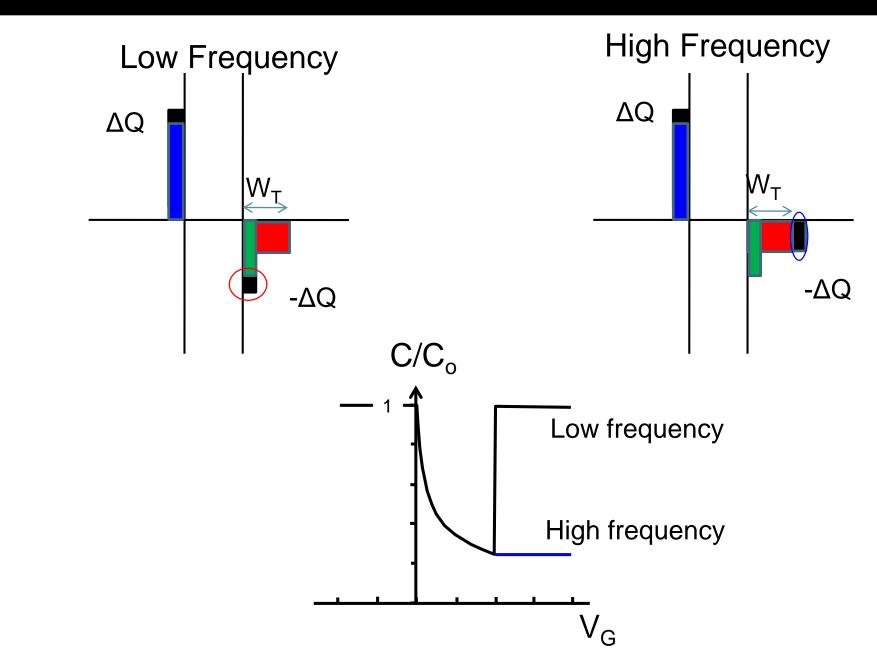
SRH Recombination-Generation

$$R = \frac{np - n_i^2}{\tau_n(p + p_1) + \tau_p(n + n_1)} \rightarrow \frac{-n_i}{\tau_n + \tau_p}$$

Ref. Lecture no. 15

High frequency response in MOS-C

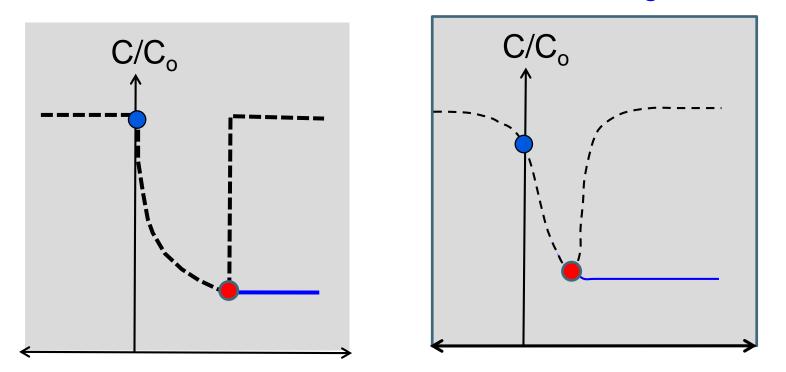




Ideal vs. Real C-V Characteristics



Blue dot: Flat band voltage ...



Red dot: Threshold voltage ...

