**Solid State Devices** 



## Section 27 Heterojunction Bipolar Transistor

## 27.2 Heterojunction Equilibrium Solution

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N-Al<sub>0.3</sub>Ga<sub>0.7</sub>As: p-GaAs (Type-I Heterojunction)









## **Built-in Potential: Boundary Condition @Infinity**









Interface Boundary Conditions





## **Analytical Solution for Heterojunctions**





Base Emitter Depletion Region





$$N_{E}x_{n,BE} = N_{B}x_{p,BE}$$

$$x_{n} = \sqrt{\frac{2\varepsilon_{0}}{q}} \frac{\kappa_{s,E}\kappa_{s,B}N_{B}}{N_{E}(\kappa_{s,E}N_{B} + \kappa_{s,B}N_{E})}V_{bi}$$

$$V_{bi} = \frac{qN_{E}x_{n,BE}^{2}}{2\kappa_{s,E}\varepsilon_{0}} + \frac{qN_{B}x_{p,BE}^{2}}{2\kappa_{s,B}\varepsilon_{0}}$$

$$x_{p} = \sqrt{\frac{2\varepsilon_{0}}{q}} \frac{\kappa_{s,E}\kappa_{s,B}N_{E}}{N_{B}(\kappa_{s,E}N_{B} + \kappa_{s,B}N_{E})}V_{bi}}$$









Herbert Kroemer, "Heterostructure bipolar transistors and integrated circuits," Proc. IEEE , 70, pp. 13-25, 1982.

