Solid State Devices



Section 27 Heterojunction Bipolar Transistor

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Section 27 Heterojunction Bipolar Transistor



 $I = G \times V$ = q × n × v × A \checkmark \uparrow \checkmark charge density velocity area

- 27.1 Applications, Concept, Innovation, Nobel Prize
- 27.2 Heterojunction Equilibrium Solution
- 27.3 Types of heterojunctions
- 27.4 Abrupt junction HBTs
- 27.5 Graded junction HBTs
- 27.6 Graded base HBTs
- 27.7 Double heterojunction HBTs
- 27.8 Modern Designs

 $\beta_{poly, ballistic} \rightarrow \frac{n_{i,B}^2}{n_{i,E}^2} \times \frac{N_E}{N_B} \times \frac{\upsilon_{th}}{\upsilon_s}$ $\log_{10}|eta|$ $f_{\rm max} = \sqrt{\frac{f_T}{8\pi R_{\odot}C_{\odot}}}$ $|\beta(\omega)| \approx \beta_{DC} \frac{\omega_{\beta}}{\omega}$ $\log_{10} f$

Mark Lundstrom, "Heterostructure Fundamentals," Purdue University, 1995. Herbert Kroemer, "Heterostructure bipolar transistors and integrated circuits," Proc. *IEEE*, **70**, pp. 13-25, 1982. **High Frequency Applications**





-military radar and communications

3) Monolithic, millimeter-wave IC's (MMIC's)

-front ends for receivers and transmitters



future need for transistors with 1 THz power-gain cutoff freq.





How to make a better Transistor?







Gummel Plot





Emitter bandgap > Base Bandgap

Some History



Schokley realized that HBT is possible,

Herbert Kroemer provided the foundation of the field and worked out the details Proc. IEEE, 70, pp 13-25, 1982.



Kroemer Nobel Prize (2000)









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A heterojunction bipolar transistor





Today: Think of all the cell phones and wifi routers... Communication networks... Satellite communications





Heterojunction Bipolar Transistors



i) Wide gap Emitter HBT



ii) **Double** Heterojunction Bipolar Transistor



Larger bandgap material in collector => do not loose control under high current

 f_{β}

 $\log_{10}|eta|$

 $\frac{n_{i,B}^{2}}{n_{i,E}^{2}} = \frac{N_{C,B}N_{V,B}e^{-E_{g,B}\beta}}{N_{C,E}N_{V,E}e^{-E_{g,E}\beta}} \approx e^{(E_{g,E}-E_{g,B})\beta}$





 $\log_{10} f$

Mesa HBTs





 $\frac{n_{i,B}^2}{n_{i,E}^2} = \frac{N_{C,B}N_{V,B}e^{-E_{g,B}\beta}}{N_{C,E}N_{V,E}e^{-E_{g,E}\beta}} \approx e^{\left(E_{g,E}-E_{g,B}\right)\beta}$ $\log_{10}|\beta|$ $\frac{1}{f_{\beta}}$ $\log_{10} f_{T}$

Intentional traps, Fermi level pinned Low conductance Low capacitance High speed





Bandgaps and Lattice Matching





 E_V

Fig. 7.6. Bandgap energy and lattice constant of various III-V semiconductors at room temperature (adopted from Tien, 1988).

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