

Section 21 PN Diode AC Response

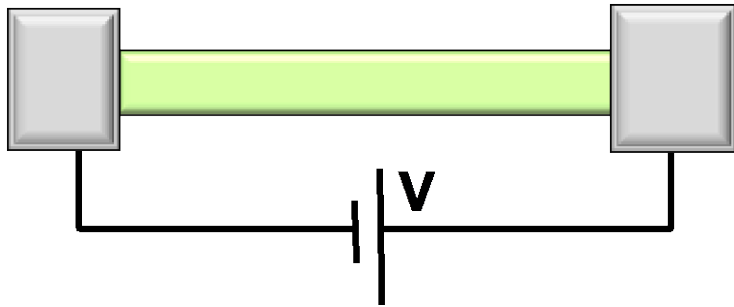
21.2 Majority carrier junction capacitance

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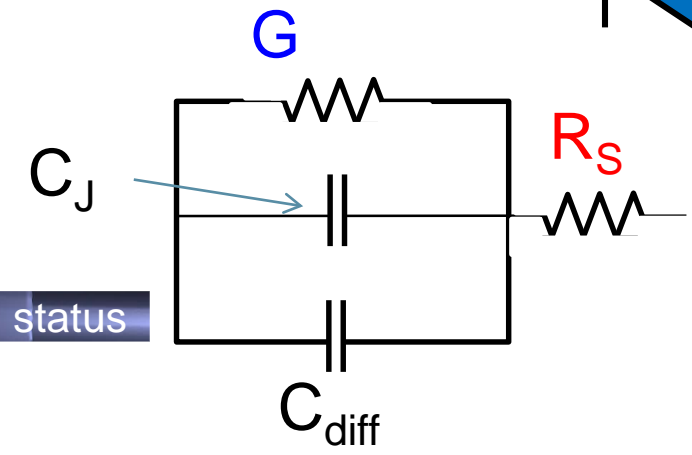
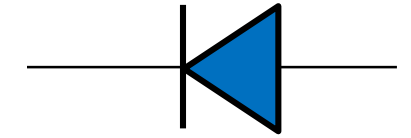
Section 21 PN Diode AC Response



$$I = G \times V$$

$$= q \times n \times v \times A$$

↑ charge density ↑ velocity ↑ area

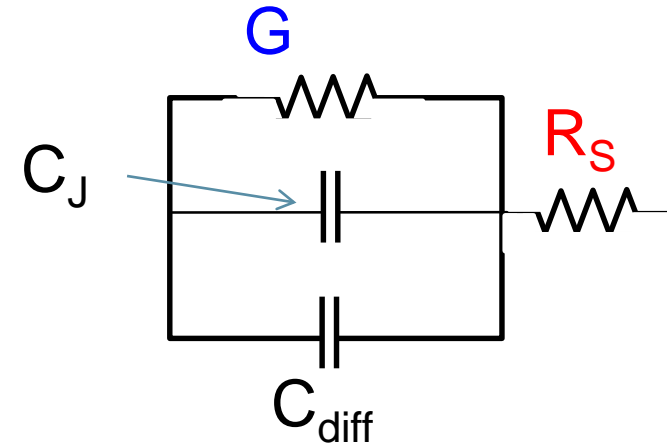
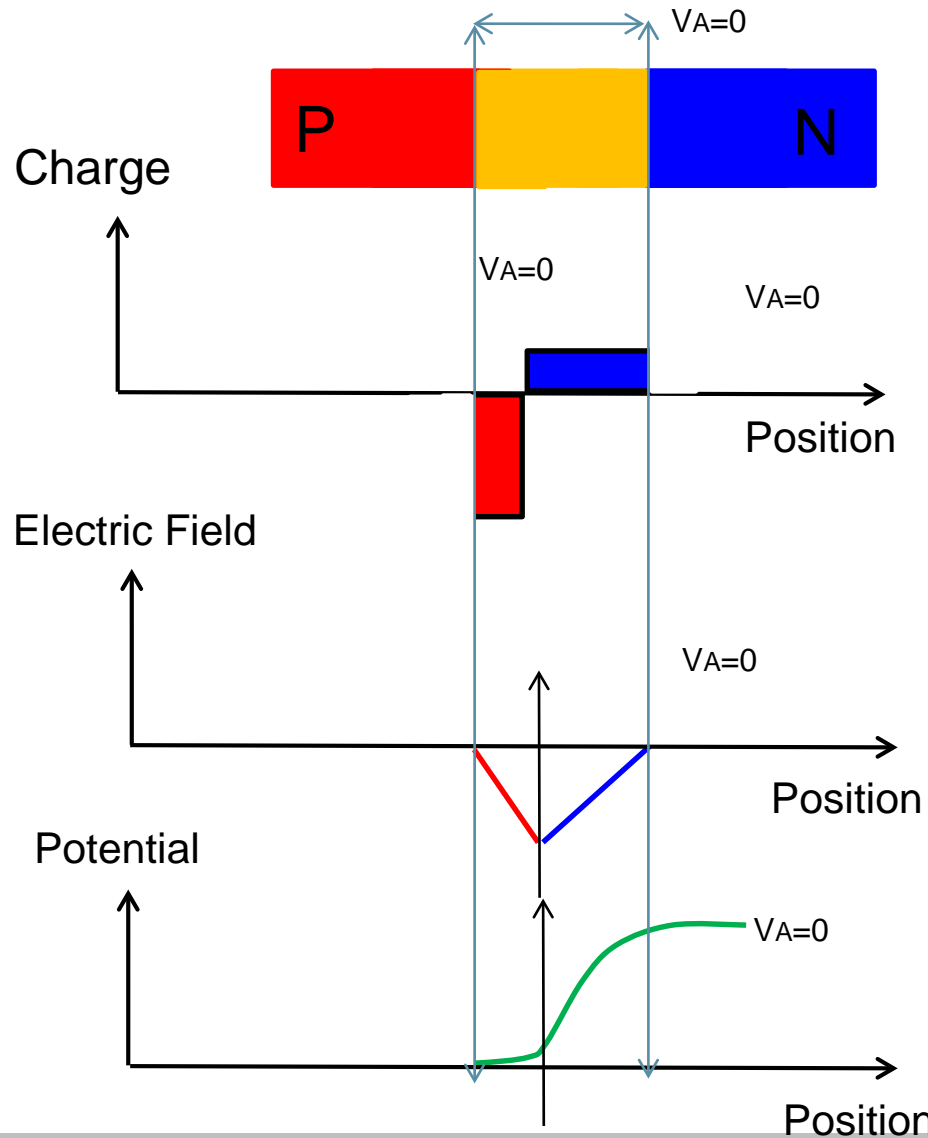


- > • 21.1 Conductance and series resistance
- > • 21.2 Majority carrier junction capacitance
- > • 21.3 Minority carrier diffusion capacitance

$$\frac{1}{g_{FB}} = R_S + \frac{m}{q\beta(I + I_0)}$$

$$\frac{1}{g_{RB}} = \frac{qn_i B_0}{2\tau \sqrt{V_{bi} - V_A}}$$

Fields and Depletion as a Function of Bias

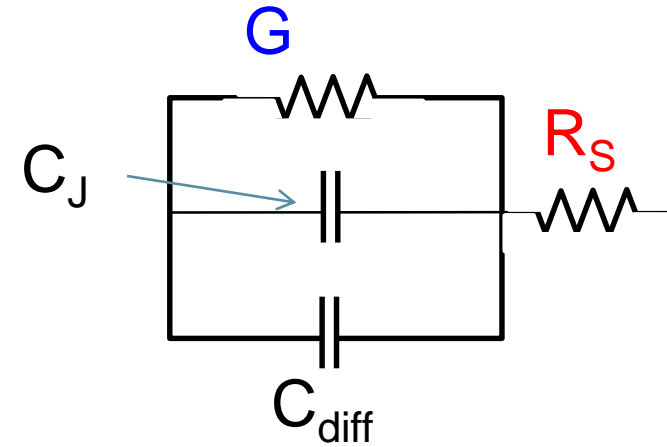
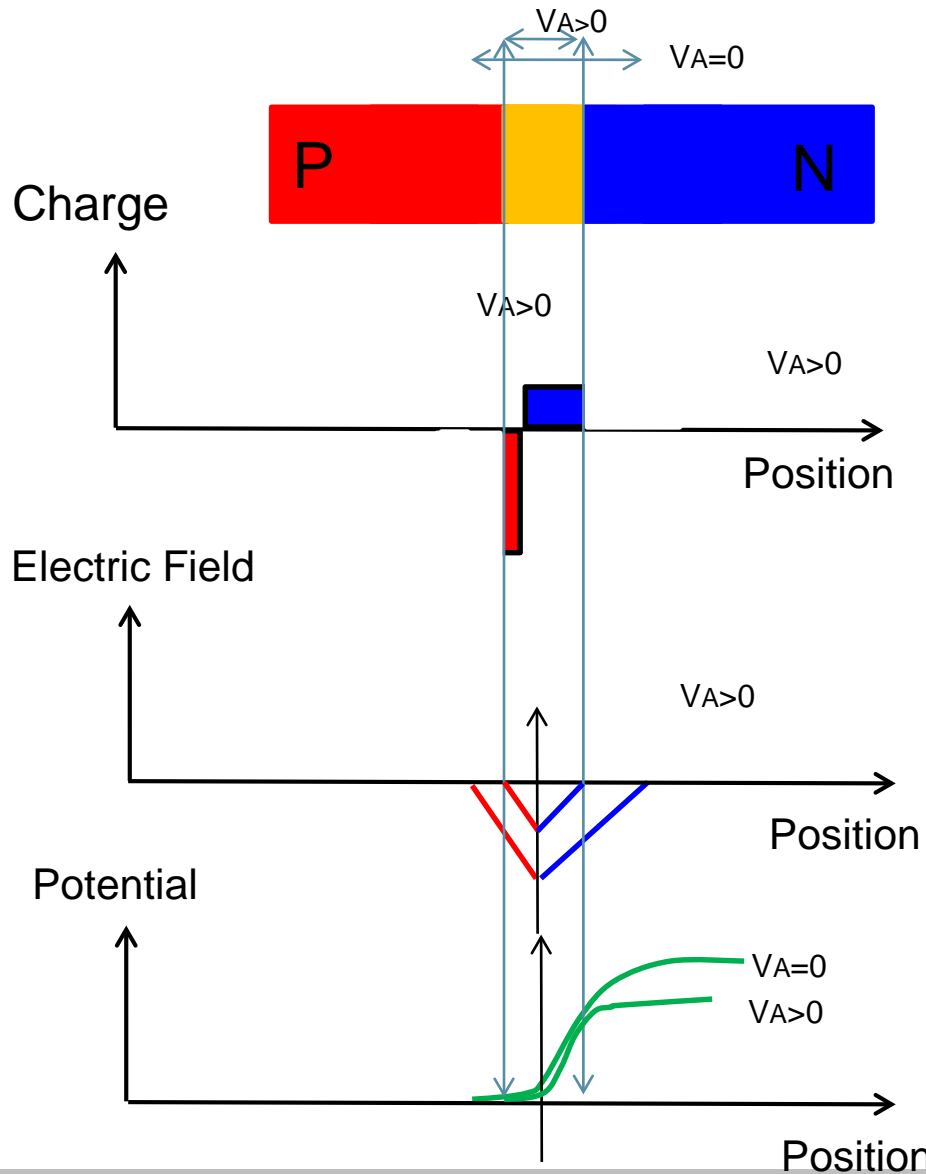


$$x_n = \sqrt{\frac{2k_s\epsilon_0}{q} \frac{N_A}{N_D(N_A + N_D)} (V_{bi} - V)}$$

Zero Bias

$$x_p = \sqrt{\frac{2k_s\epsilon_0}{q} \frac{N_D}{N_A(N_A + N_D)} (V_{bi} - V)}$$

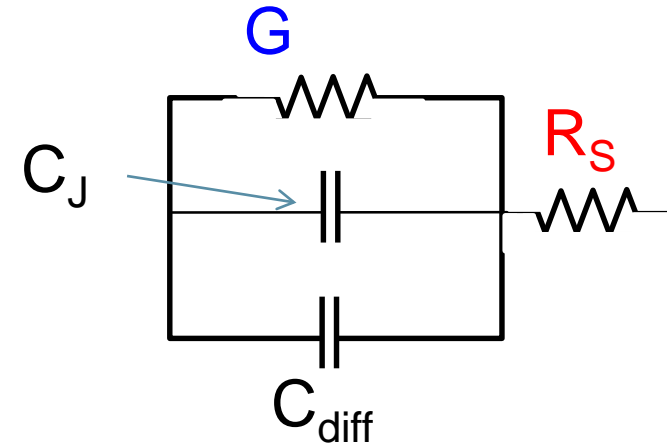
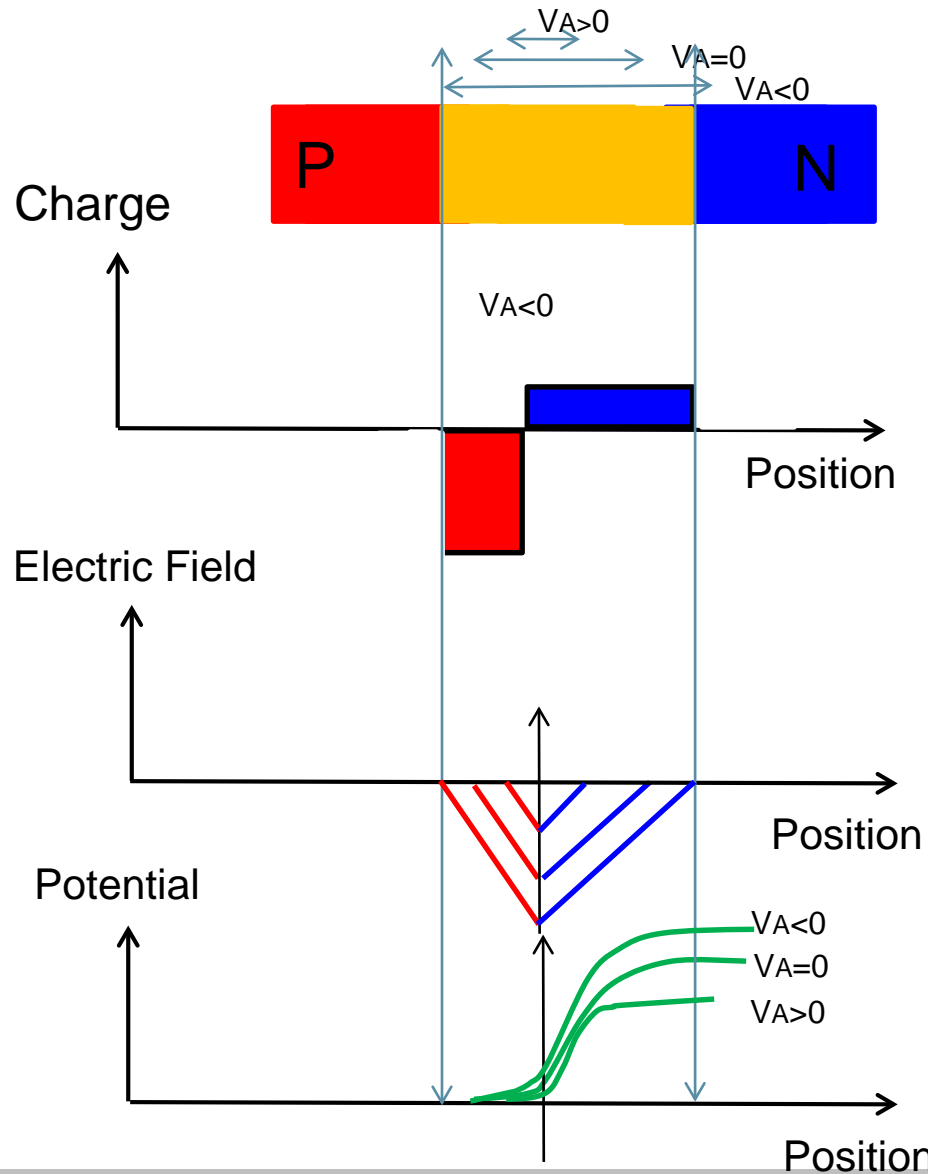
Fields and Depletion as a Function of Bias



$$x_n = \sqrt{\frac{2k_s \epsilon_0}{q} \frac{N_A}{N_D(N_A + N_D)} (V_{bi} - V)}$$

$$DC + AC \quad x_p = \sqrt{\frac{2k_s \epsilon_0}{q} \frac{N_D}{N_A(N_A + N_D)} (V_{bi} - V)}$$

Fields and Depletion as a Function of Bias

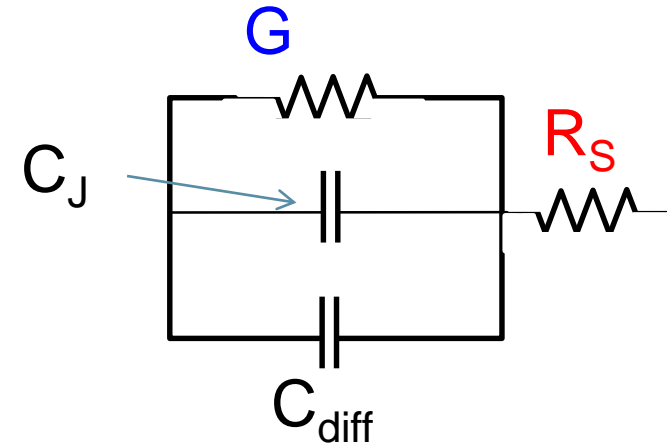
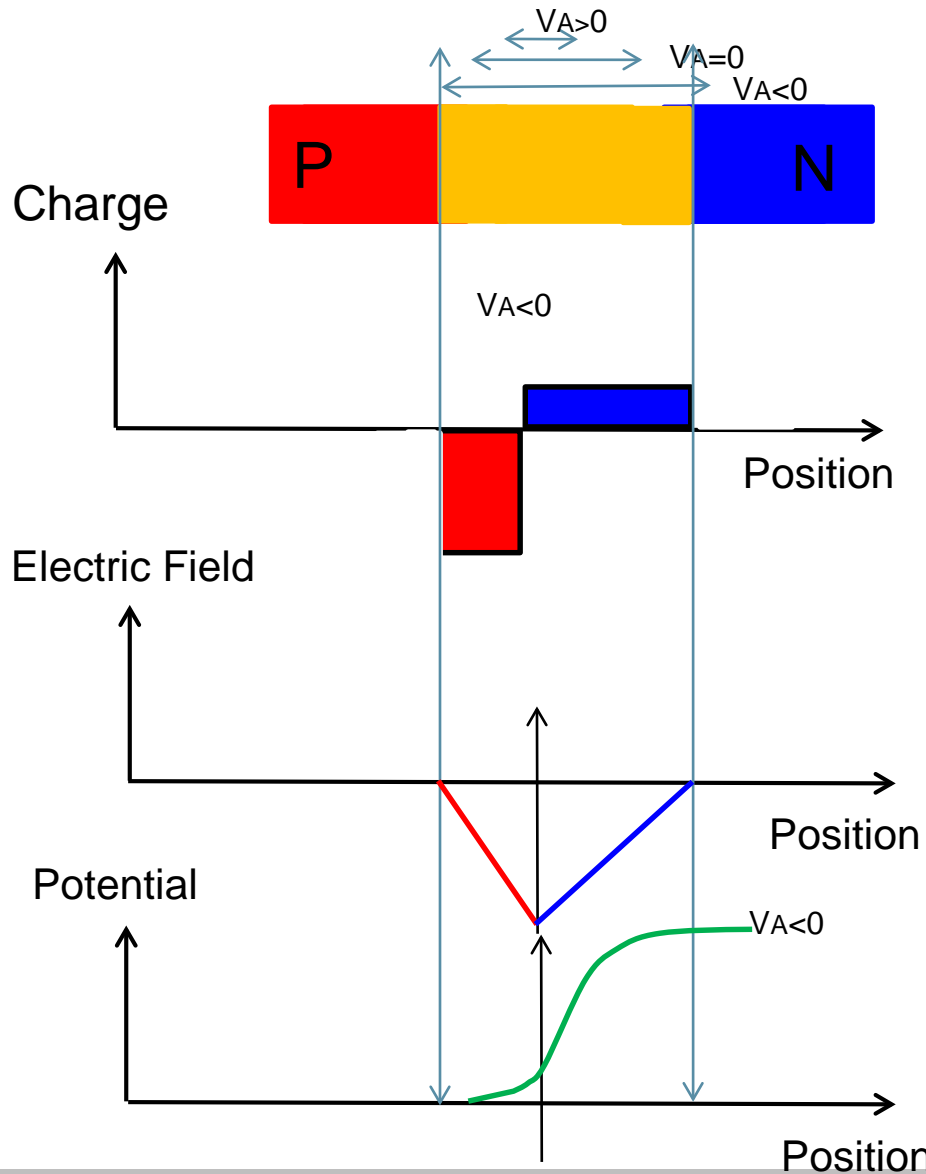


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DC - AC

$$x_p = \sqrt{\frac{2k_s \epsilon_0}{q} \frac{N_D}{N_A(N_A + N_D)} (V_{bi} - V)}$$

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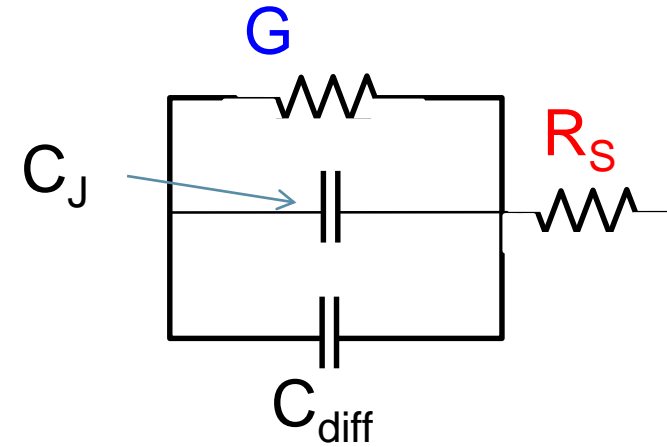
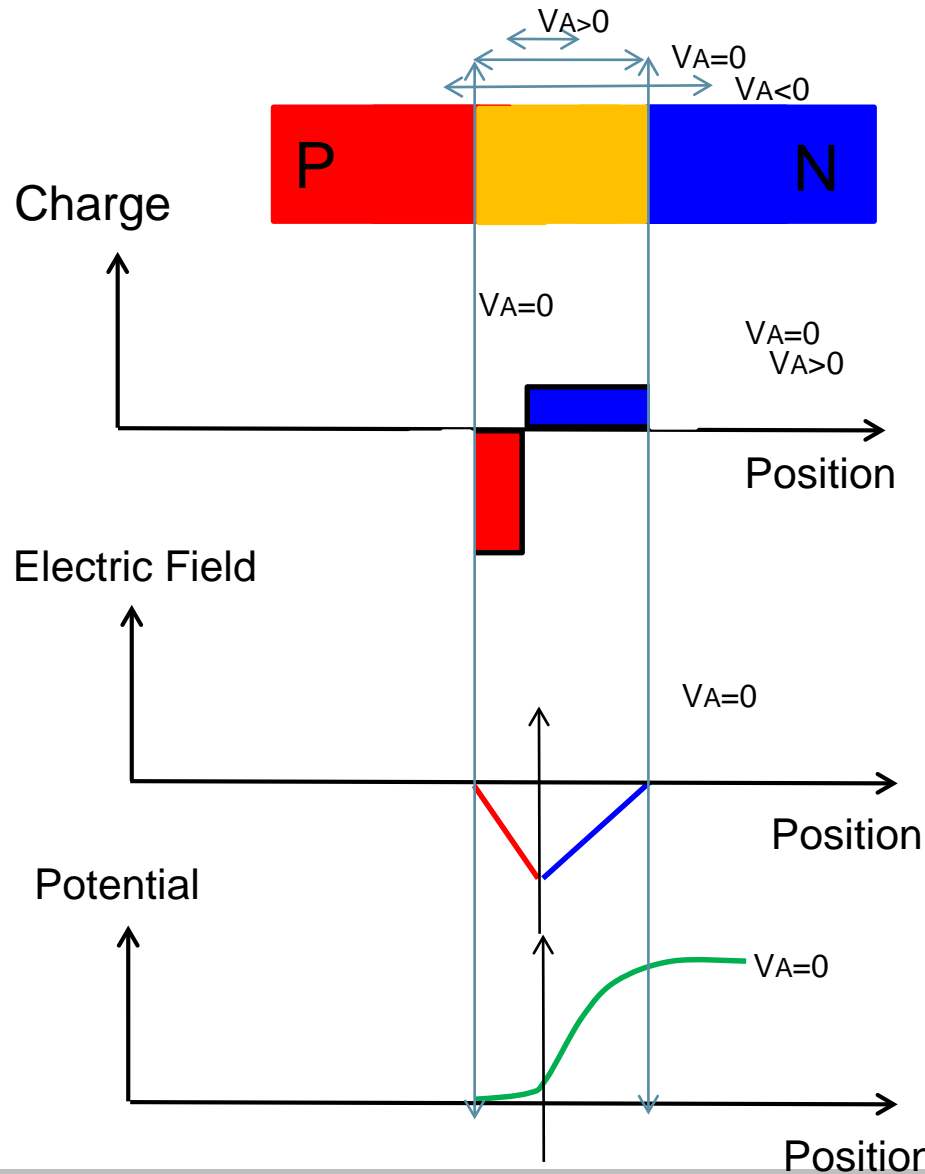


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Fields and Depletion as a Function of Bias

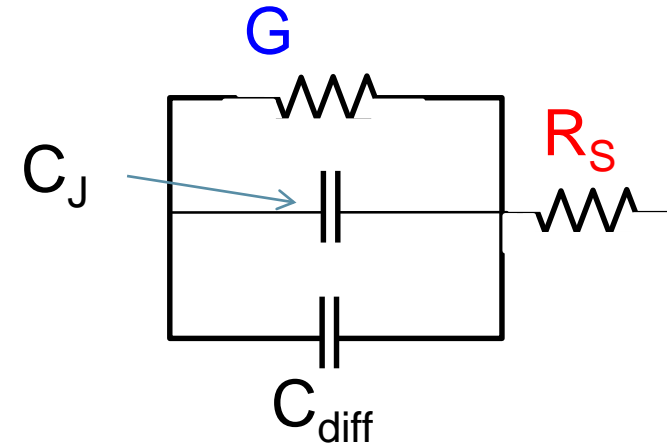
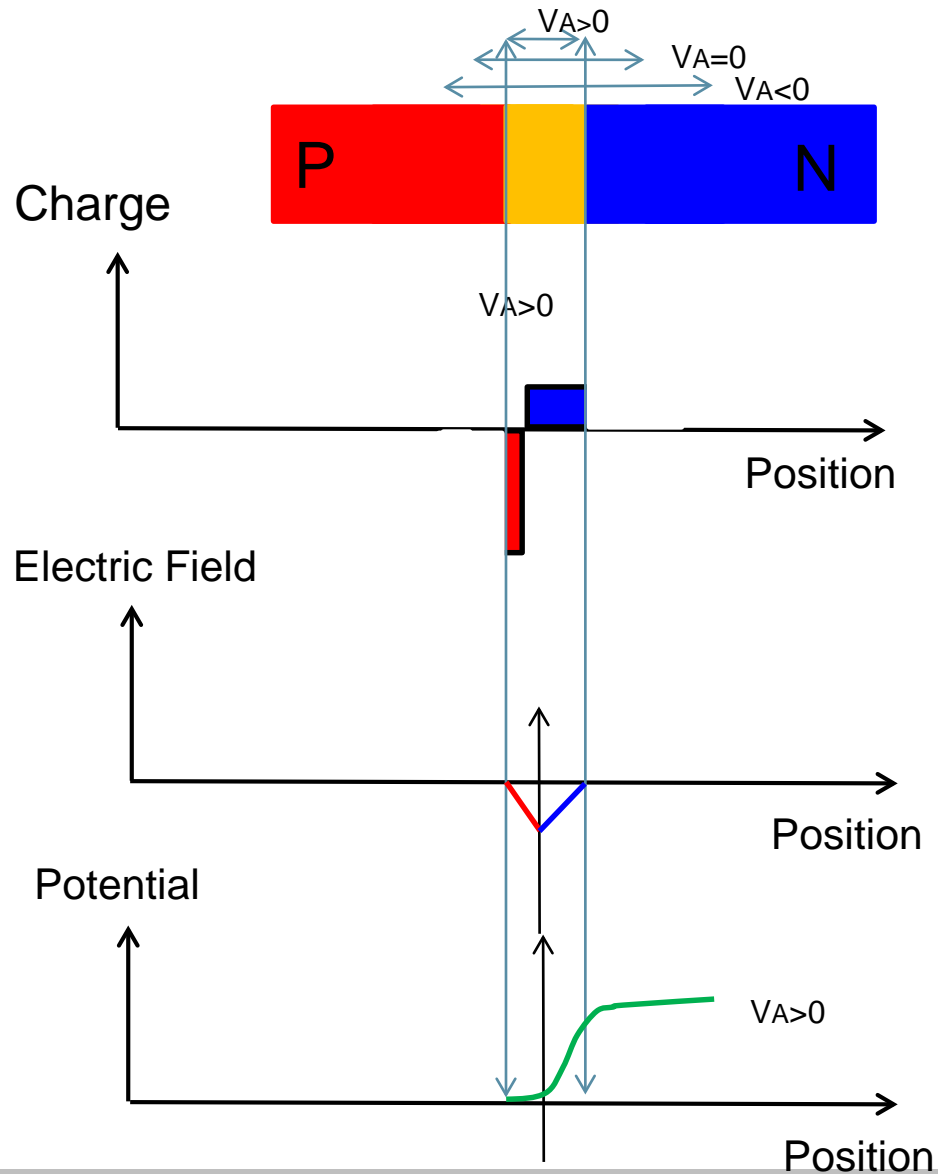


$$x_n = \sqrt{\frac{2k_s \epsilon_0}{q} \frac{N_A}{N_D(N_A + N_D)} (V_{bi} - V)}$$

DC

$$x_p = \sqrt{\frac{2k_s \epsilon_0}{q} \frac{N_D}{N_A(N_A + N_D)} (V_{bi} - V)}$$

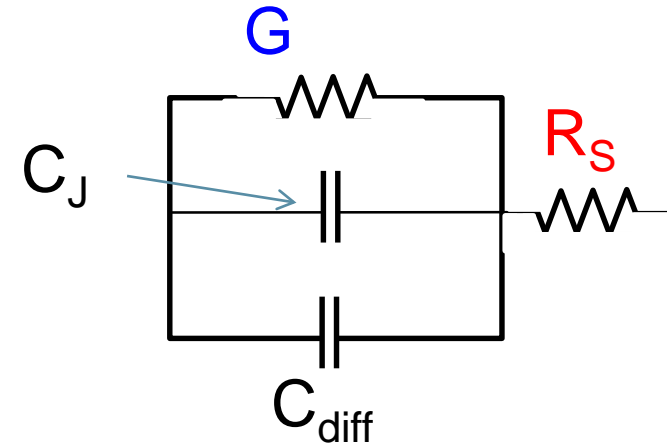
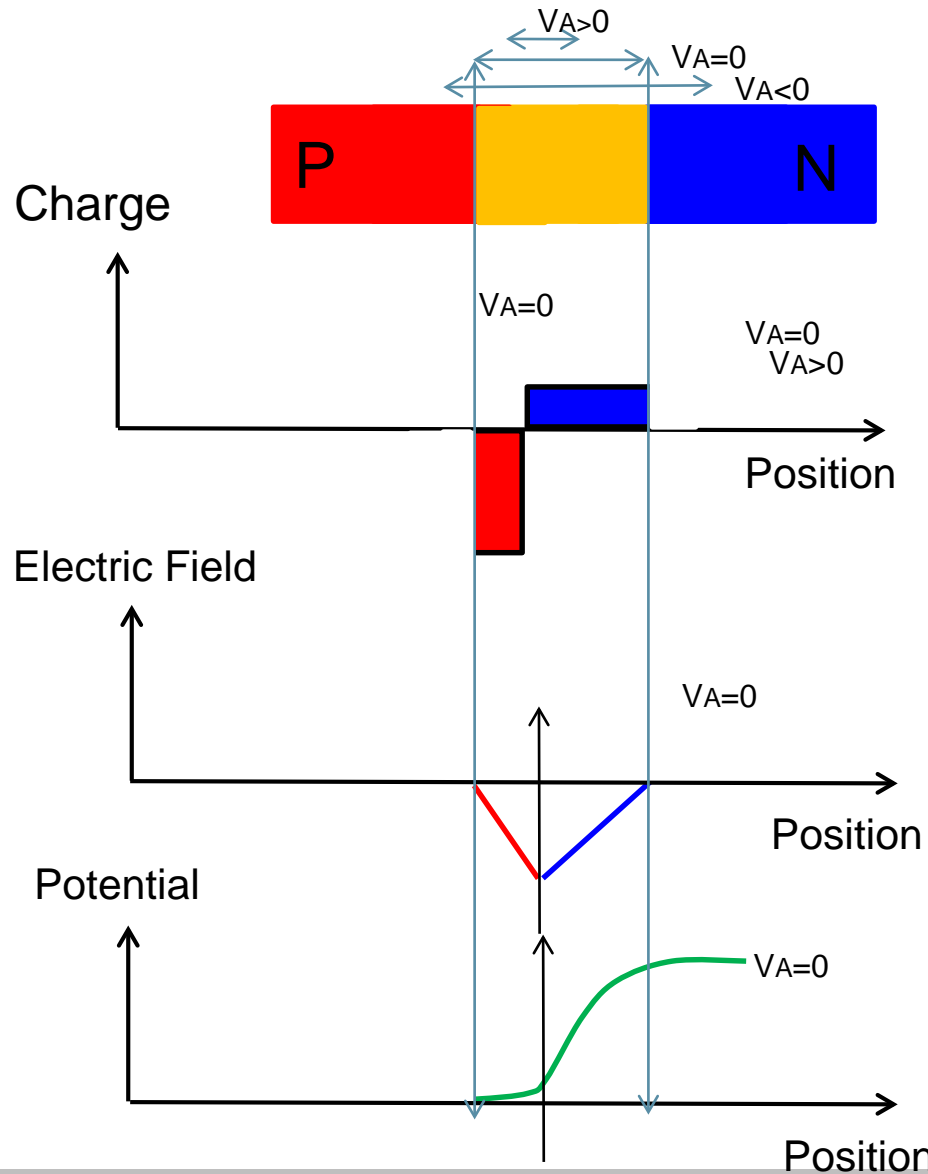
Fields and Depletion as a Function of Bias



$$x_n = \sqrt{\frac{2k_s \epsilon_0}{q} \frac{N_A}{N_D(N_A + N_D)} (V_{bi} - V)}$$

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Fields and Depletion as a Function of Bias

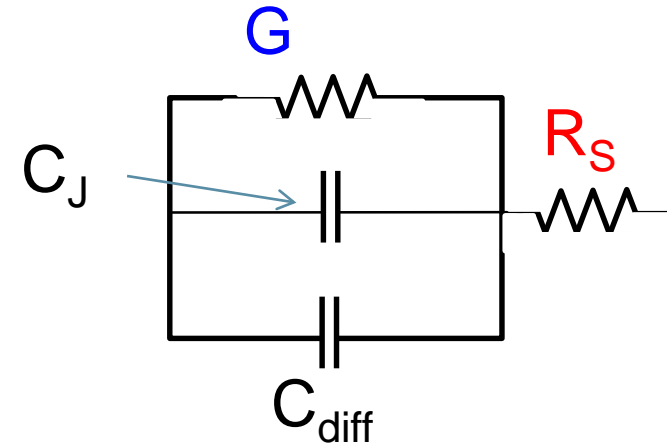
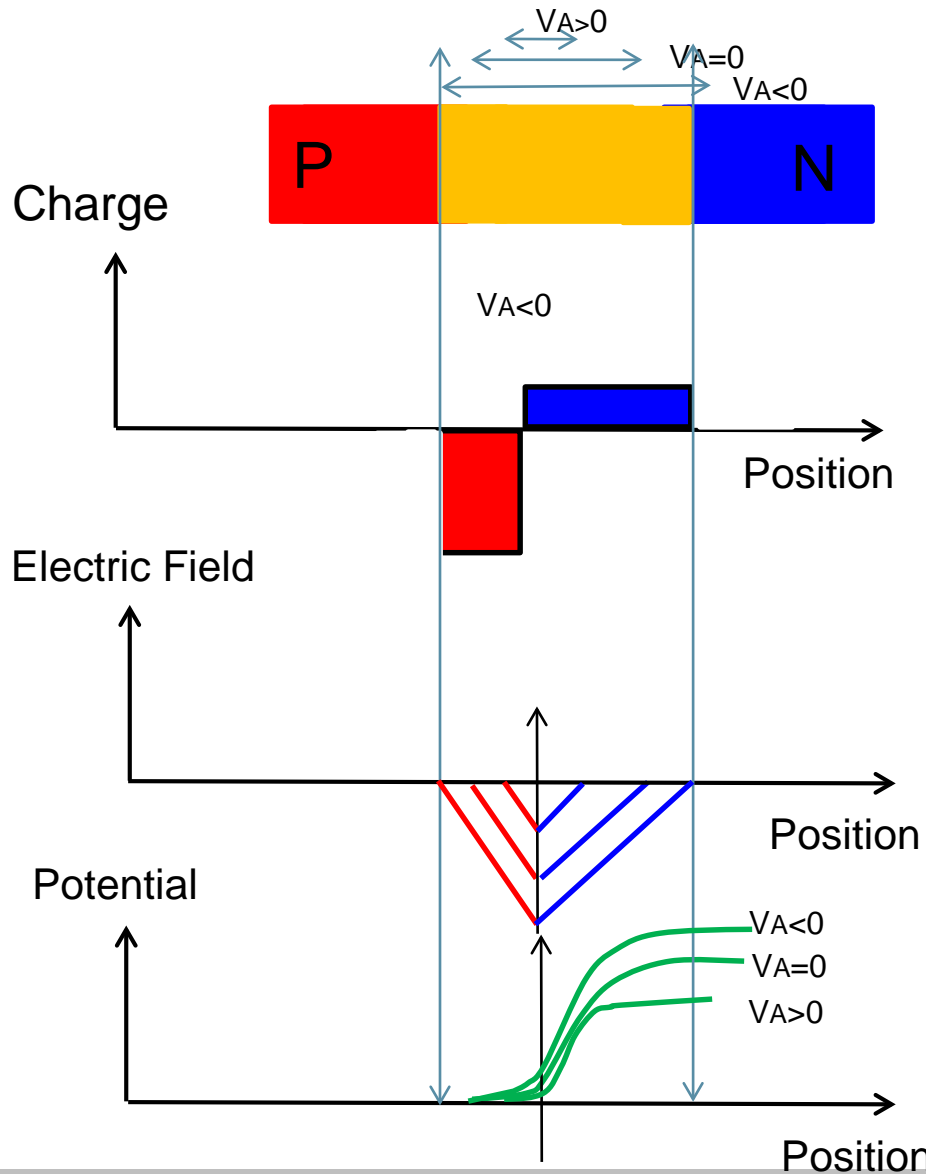


$$x_n = \sqrt{\frac{2k_s \epsilon_0}{q} \frac{N_A}{N_D(N_A + N_D)} (V_{bi} - V)}$$

DC

$$x_p = \sqrt{\frac{2k_s \epsilon_0}{q} \frac{N_D}{N_A(N_A + N_D)} (V_{bi} - V)}$$

Fields and Depletion as a Function of Bias



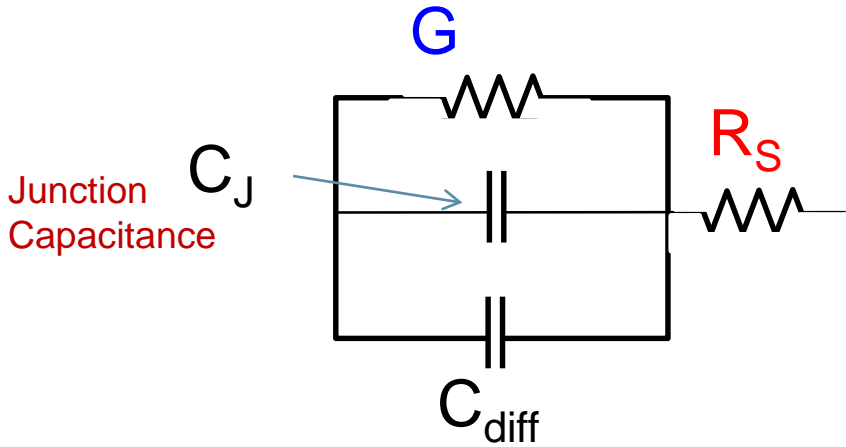
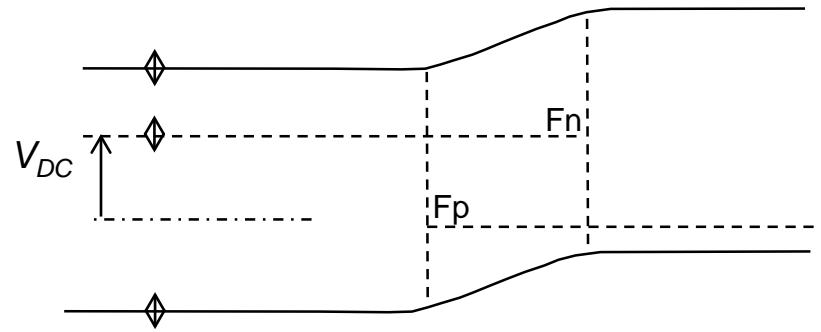
$$x_n = \sqrt{\frac{2k_s \epsilon_0}{q} \frac{N_A}{N_D(N_A + N_D)} (V_{bi} - V)}$$

DC - AC

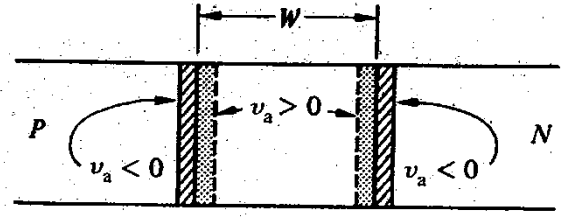
$$x_p = \sqrt{\frac{2k_s \epsilon_0}{q} \frac{N_D}{N_A(N_A + N_D)} (V_{bi} - V)}$$

Junction Capacitance

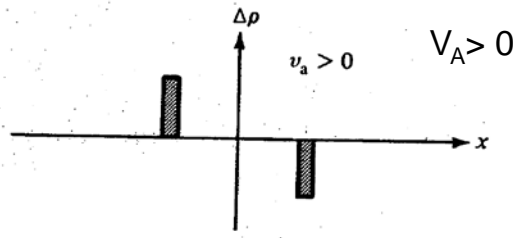
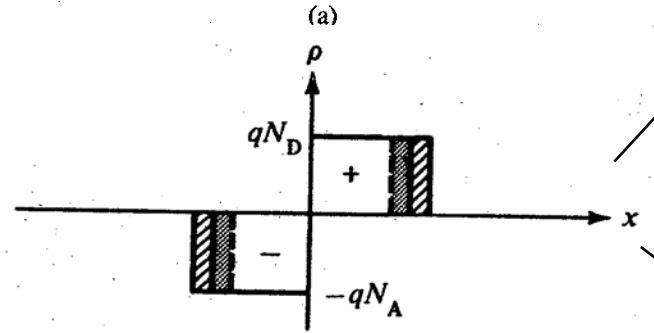
Forward biased diode + AC signal



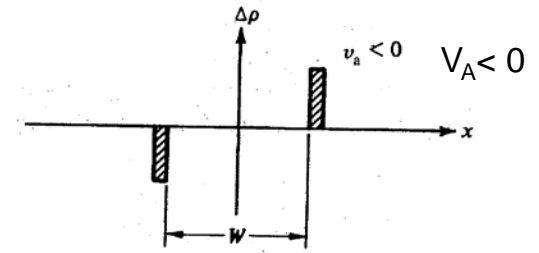
Depletion width modulation



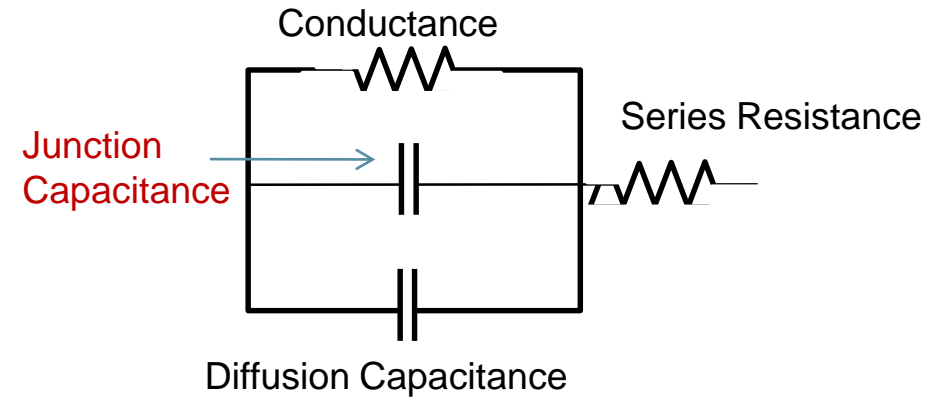
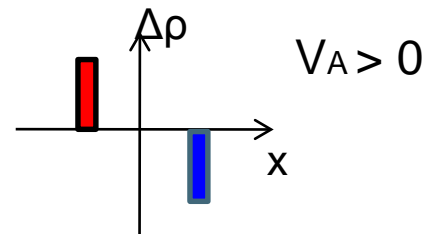
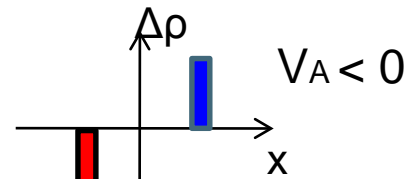
Charge modulation



Majority carrier effect



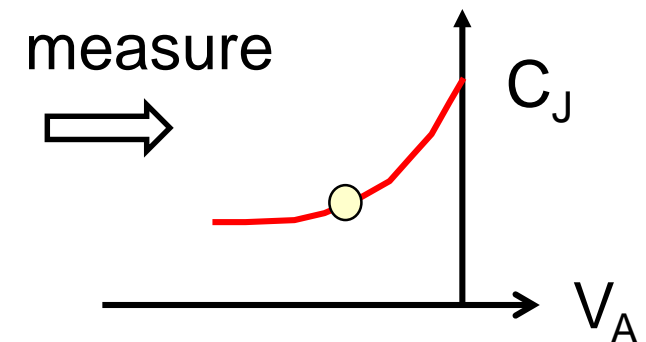
Majority Carrier Junction Capacitance



$$C_{plate} = \frac{K_s \epsilon_0 A}{d}$$

Majority Carrier Junction Capacitance

$$C_J = \frac{K_s \epsilon_0 A}{W_n + W_p} \quad C_J = \frac{K_s \epsilon_0 A}{\sqrt{\left(\frac{2K_s \epsilon_0}{qN_D} + \frac{2K_s \epsilon_0}{qN_A}\right) (V_{bi} - V_A)}}$$

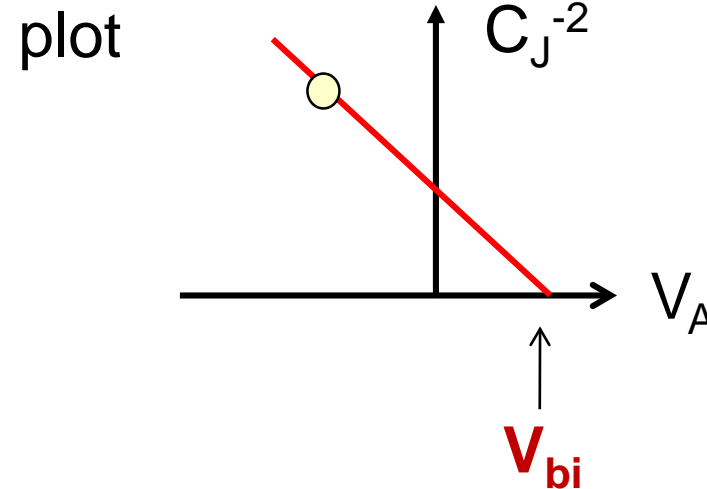
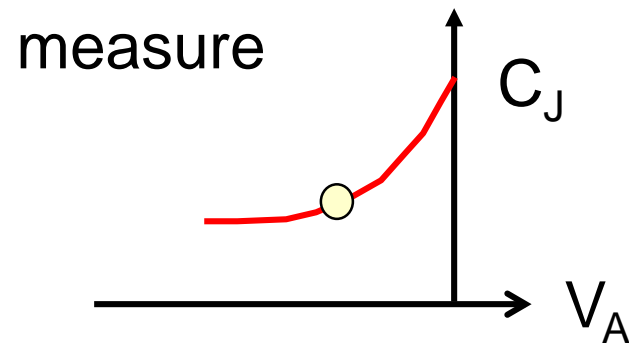


Measurement of Built-in Potential

$$C_J = \frac{K_s \epsilon_0 A}{\sqrt{\left(\frac{2K_s \epsilon_0}{qN_D} + \frac{2K_s \epsilon_0}{qN_A}\right) (V_{bi} - V_A)}}$$

$$\frac{1}{C_J^2} \approx \frac{2}{qN_D(x)K_s \epsilon_0 A^2} (V_{bi} - V_A)$$

(Assume single sided p⁺-n junction)

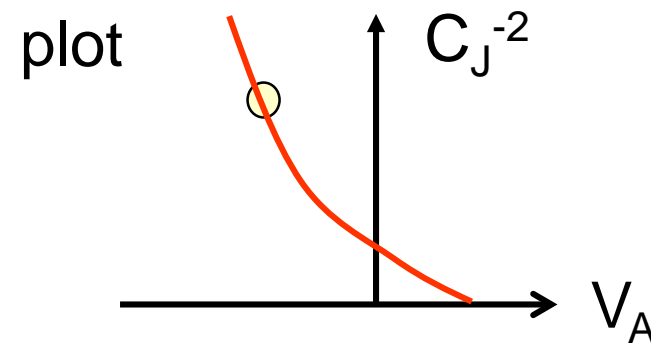
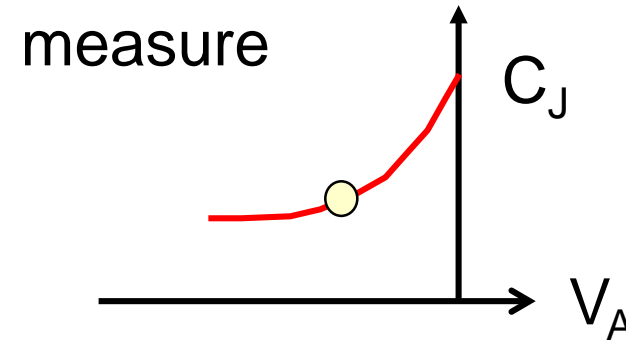
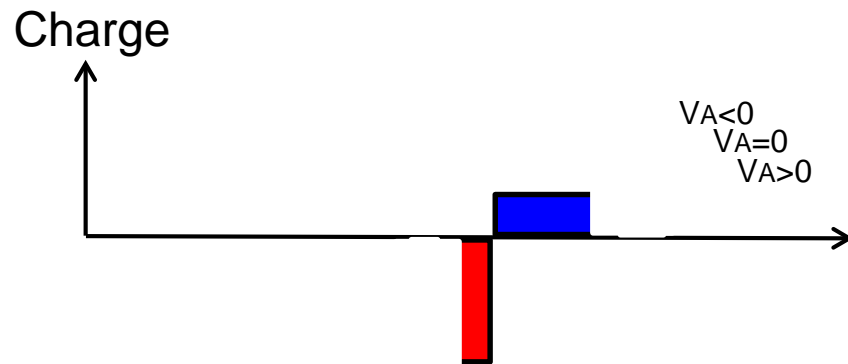


.. And Variable Doping - probe charge at depletion region edge

$$\frac{1}{C_J^2} \approx \frac{2}{qN_D(x)K_s\epsilon_0A^2}(V_{bi} - V_A)$$

$$N_D(x) = \frac{2}{qK_s\epsilon_0A^2} \frac{1}{d(1/C_J^2)/dV_A}$$

Measure doping concentration as a function of position



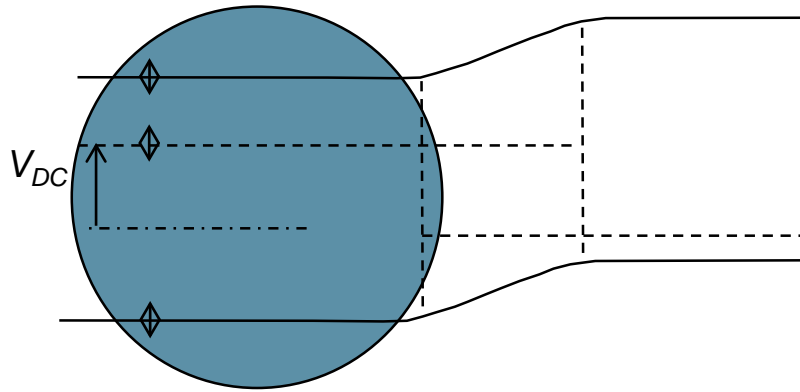
Dielectric Relaxation Time (majority side)

Majority side

$$J_n = qn\mu_N E + q\cancel{D}_N \nabla n$$

Neglect

$$\frac{dn}{dt} = \frac{1}{q} \frac{dJ_n}{dx} - \cancel{R}_n + \cancel{G}_n$$



$$\frac{d(\Delta n)}{dt} = \frac{1}{q} \frac{d(qn\mu_N \mathcal{E})}{dx} = N_D \mu_N \frac{d\mathcal{E}}{dx}$$

N-side

$$\frac{d\mathcal{E}}{dx} = \frac{q}{k_s \epsilon_0} (\cancel{P} - n_0 - \Delta n + N_D - \cancel{N}_A)$$

$$N_D = n_0$$

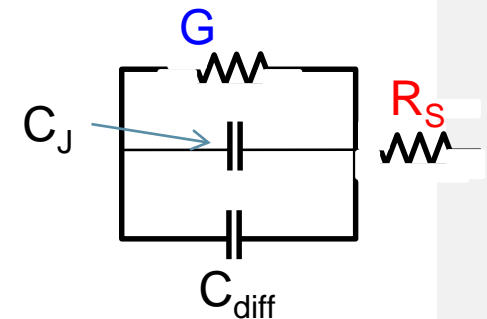
How long does it take for the signal to cross the junction?

$$\tau_d = \frac{K_s \epsilon_0}{\sigma} \approx 0.1 \text{ ps}$$

Very fast

$$\frac{d(\Delta n)}{dt} = -\frac{qN_D \mu_N}{k_s \epsilon_0} \Delta n = -\frac{\sigma_0 \Delta n}{k_s \epsilon_0}$$

$$\Delta n(t) = n_0 e^{-\frac{\sigma_0 t}{k_s \epsilon_0}} = n_0 e^{-\frac{t}{\tau_d}}$$

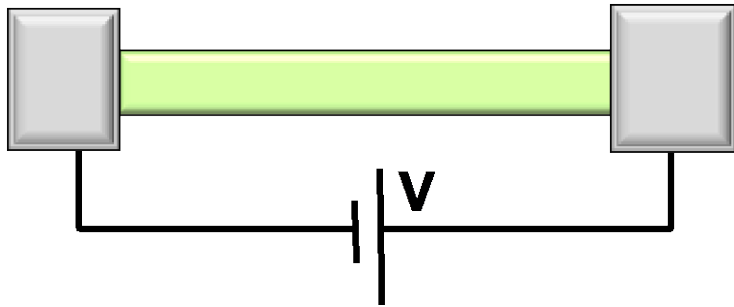


Overall electronic system responds => dielectric response

$$\frac{1}{C_J^2} \approx \frac{2}{qN_D(x)K_s \epsilon_0 A^2} (V_{bi} - V_A)$$

Section 21

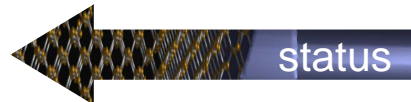
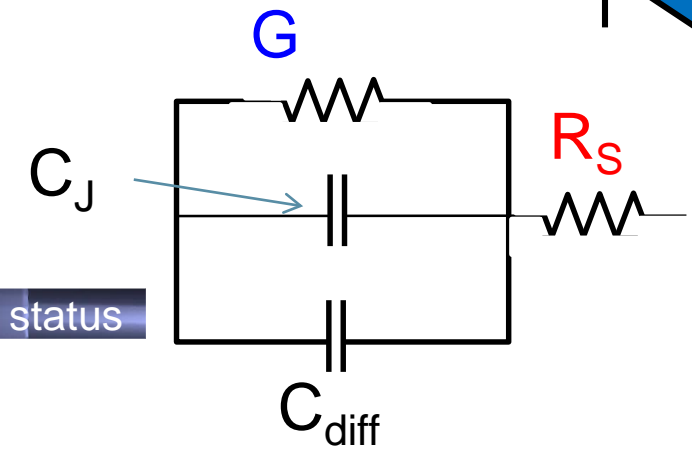
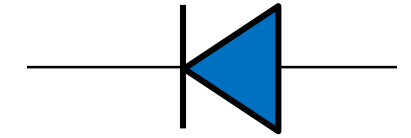
PN Diode AC Response



$$I = G \times V$$

$$= q \times n \times v \times A$$

charge density velocity area

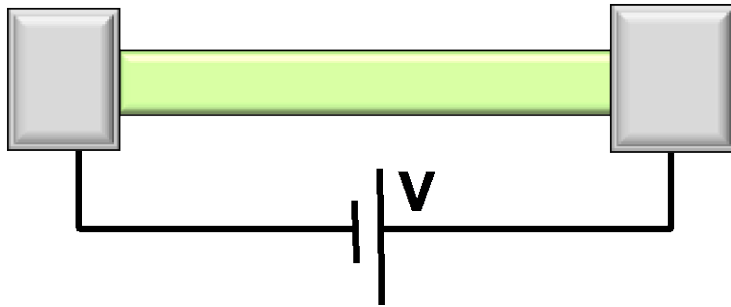


- > • 21.1 Conductance and series resistance
- > • 21.2 Majority carrier junction capacitance
- > • 21.3 Minority carrier diffusion capacitance

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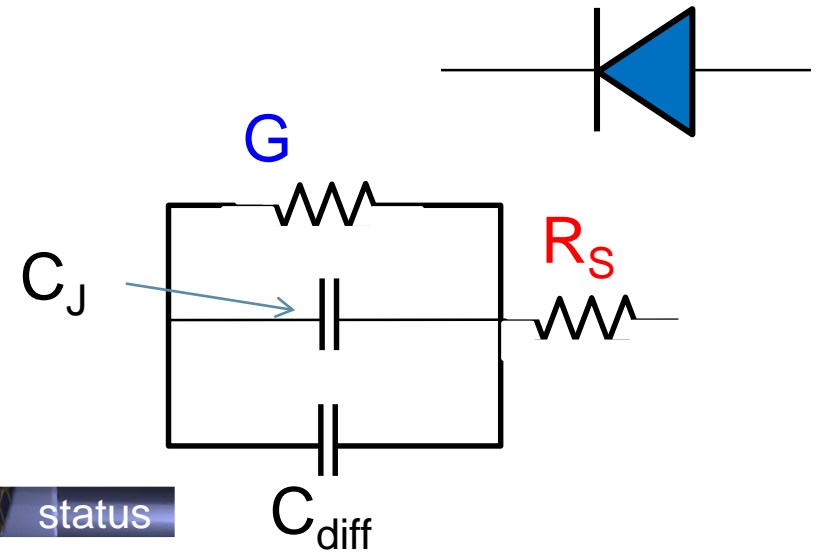


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↑ charge density ↑ n ↑ v ↑ area

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