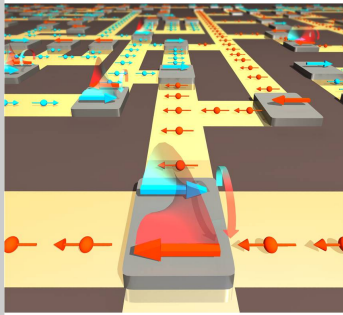


## All Spin Logic

**This page is under construction. Updates coming soon ...**



Electronic devices have traditionally been based on controlling the flow of charge. However, electrons carry both charge and “spin”, the latter being responsible for magnetic phenomena. Over the last decade or so, the fields of Spintronics and magnetism have evolved into one large field. This is primarily due to two key experimentally demonstrated phenomena: spin injection into metals/semiconductors from magnetic contacts and spin induced magnetization dynamics of magnets by spin transfer torque. These demonstrations suggest an all-spin approach to information processing. Magnets inject spins and spins turn magnets forming a closed “**ecosystem**” without the need to convert to charge. A recently proposed concept called **all spin logic** (ASL) uses magnets to represent non-volatile data and communicate between them using spin currents with the energy coming from the power supply. ASL approach to information processing uses a formalism recently developed at Purdue University for integrated spin-magnet circuits. This approach is particularly powerful in that it combines various spin related phenomena such as spin-injection, spin-transport and spin-detection with that of magnet dynamics into one unified **spin-circuit** theory. This formalism does not use approximations in defining the elements of the spin-magnet circuit as usually done in defining compact models. At the same time, it is capable of modeling integrated circuits composed of many magnets interconnected with spin-coherent interconnects.

Learn more about the ASL approach to information processing from the resources available on this site, listed below.

- 1) Information processing with spins and magnets: basic concepts with simple figures
- 2) Applications to digital logic
- 3) Applications to neuromorphic networks
- 4) Research Publications

- \* B. Behin-Aein, D. Datta, S. Salahuddin and S. Datta, "*Proposal for and all-spin logic device with built-in memory,*" [Nature Nanotechnology, 5, 266, \(2010\)](#)
- \* B. Behin-Aein and S. Datta, "*All-spin logic,*" [Device Research Conference \(DRC\), \(2010\)](#)
- \* B. Behin-Aein, A. Sarkar, S. Srinivasan and S. Datta, "*Switching energy-delay of all spin logic devices,*" [Applied Physics Letters 98, 123510, \(2011\)](#)
- \* S. Srinivasan, A. Sarkar , B. Behin-Aein and S. Datta, "*Unidirectional information transfer with cascaded All Spin Logic devices: A Ring Oscillator,*" [Device Research Conference \(DRC\), \(2011\)](#)
- \* S. Srinivasan, A. Sarkar , B. Behin-Aein and S. Datta, "*All-Spin Logic Device with inbuilt Non-Reciprocity,*" [IEEE Transactions on Magnetism, 47,4026, \(2011\)](#)
- \* A. Sarkar, S. Srinivasan, B. Behin-Aein and S. Datta, "*Modeling All Spin Logic: Multi-magnet Networks Interacting via Spin Currents,*" *To appear: International electron devices meeting (IEDM) technical digest, (2011).*

### 5) Review Articles