

Spring 2019 Purdue University

ECE 255: L13

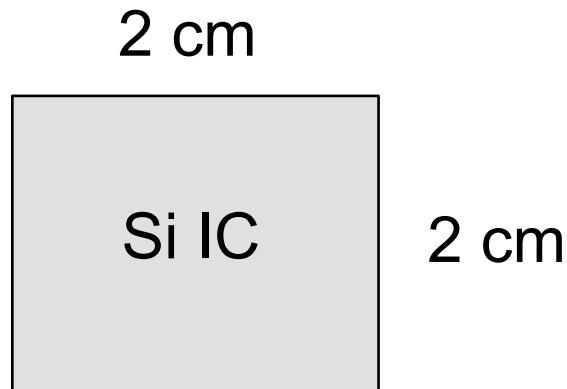
MOSFETs

(Sedra and Smith, 7th Ed., Sec. 5.1)

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School of ECE
Purdue University
West Lafayette, IN USA

Lundstrom: 2019

Quiz



How many kilometers of wire are there in a modern Si chip?

$$1 \text{ km} = 10^5 \text{ cm}$$

- a) 2×10^{-5}
- b) 10×10^{-5}
- c) 4×10^{-2}
- d) 2×10^{-1}
- e) 3

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Company Information Session

Monday, February 11th

7:00 - 8:00 PM

MJIS 1001

Presentation followed by Q&A

Learn about the history of our company, an overview of our current areas, and what we look for in a BME or EE intern, co-op, and new employee!

www.cookresearchinc.com

**Visit our booth at the Professional Practice Career Fair
Tuesday February 19, 10 AM-3 PM, in PMU Ballrooms**

Mon 2/18 Information Sessions HCRS 1076

8:30am Fulbright U.S. Student Program Overview
11:30am Fulbright U.S. Student Program Overview

**Mon 2/18 Fulbright Program Mixer Duhme Hall
Atrium**

6:00pm Join us for food and drink with Purdue Fulbright alumni
to hear about their experiences in the program.

**Tues 2/19 Information Sessions HCRS
1066**

3:30pm Research & Study Grants – guidance for graduate students
4:15pm 2020-21 English Teaching Assistant Grants
5:00pm 2020-21 Research and Study Grants

Wed 2/20 Information Sessions HCRS 1066

5:00pm 2020-21 Research and Study Grants
5:45pm 2020-21 English Teaching Assistant Grants
6:30pm Research & Study Grants – guidance for graduate students

Thurs 2/21 Information Sessions HCRS 1066

5:00pm 2020-21 English Teaching Assistant Grants
5:45pm 2020-21 Research and Study Grants



FEB. 18-21

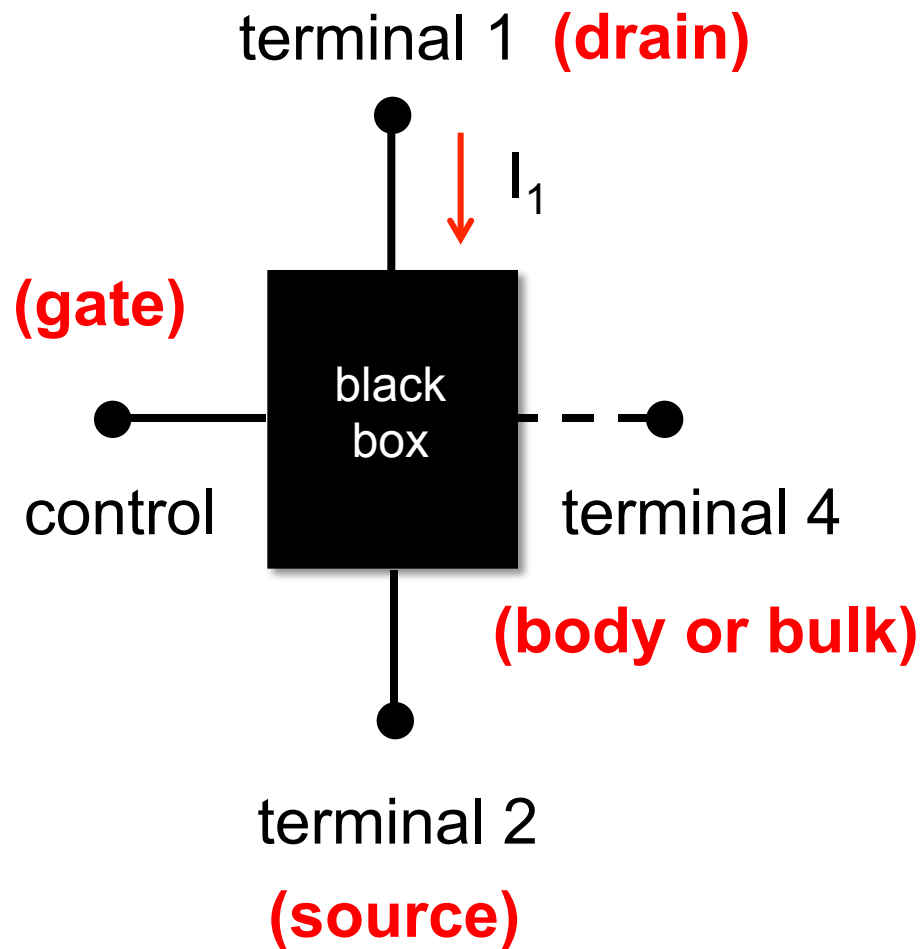
Juniors begin applications now for a Fulbright U.S. Student Program grant after graduation in one of 140+ countries. Seniors look ahead a year to 2020-21. Graduate students conduct thesis/dissertation research abroad. Freshmen and sophomores explore options.

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HONORS COLLEGE

MOSFETs

- 1) Physical structure
- 2) IV characteristics (descriptive)
- 3) IV characteristics (energy band approach)
- 4) IV characteristics (mathematical model)

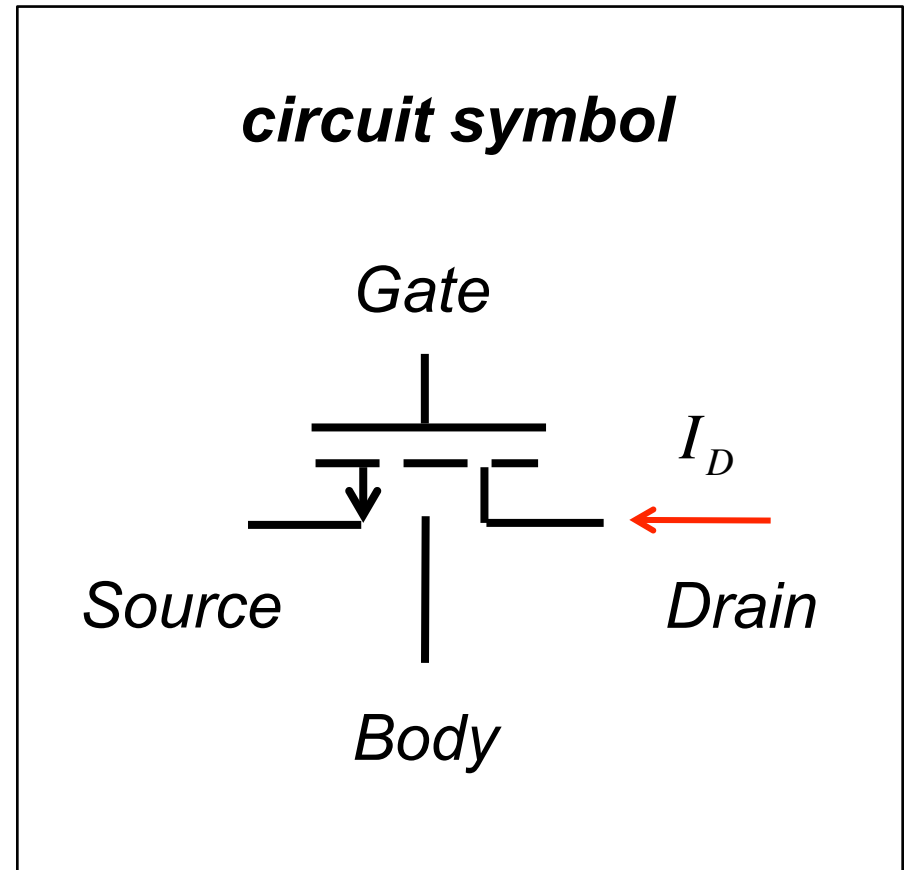
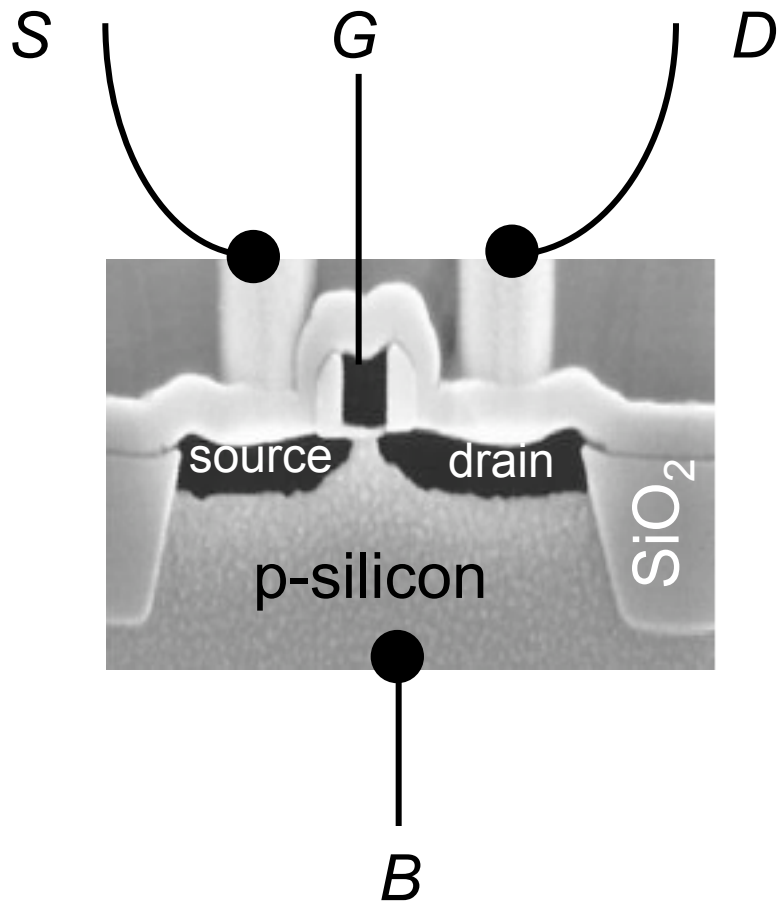
Transistor as a “black box”



A small current (or voltage) on the control terminal controls a much larger current through two other terminals.

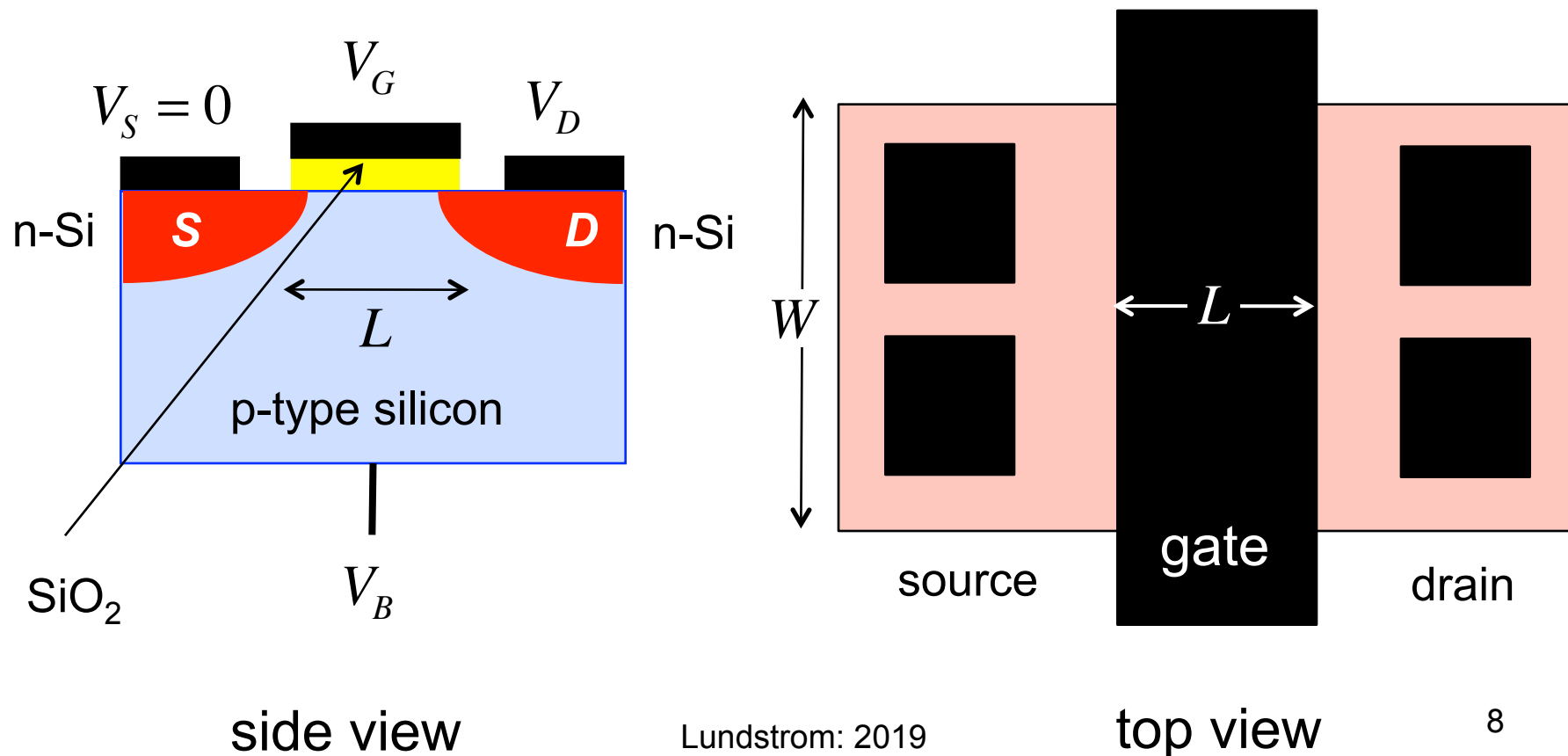
Current control: BJT
Voltage control: MOSFET

Physical structure

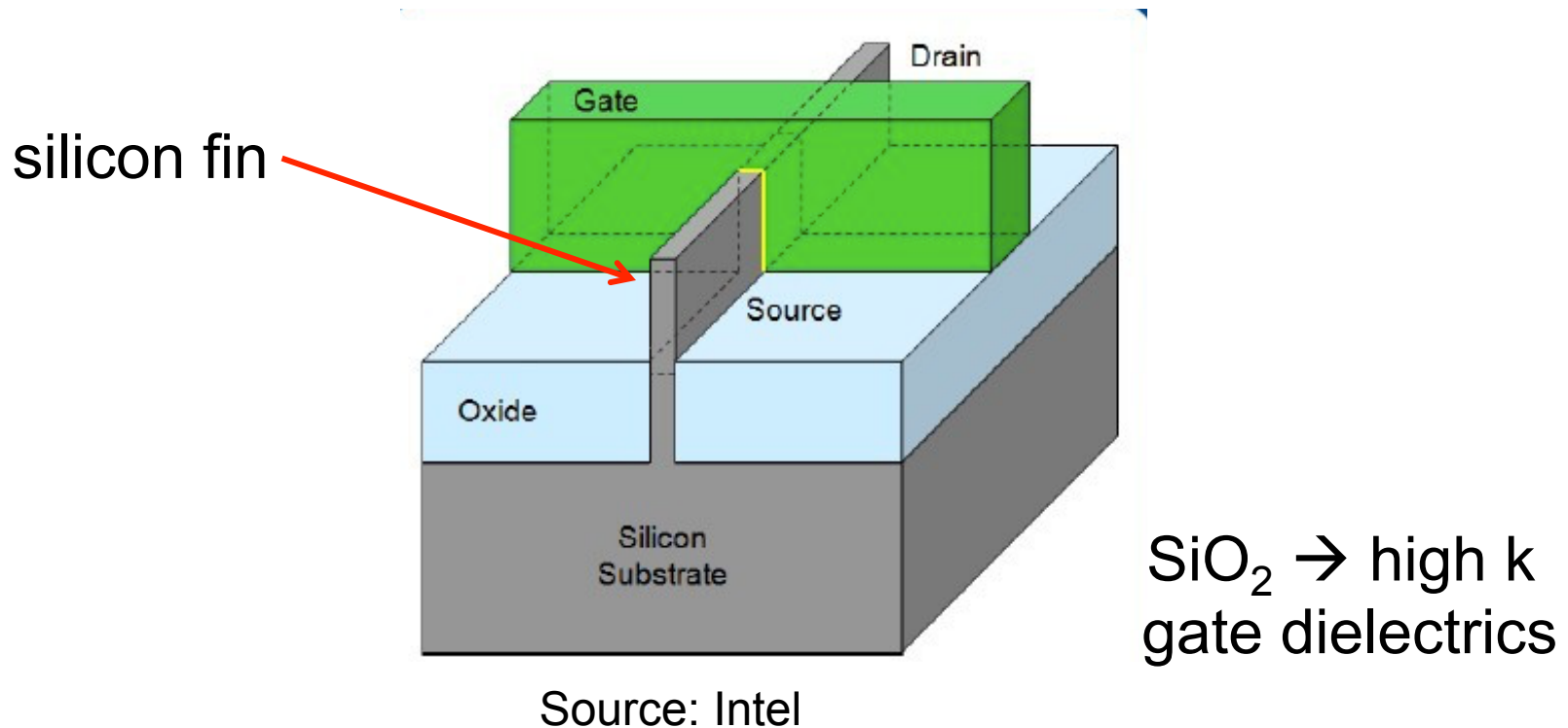


Side and top views of an n-channel MOSFET

Metal Oxide Semiconductor Field Effect Transistor

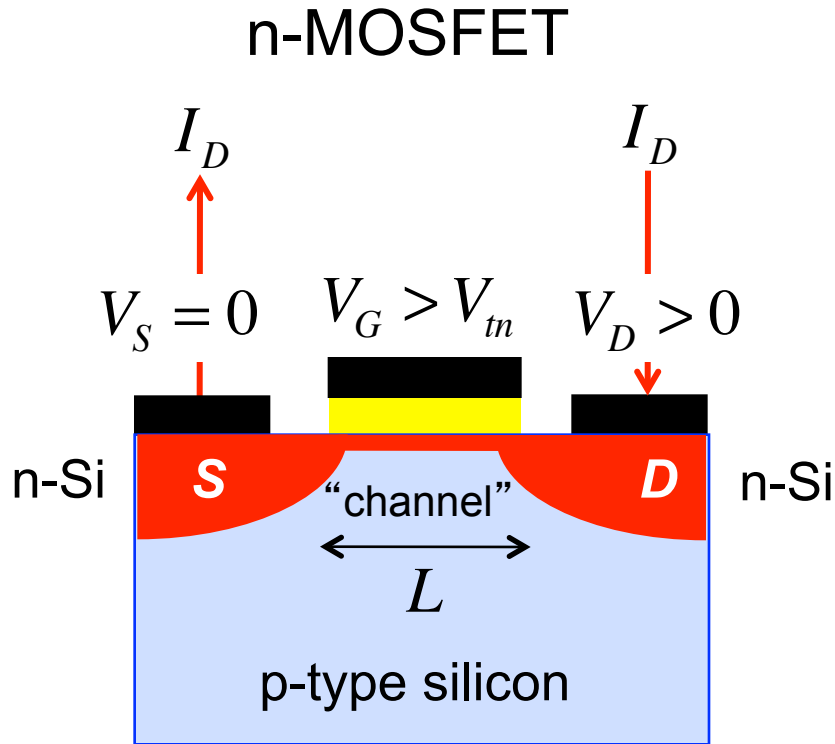


Modern MOSFETs: FinFETs



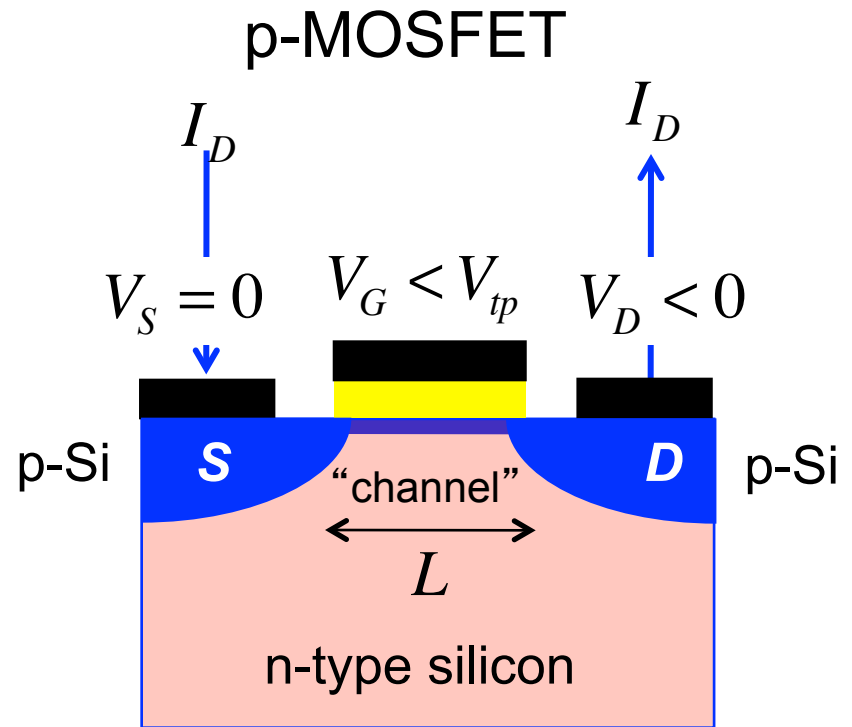
Digh Hisamoto, Wen-Chin Lee, Jakub Kedzierski, Hideki Takeuchi, Kazuya Asano, Charles Kuo, Erik Anderson, Tsu-Jae King, Jeffrey Bokor, Chenming Hu, "FinFET-a self-aligned double-gate MOSFET scalable to 20 nm," *IEEE Transactions on Electron Devices*, **47**, 2320-2325, 2000.

N-channel vs. P-channel MOSFET



side view

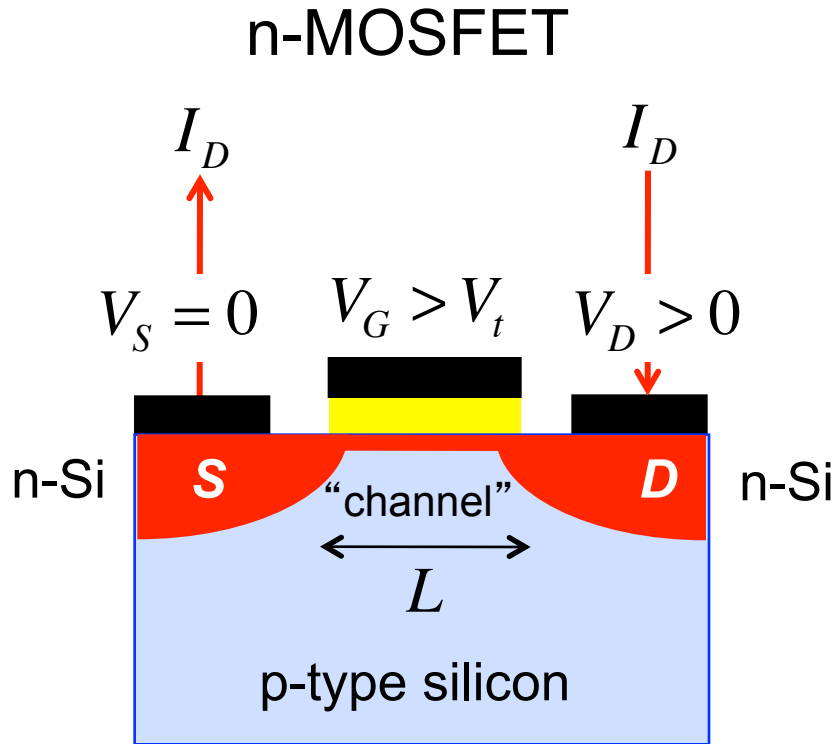
Current flows **in** the drain



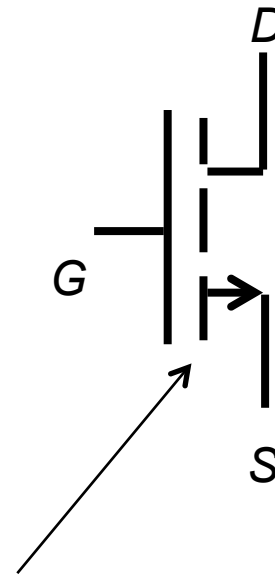
side view

Current flows **out** the drain ₁₀

N-channel MOSFET: circuit symbol

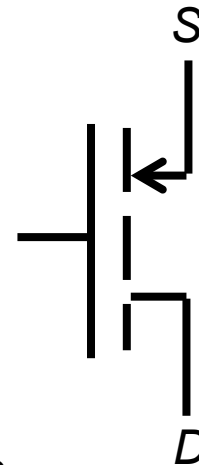
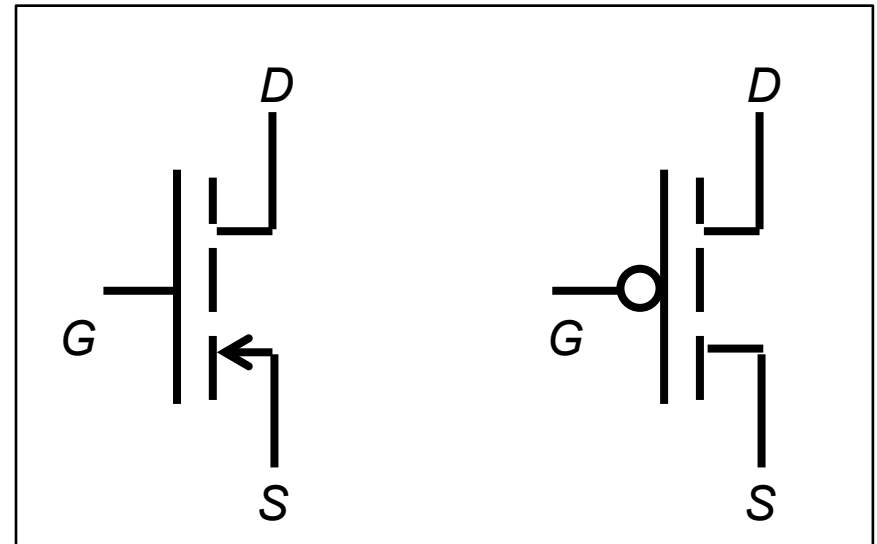
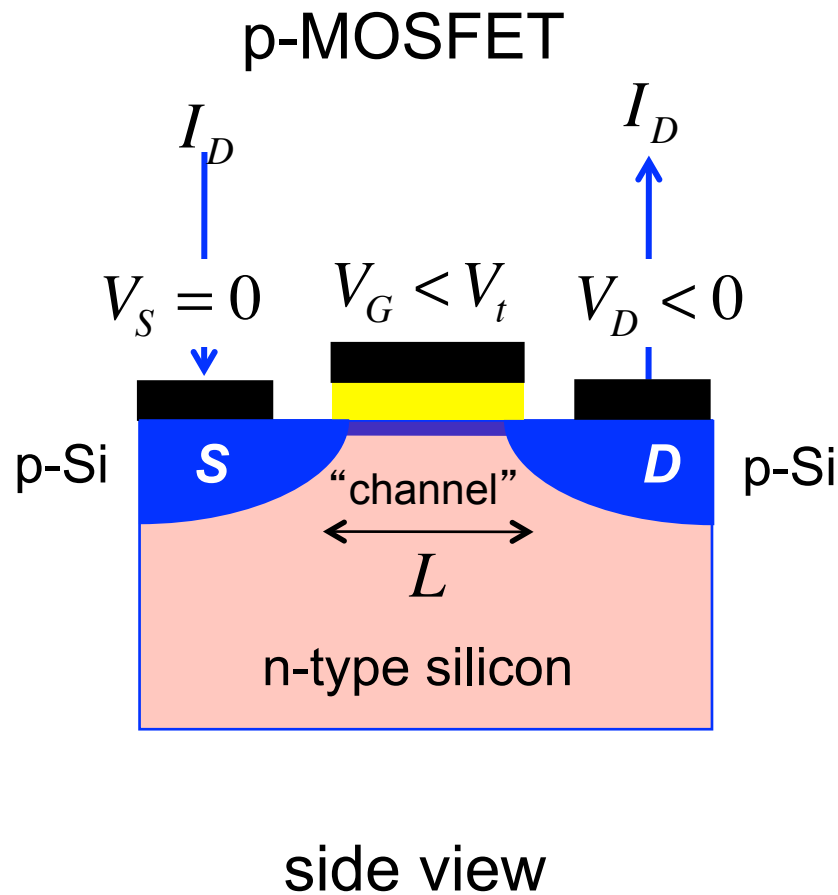


side view

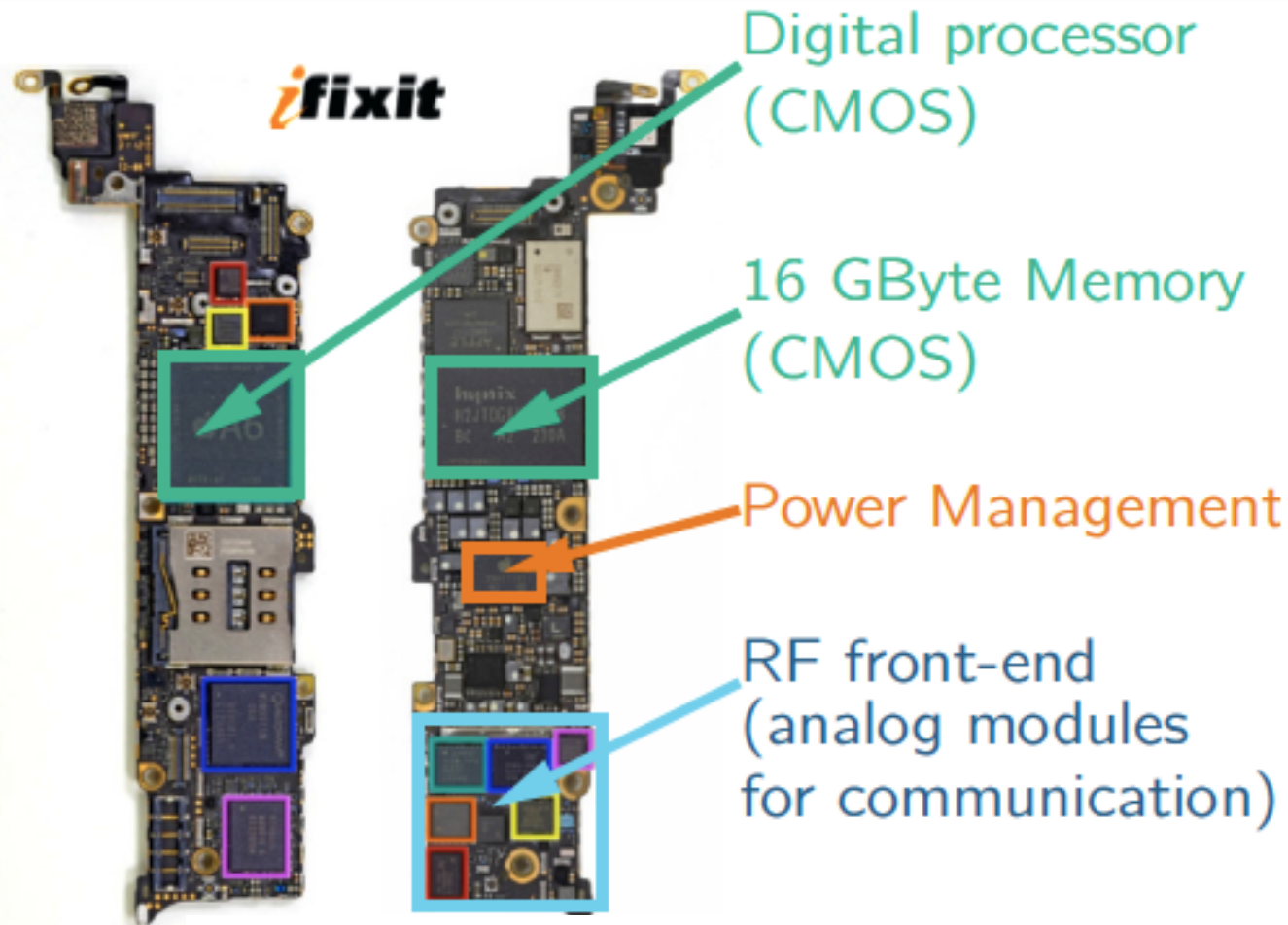


dashed line indicates an **enhancement mode** MOSFET.

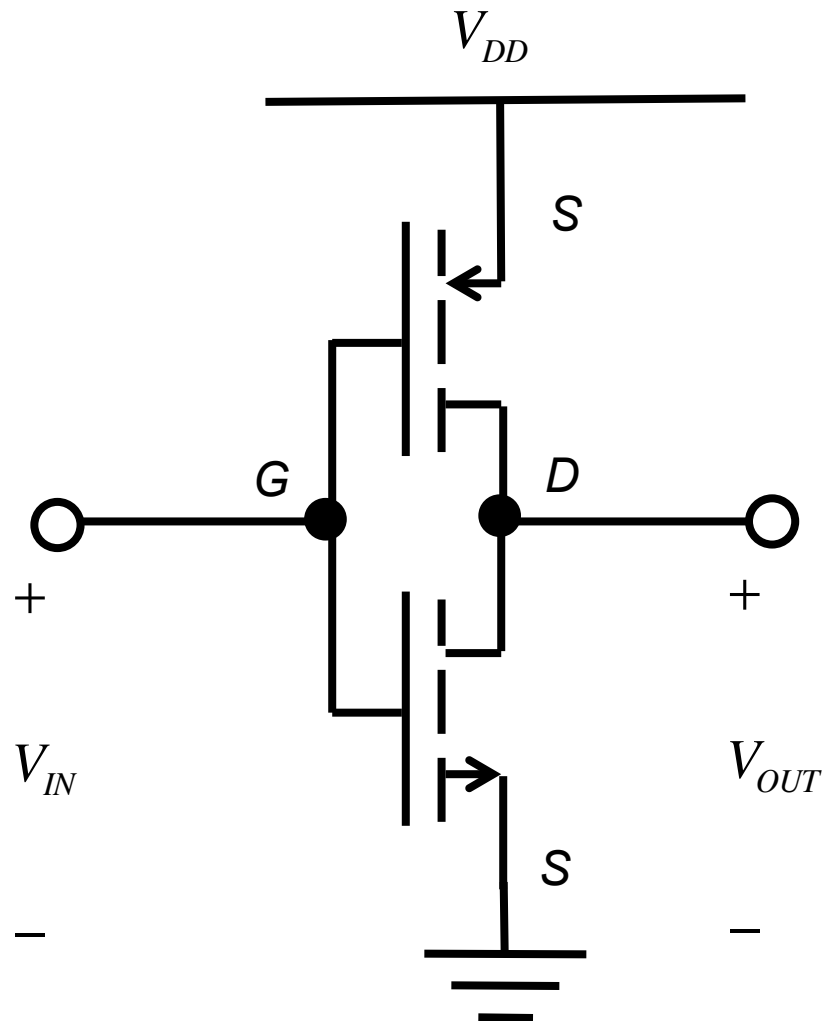
p-channel MOSFET: circuit symbol



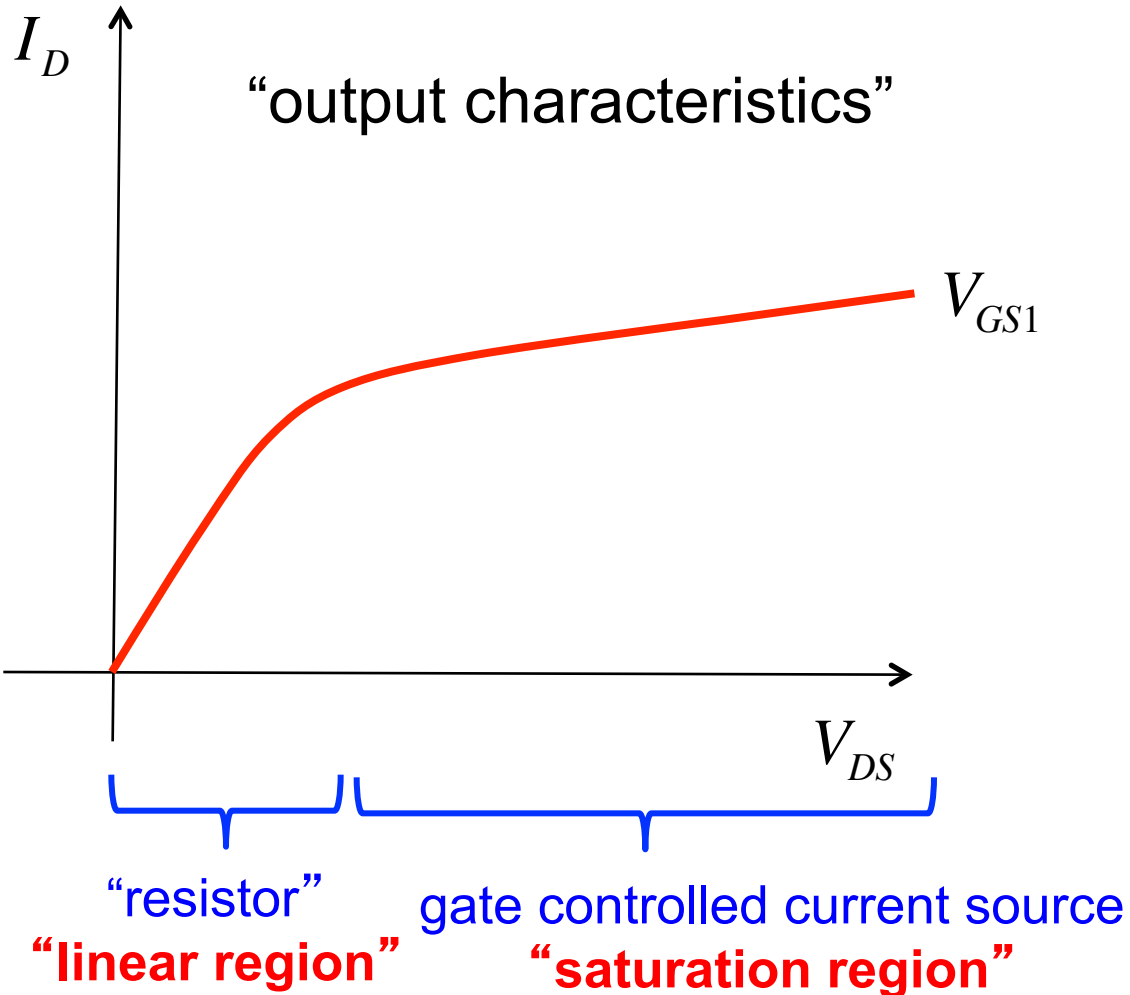
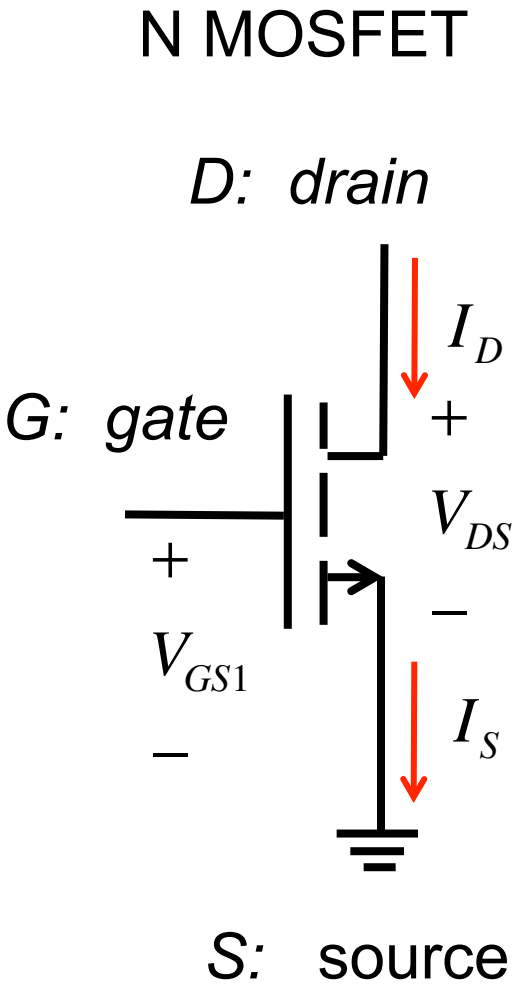
MOSFETs and BJT



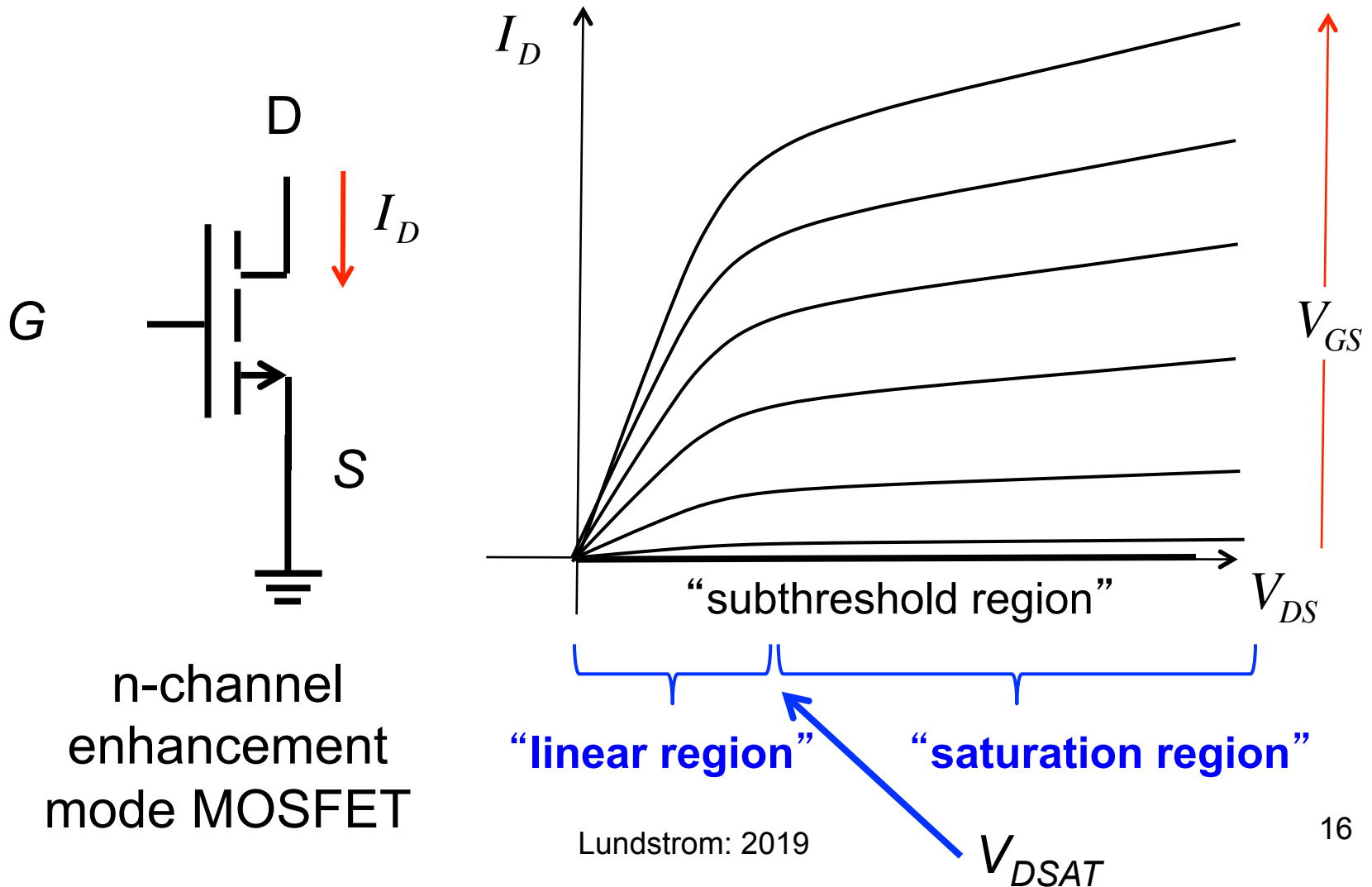
CMOS digital technology



N-MOSFET IV characteristics



N-MOSFET output characteristics

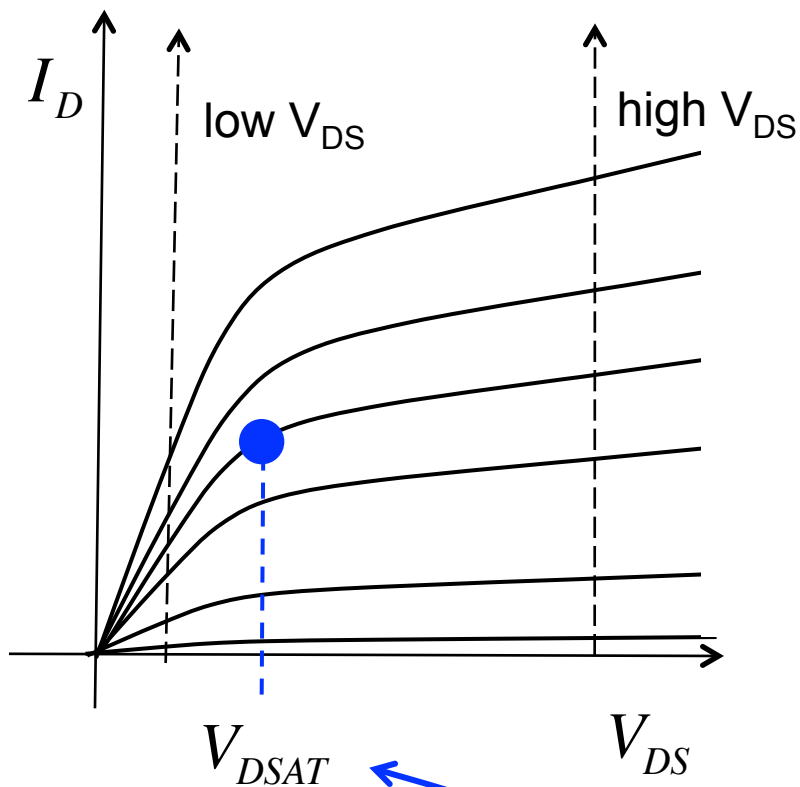


Output vs. transfer characteristics

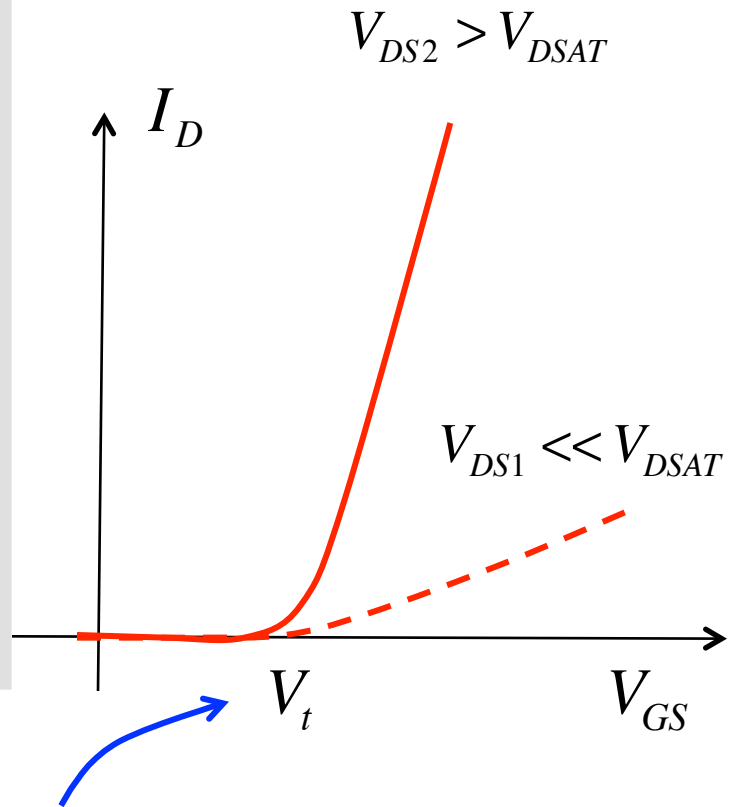
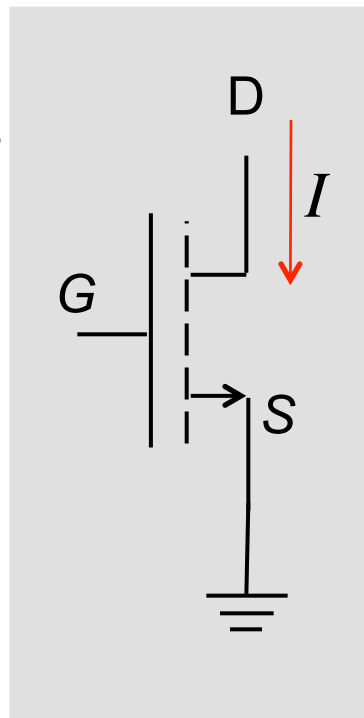
output characteristics



transfer characteristics

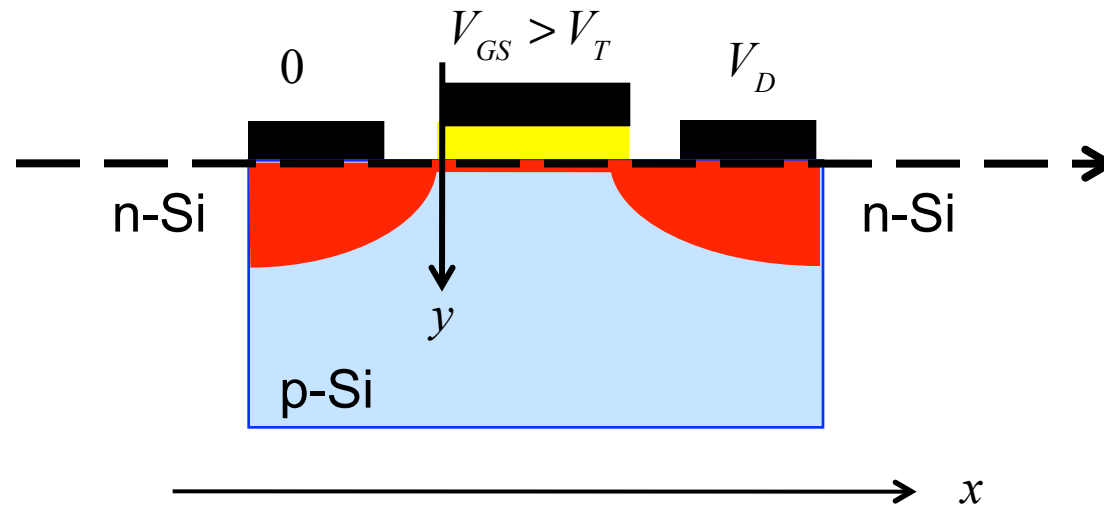


“saturation voltage”



Lundstrom: 2019 “threshold voltage”

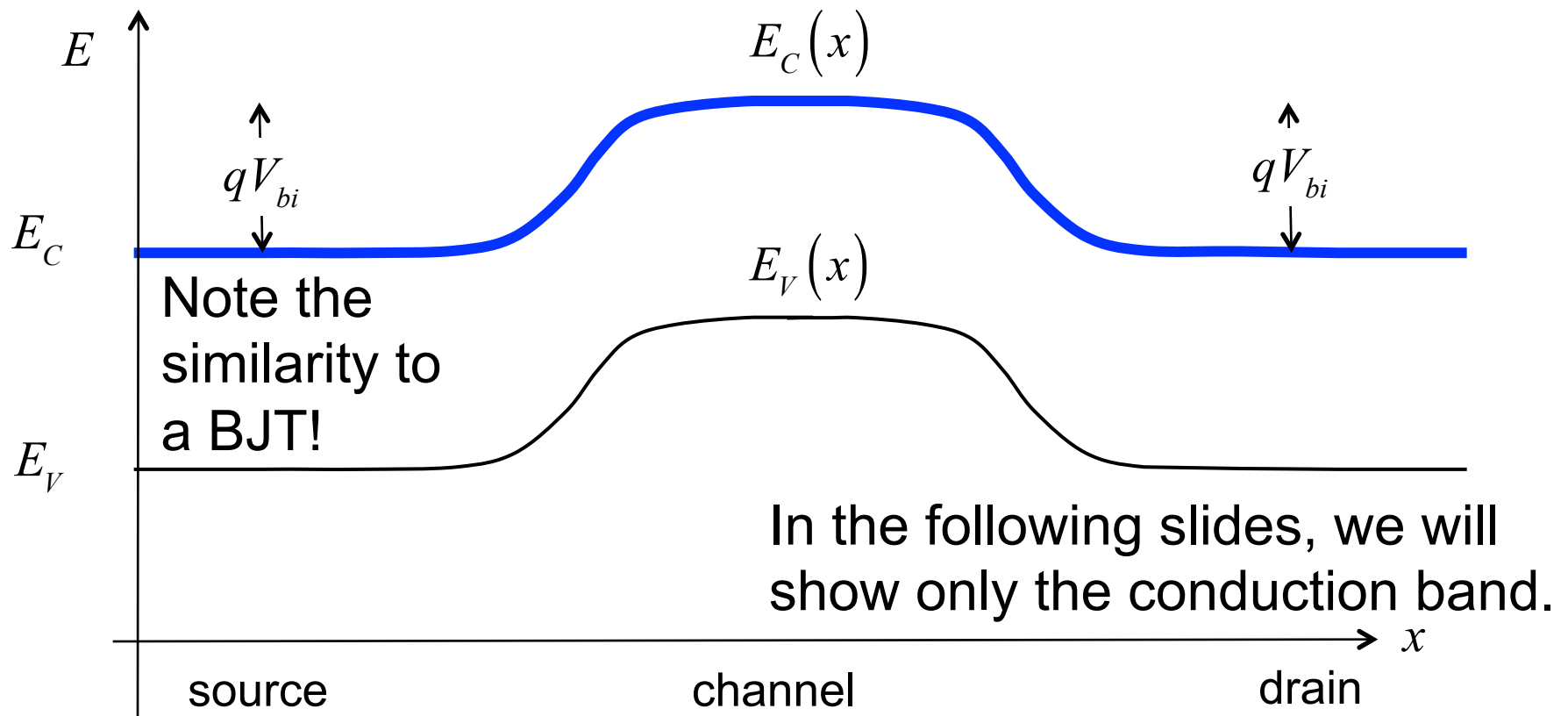
Energy band treatment of the MOSFET



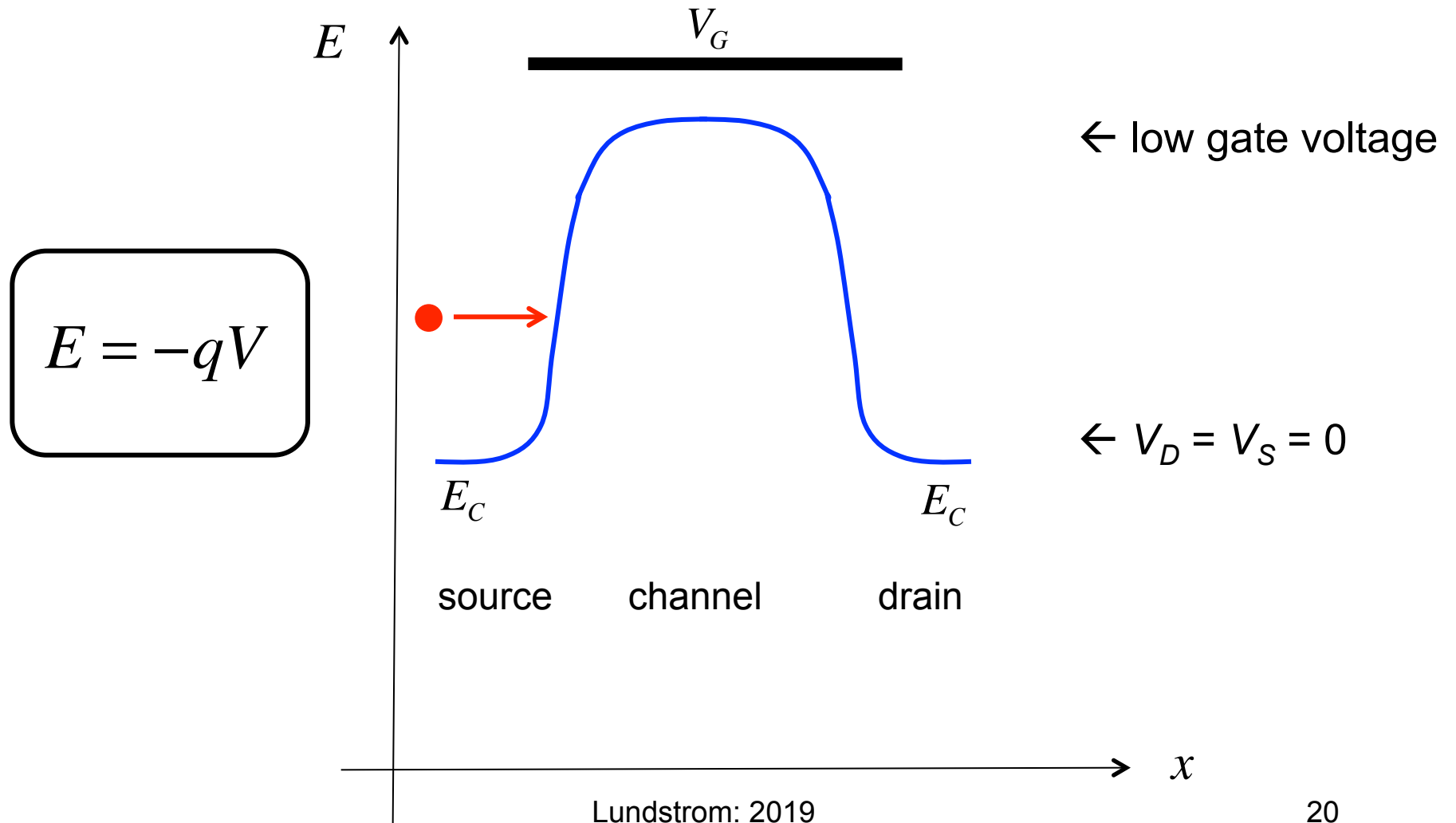
To understand this device, we should first draw an ***Energy Band Diagram*** from the source, across the channel, and out the drain.

Equilibrium energy band diagram

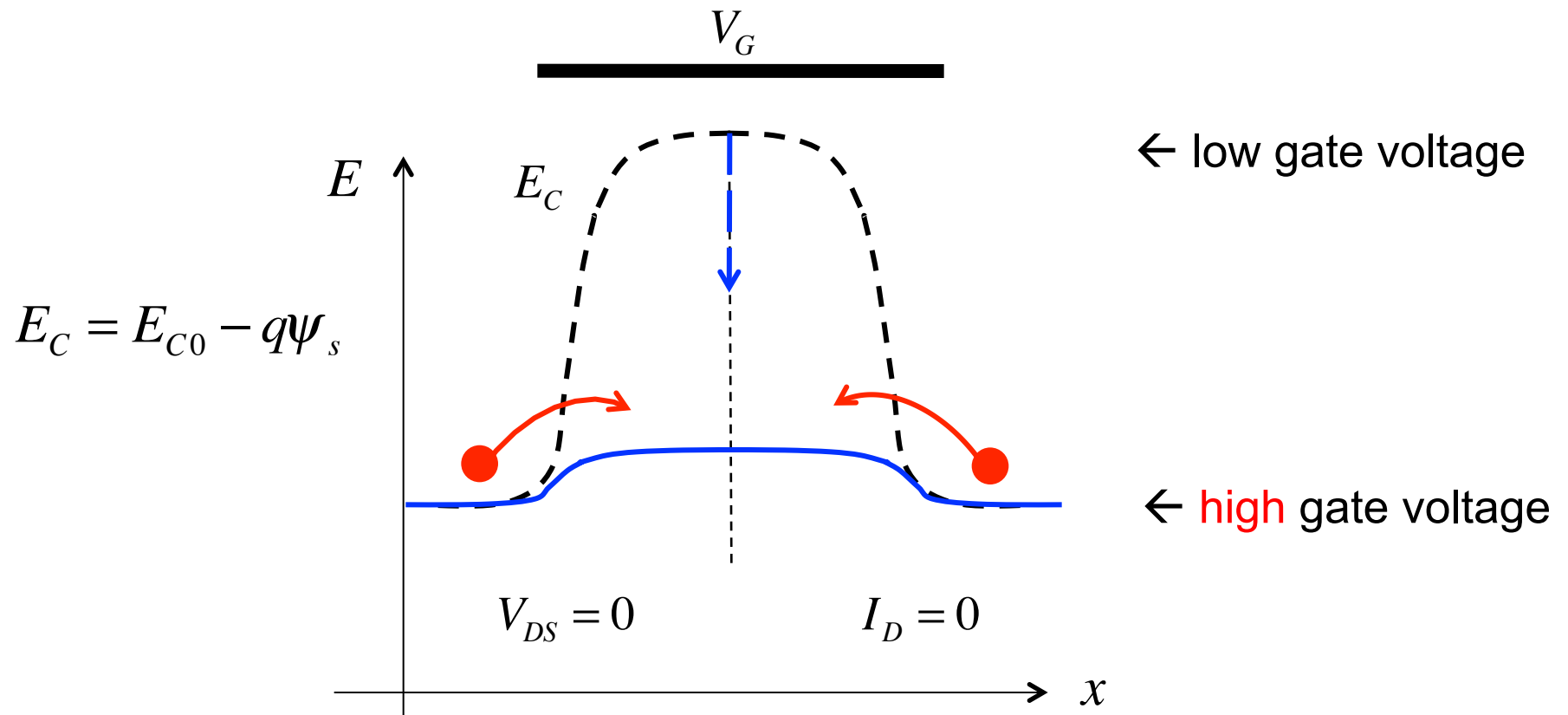
Now, what effect does a gate voltage have?



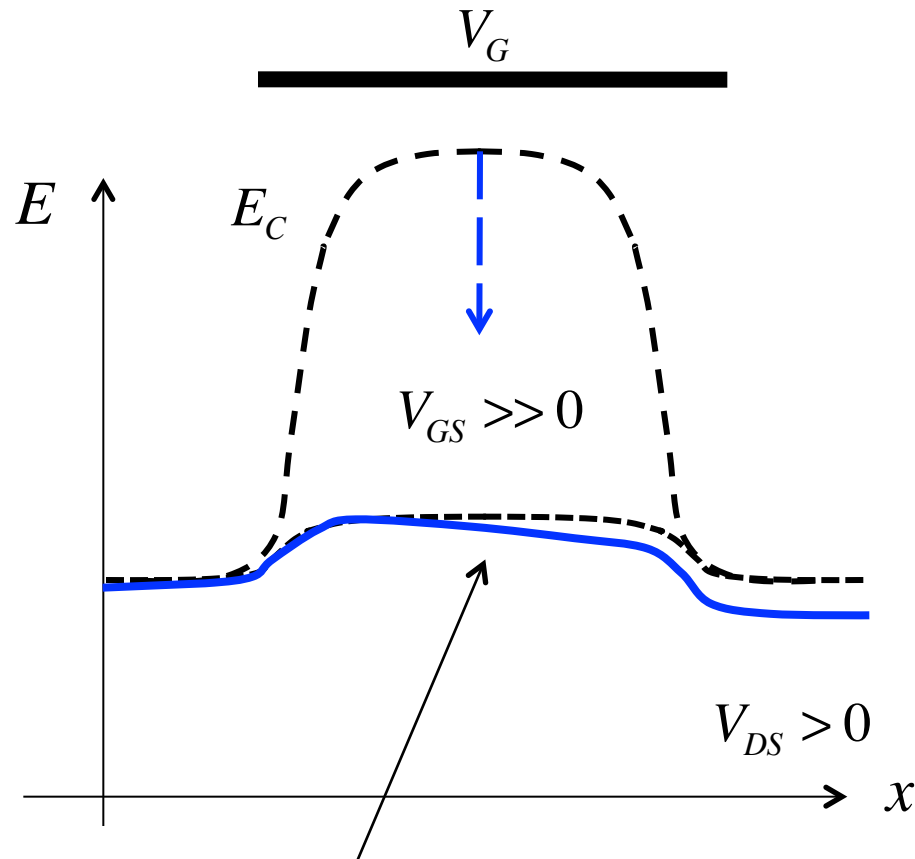
The MOSFET as a barrier controlled device



Examine effect of gate voltage first



Now add a small drain voltage

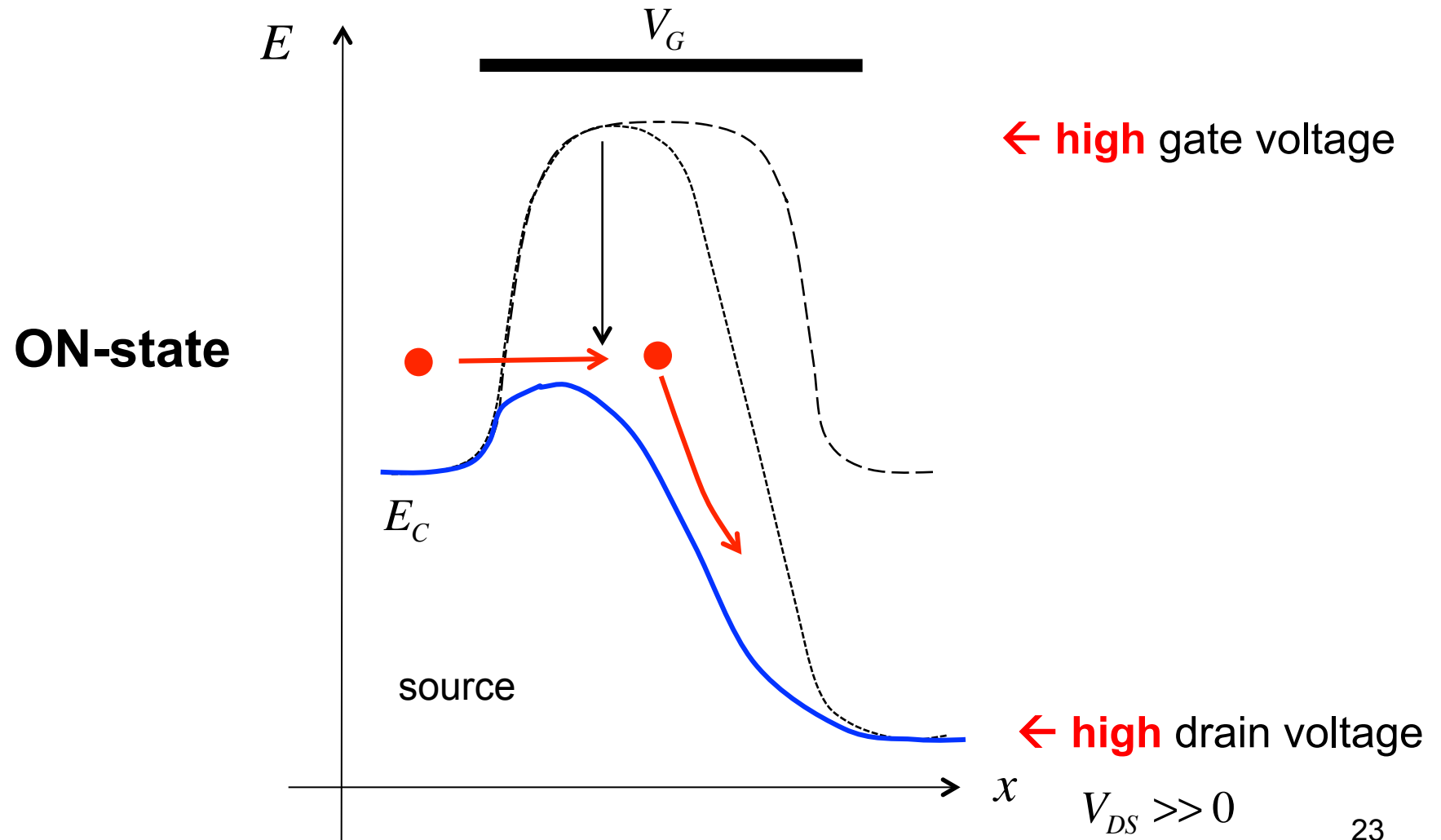


What if we apply a small positive voltage to the drain?

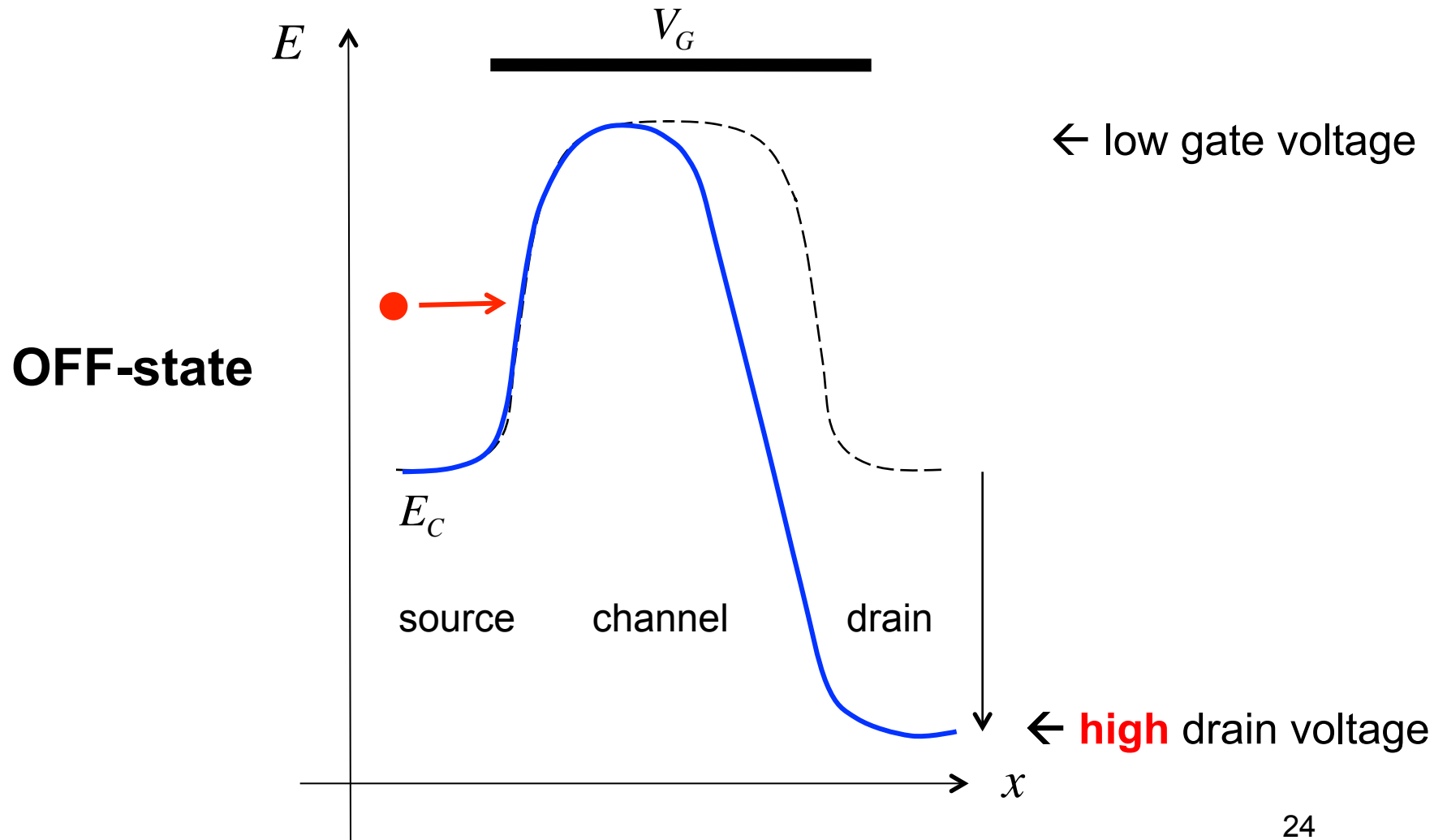
The conduction band in the drain is lowered.

constant electric field and substantial electron density

Now increase the drain voltage

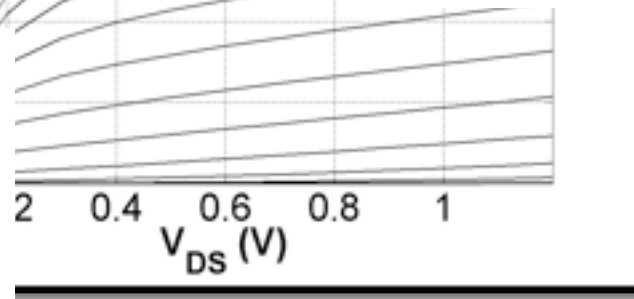
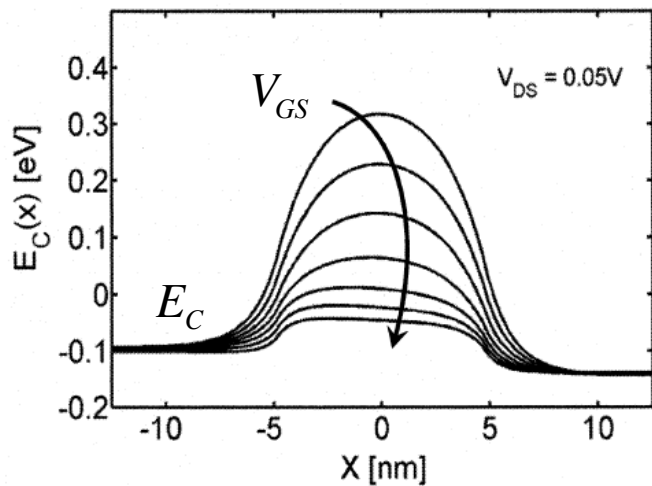
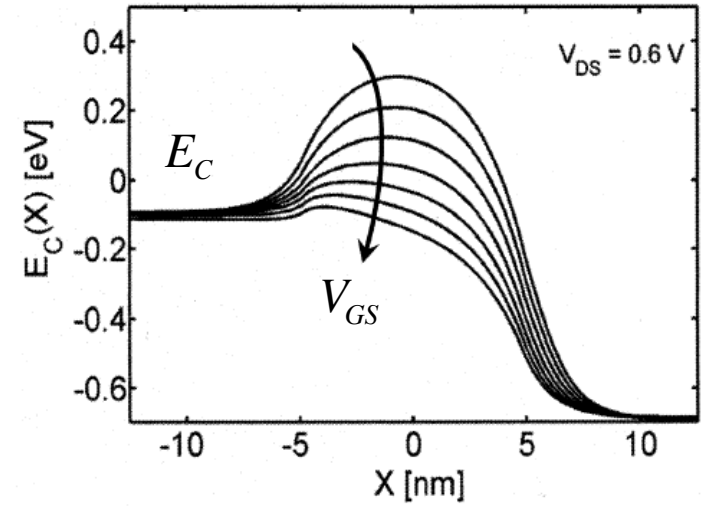
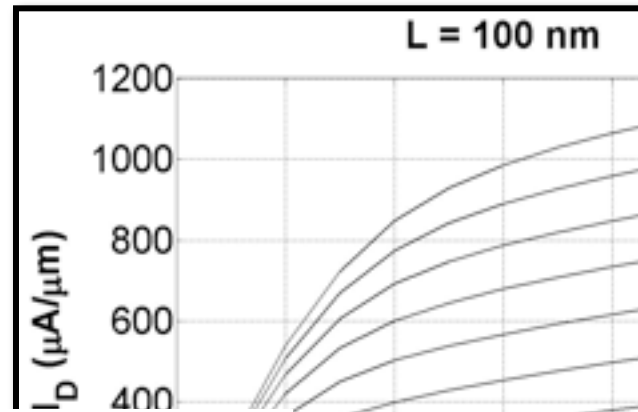


Now remove the gate voltage



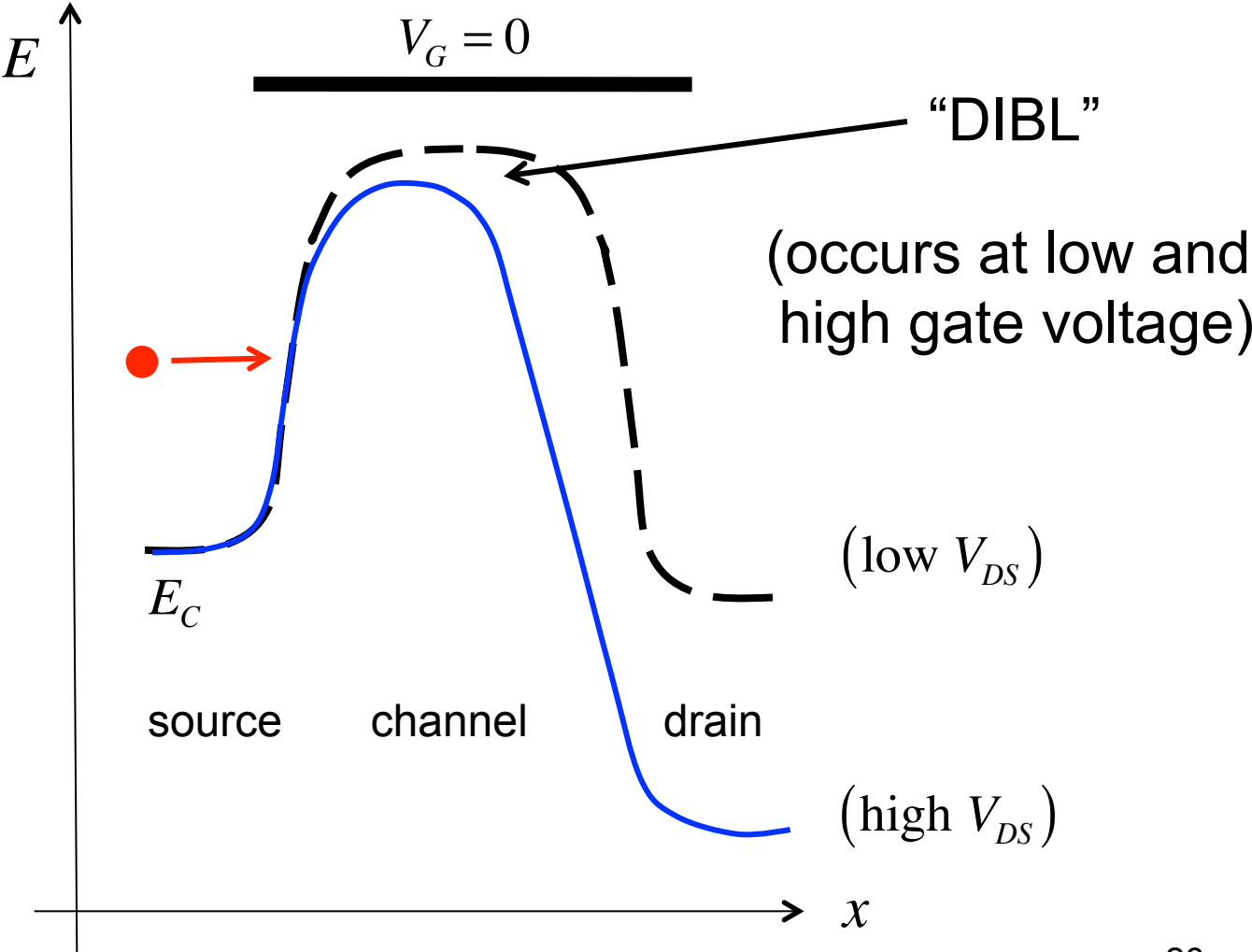
How MOSFETs work

2007 N-MOSFET



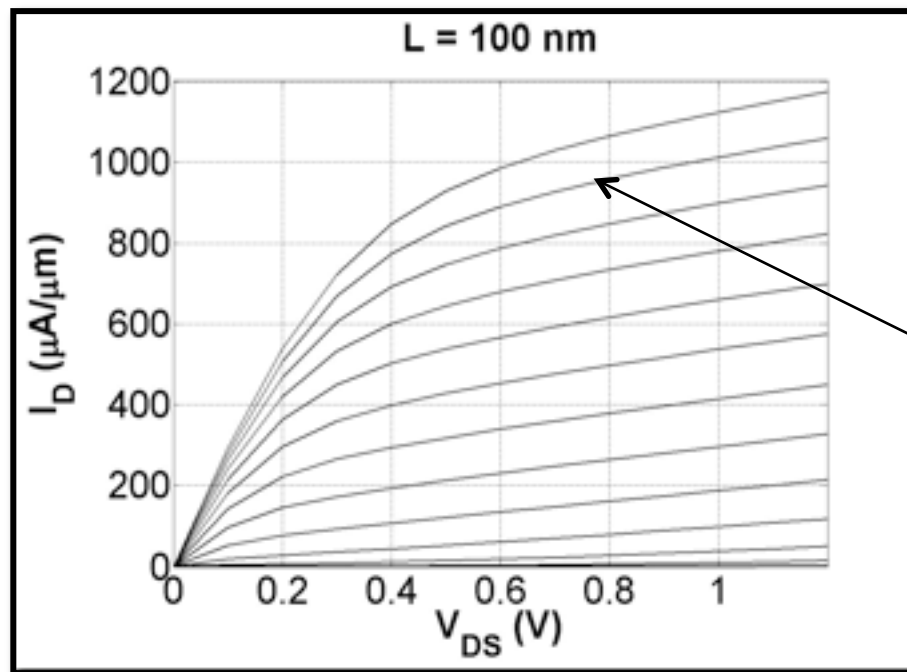
tesy, Shuji Ikeda, ATDF, Dec. 2007)

Effect of drain voltage



Output conductance

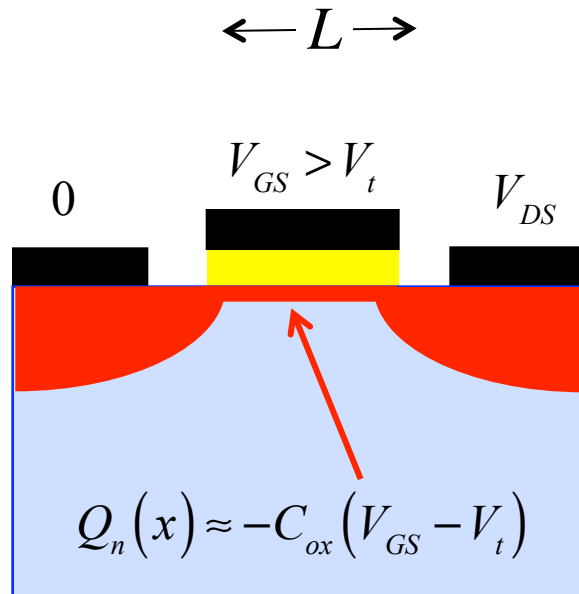
2007 N-MOSFET



DIBL explains the slope in the saturation region

(Courtesy, Shuji Ikeda, ATDF, Dec. 2007)

Mathematical treatment: Small V_{DS}



$$V_{OV} = (V_{GS} - V_t)$$

Sedra and Smith

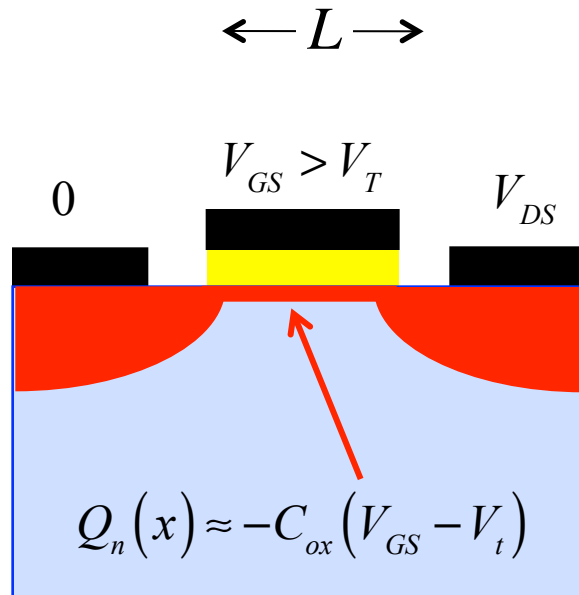
$$Q = CV$$

$$C = \frac{\kappa_{ox} \epsilon_0}{t_{ox}} A = \frac{\epsilon_{ox}}{t_{ox}} A \quad \text{F}$$

$$C_{ox} = \frac{C}{A} = \frac{\kappa_{ox} \epsilon_0}{t_{ox}} \quad \frac{\text{F}}{\text{cm}^2}$$

$$Q_n(x) \approx -C_{ox}(V_{GS} - V_t) \frac{C}{\text{cm}^2}$$

Mathematical treatment: Small V_{DS}

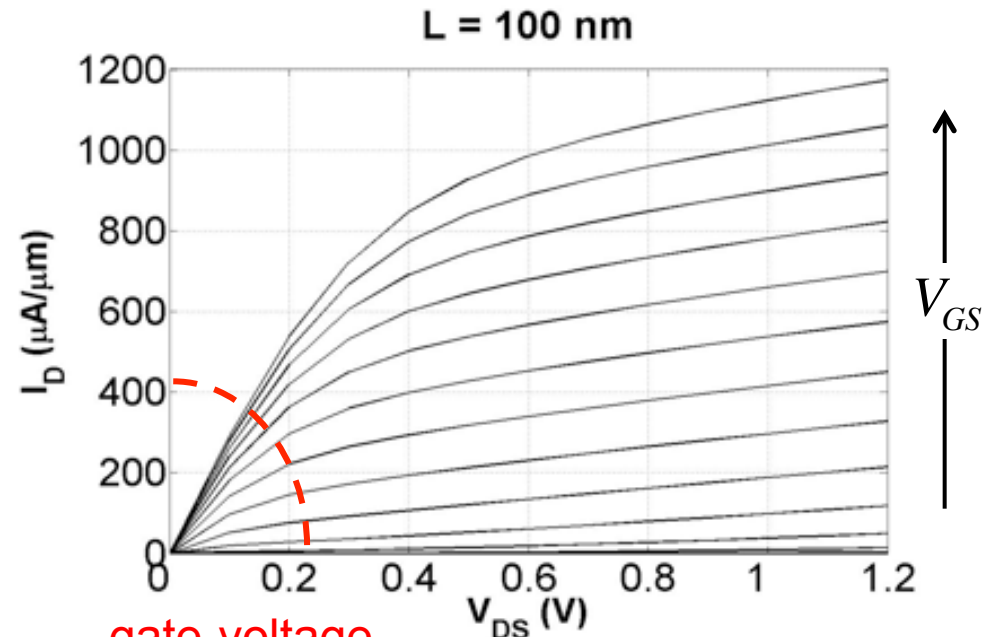


$$I_D = -W Q_n(x) \langle v_x(x) \rangle$$

$$Q_n = -C_{ox}(V_{GS} - V_t)$$

$$\langle v_x(x) \rangle = -\mu_n \mathcal{E}_x$$

$$\mathcal{E}_x = -V_{DS}/L$$



gate-voltage
controlled
resistor

$$I_D = \frac{W}{L} \mu_n C_{ox} (V_{GS} - V_t) V_{DS}$$



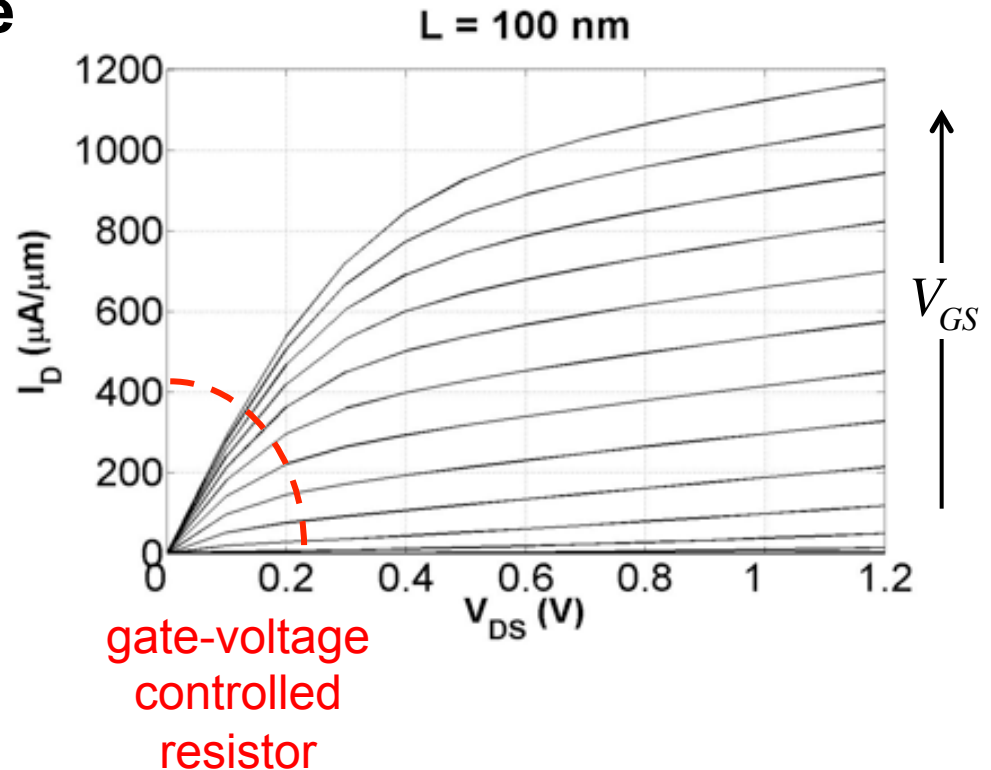
The transconductance parameter

MOSFET transconductance parameter:

$$k_n = \frac{W}{L} \mu_n C_{ox}$$

$$k'_n = \mu_n C_{ox}$$

$$k_n = k'_n \frac{W}{L}$$

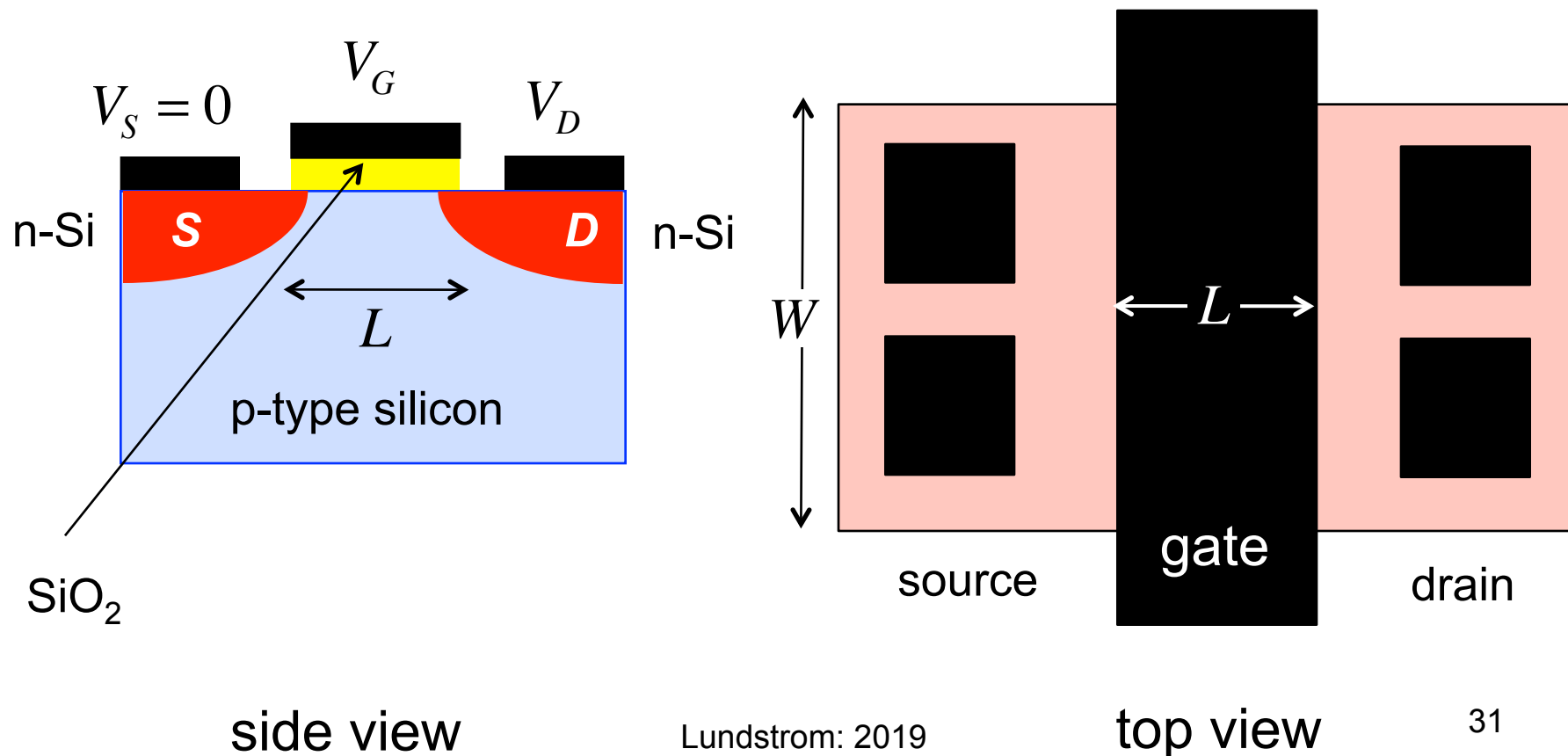


MOSFET aspect ratio: W/L

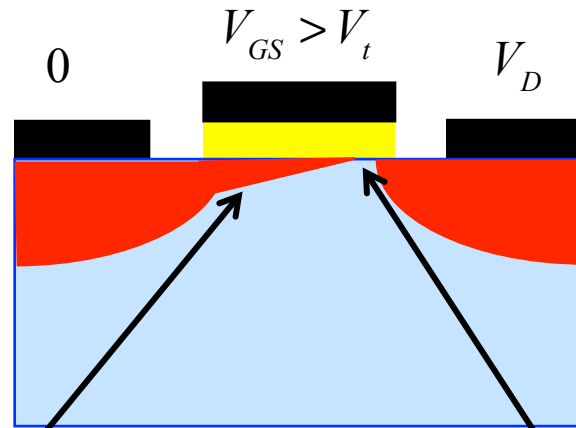
$$I_D = \frac{W}{L} k'_n (V_{GS} - V_t) V_{DS}$$

Aspect ratio

Metal Oxide Semiconductor Field Effect Transistor



Mathematical treatment: Large V_{DS}



“pinch-off”

$$Q_n(x) = -C_{ox} (V_{GS} - V_t - V(x))$$

$$0 \leq V(x) \leq V_{GS} - V_t$$

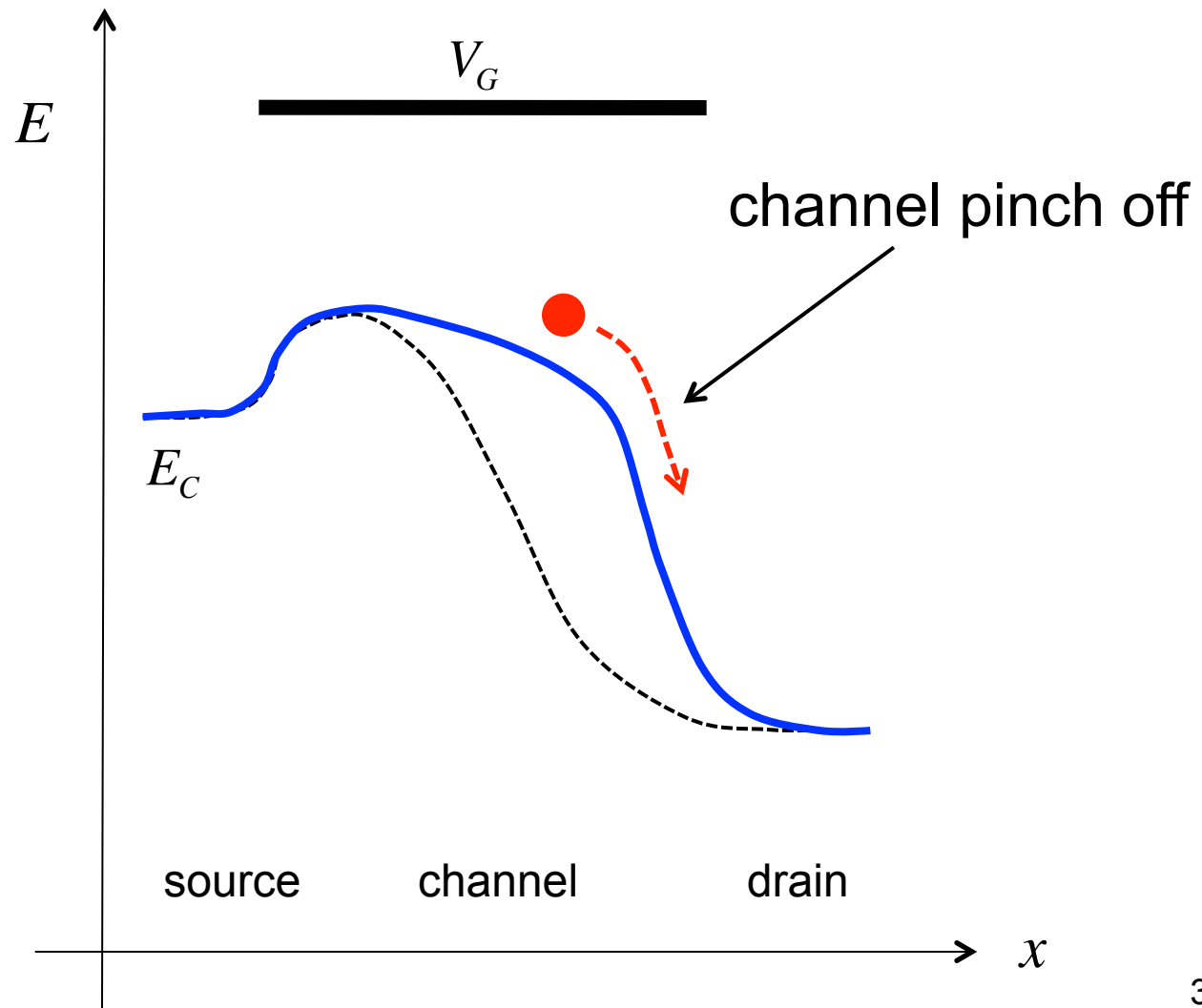
$$V(x_{pinch}) = (V_{GS} - V_t)$$

$$Q_n(x_{pinch}) \approx 0$$

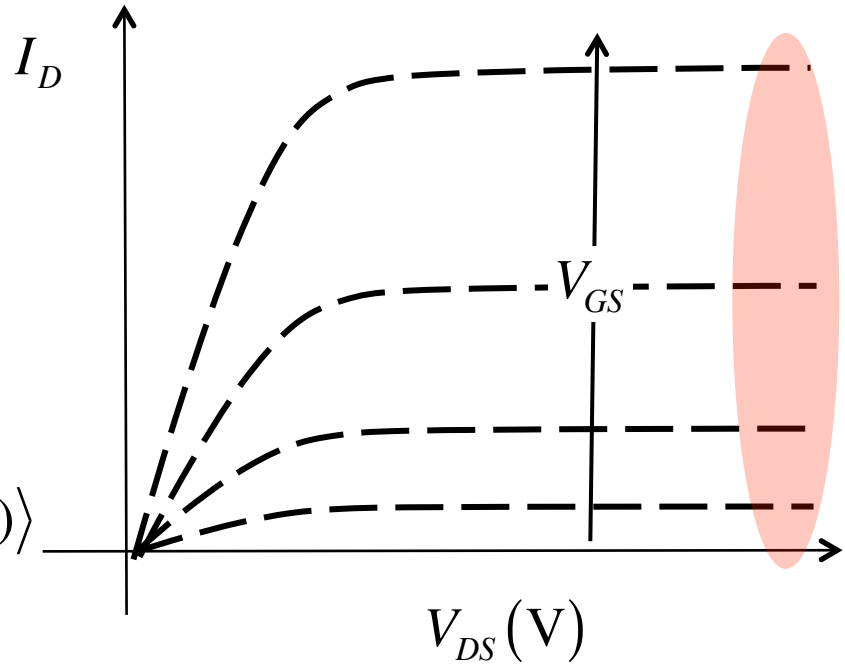
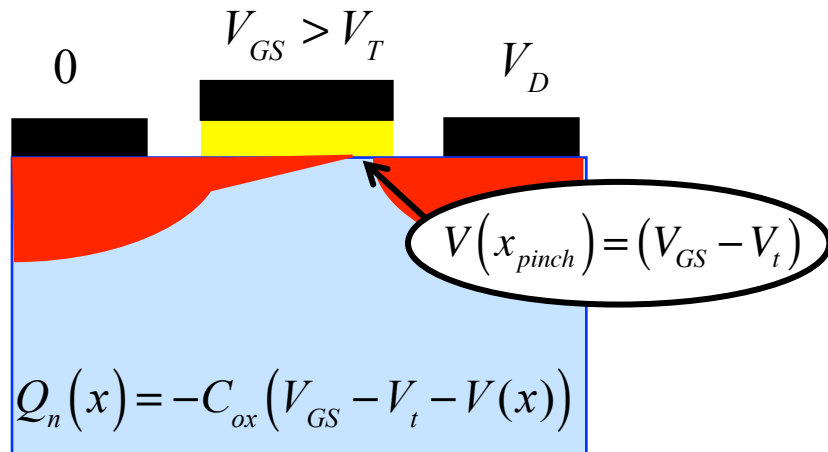
Note: thickness of channel illustrates the areal density of electrons – not the actual thickness.

Electric field is very large in the pinch-off region.

“Pinch off” on an energy band diagram



Square law MOSFET



$$I_D = -W Q_n(x) \langle v_x(x) \rangle = W Q_n(0) \langle v_x(0) \rangle$$

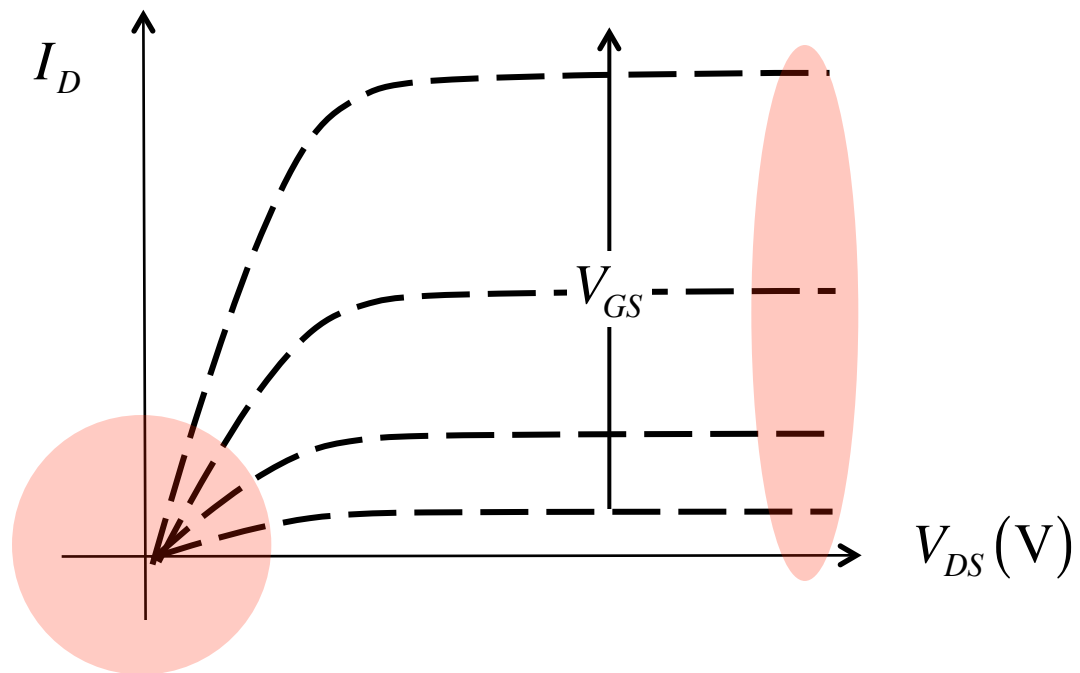
$$Q_n(0) = -C_{ox} (V_{GS} - V_t)$$

$$\langle v_x(0) \rangle = -\mu_n \mathcal{E}_x(0)$$

$$\mathcal{E}_x(0) \approx -V(x_{pinch})/L = -(V_{GS} - V_t)/L$$

$$I_D = \frac{W}{2L} \mu_n C_{ox} (V_{GS} - V_t)^2$$

Square law MOSFET



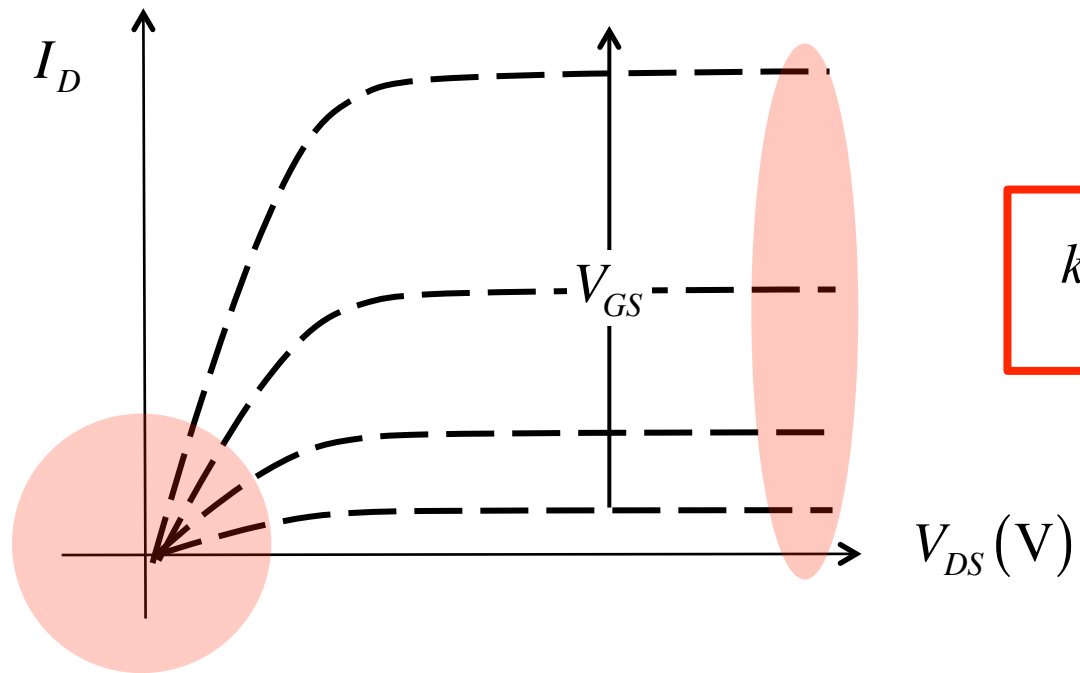
$$I_D = \frac{W}{L} \mu_n C_{ox} (V_{GS} - V_t) V_{DS}$$

$$V_{GS} > V_t \quad V_{DS} \ll V_{GS} - V_t$$

$$I_D = \frac{W}{L} \mu_n C_{ox} (V_{GS} - V_t)^2$$

$$V_{GS} > V_t \quad V_{DS} > V_{GS} - V_t$$

Square law MOSFET (technology constant)



$$I_D = \frac{W}{L} k'_n (V_{GS} - V_t) V_{DS}$$

$$V_{GS} > V_t \quad V_{DS} \ll V_{GS} - V_t$$

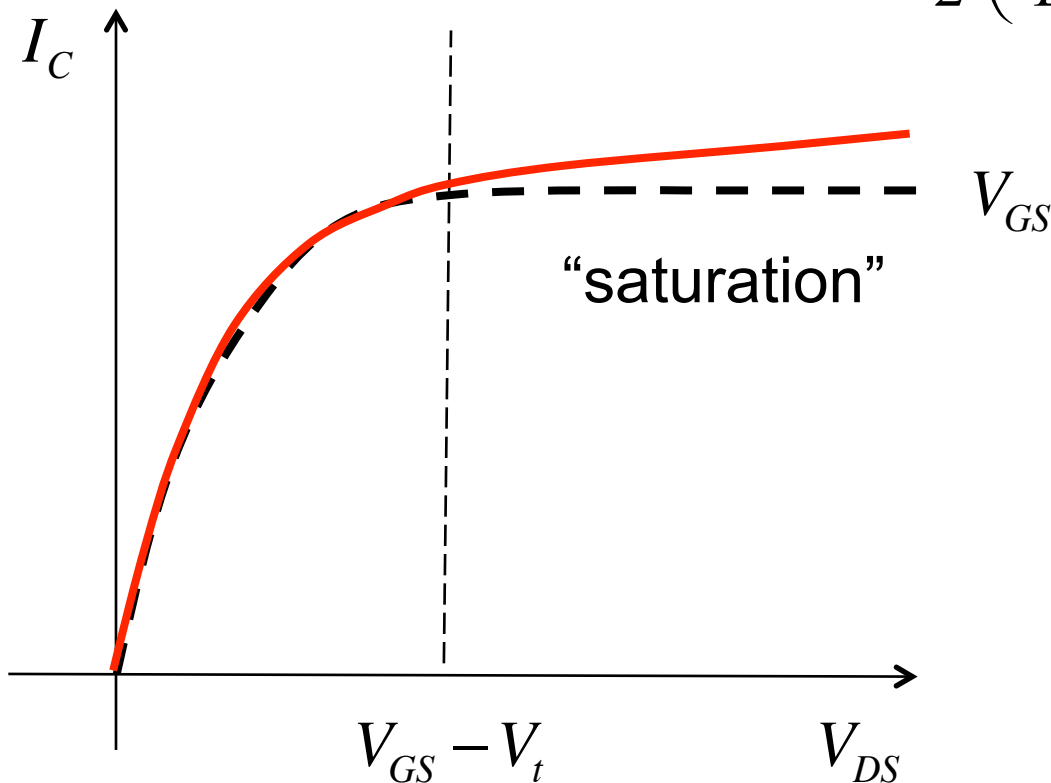
$$I_D = \frac{W}{L} \frac{k'_n}{2} (V_{GS} - V_t)^2$$

$$V_{GS} > V_t \quad V_{DS} > V_{GS} - V_t$$

Output resistance

$$I_D = \frac{k'_n}{2} \left(\frac{W}{L} \right) (V_{GS} - V_t)^2 (1 + \lambda V_{DS})$$

$$\lambda = \frac{1}{V_A}$$

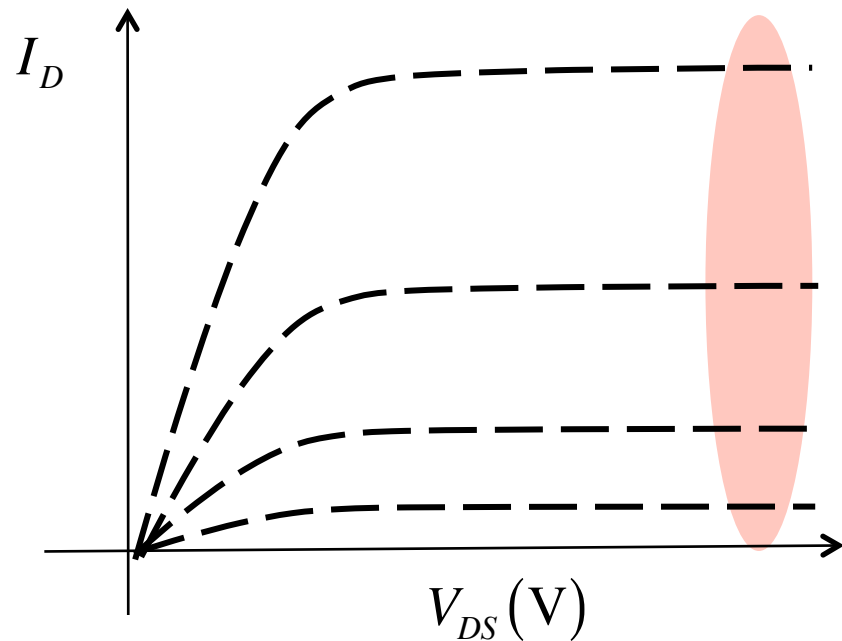


Like the Early voltage for a BJT.

Long vs. Short Channel MOSFETs

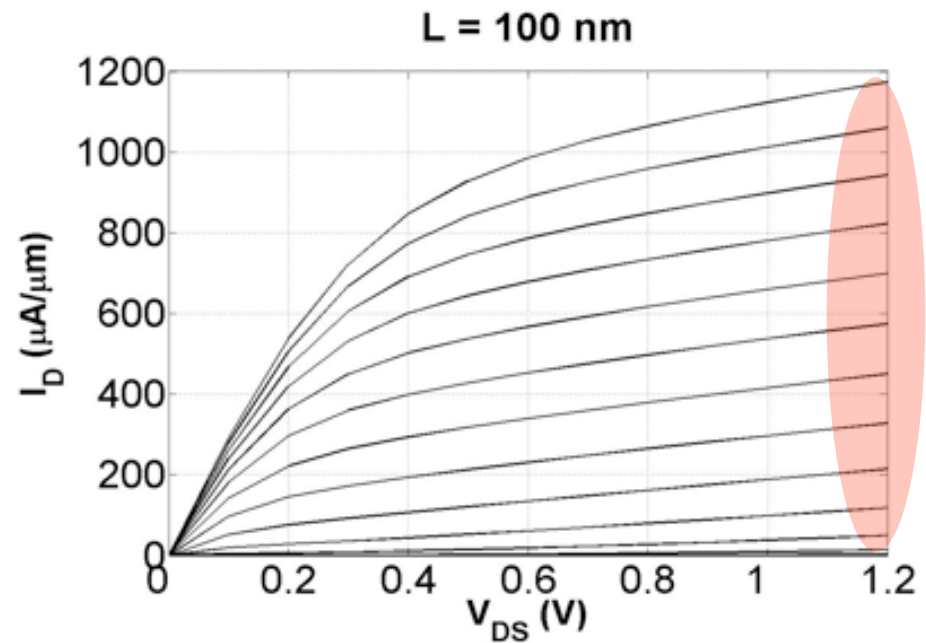
Square Law

$$I_{DSAT} \propto (V_{GS} - V_t)^2$$

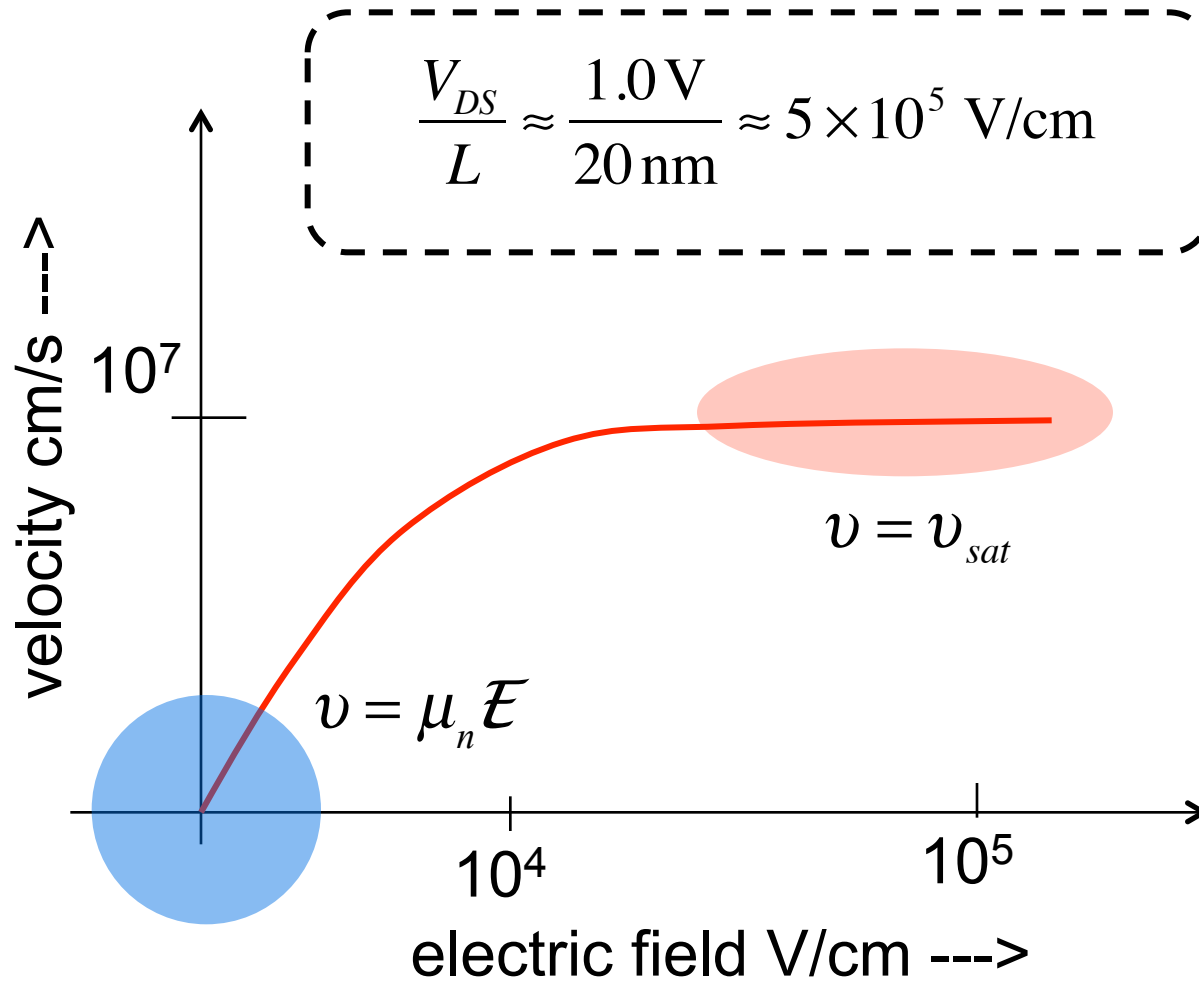


“Velocity saturated”

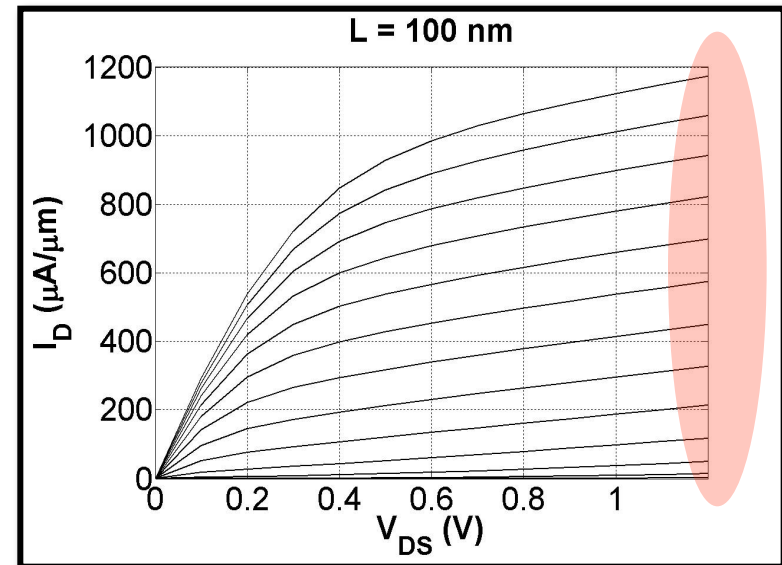
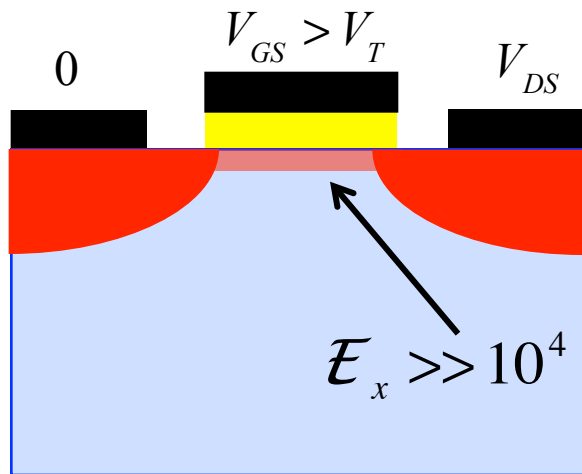
$$I_{DSAT} \propto (V_{GS} - V_t)$$



High V_{DS} : Velocity saturation



MOSFET IV: velocity saturation



(Courtesy, Shuji Ikeda, ATDF, Dec. 2007)

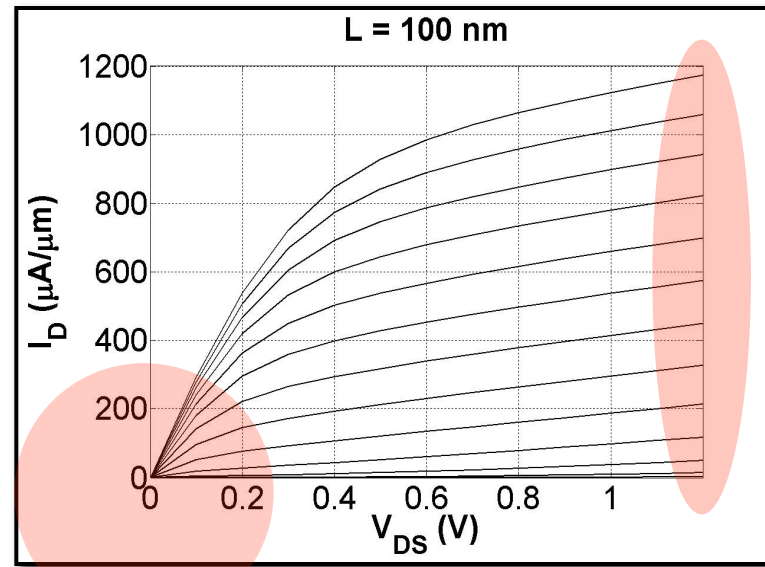
$$I_D = -W Q_n(x) \langle v_x(x) \rangle$$

$$Q_n = -C_{ox} (V_{GS} - V_t)$$

$$\langle v_x \rangle = v_{sat}$$

$$I_D = W C_{ox} v_{sat} (V_{GS} - V_t) \quad \checkmark$$

The velocity saturated MOSFET



(Courtesy, Shuji Ikeda, ATDF, Dec. 2007)

$$I_D = \frac{W}{L} \mu_n C_{ox} (V_{GS} - V_t) V_{DS}$$

$$V_{GS} > V_t \quad V_{DS} \ll V_{GS} - V_t$$

$$I_D = W C_{ox} v_{sat} (V_{GS} - V_t)$$

$$V_{GS} > V_t \quad V_{DS} > V_{GS} - V_t$$

Summary

A MOSFET is a barrier controlled transistor (just like a BJT).

MOSFETs come in two flavors – N-channel (NPN) and P-channel (PNP).

In the **saturation region**, the gate to source voltage is greater than the **threshold voltage**, and the drain voltage is greater than the **drain saturation voltage**.

In the **linear region**, the gate to source voltage is greater than the **threshold voltage**, and the drain voltage is **much less than** the drain saturation voltage.

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- 2) IV characteristics (descriptive)
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- 4) IV characteristics (mathematical model)

