

Spring 2019 Purdue University

ECE 255: L17

BJT Transistor Amplifiers

(Sedra and Smith, 7th Ed., Sec. 7.1, 7.2.2)

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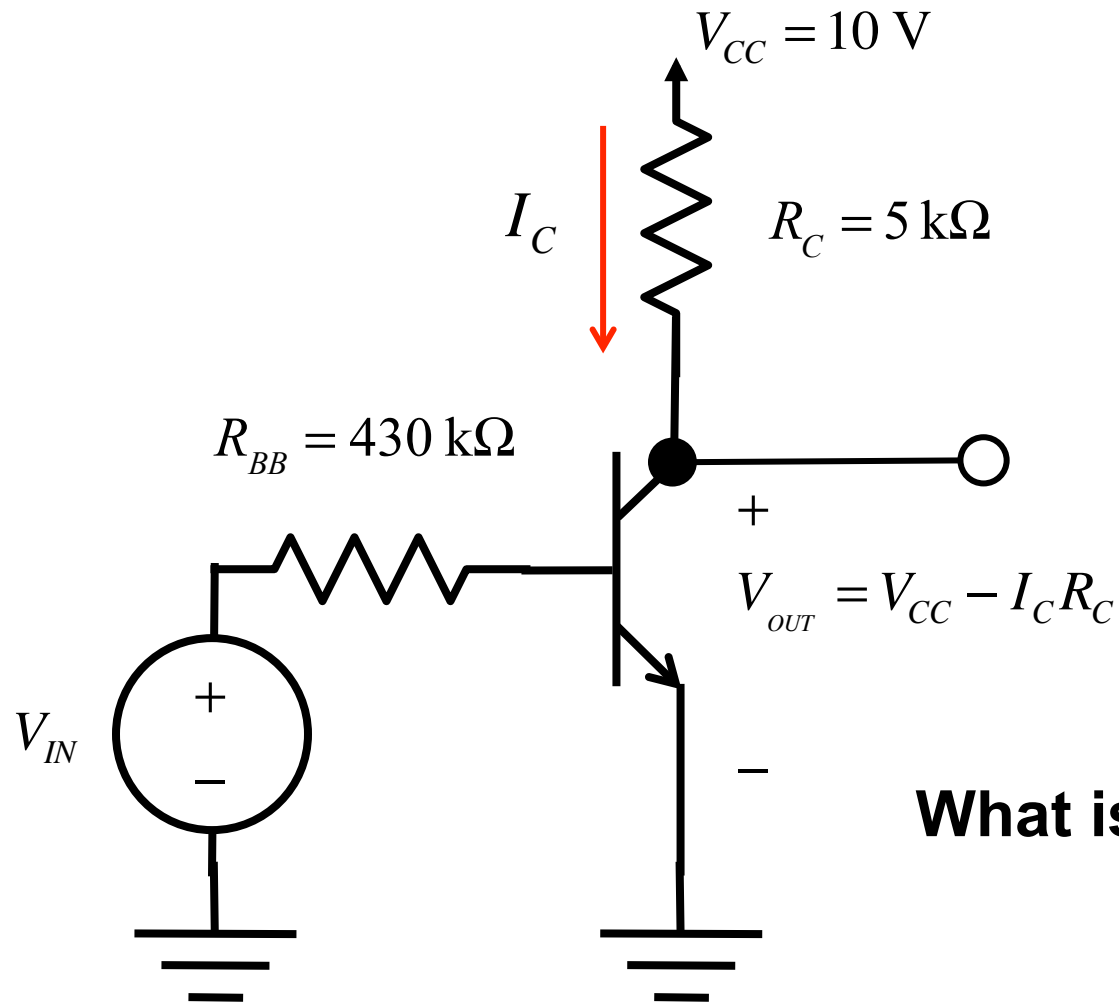
Announcements

- 1) LTspice Project 2 is posted. The due date for it is Wed. Feb. 27th by 5:00PM electronically. You should find your assigned your beta value in your grade book.
- 2) HW6 will be posted today
- 3) Exam 2 in on Tuesday, March 5, 6:30-7:30 PM PHYS 112

Outline

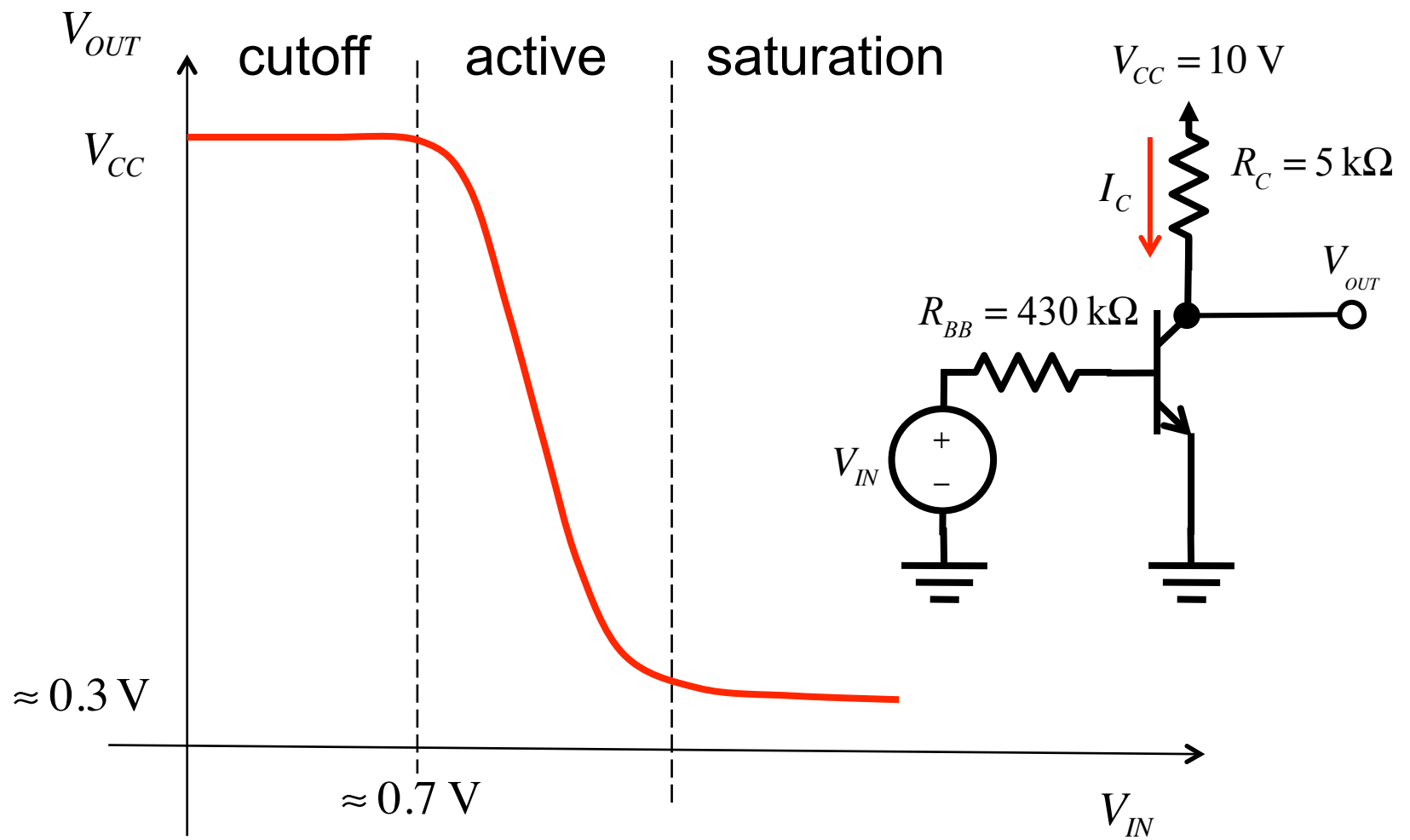
- 1) Voltage transfer characteristic (VTC)
- 2) Small signal model for BJT
- 3) Small signal analysis

Voltage Transfer Characteristic

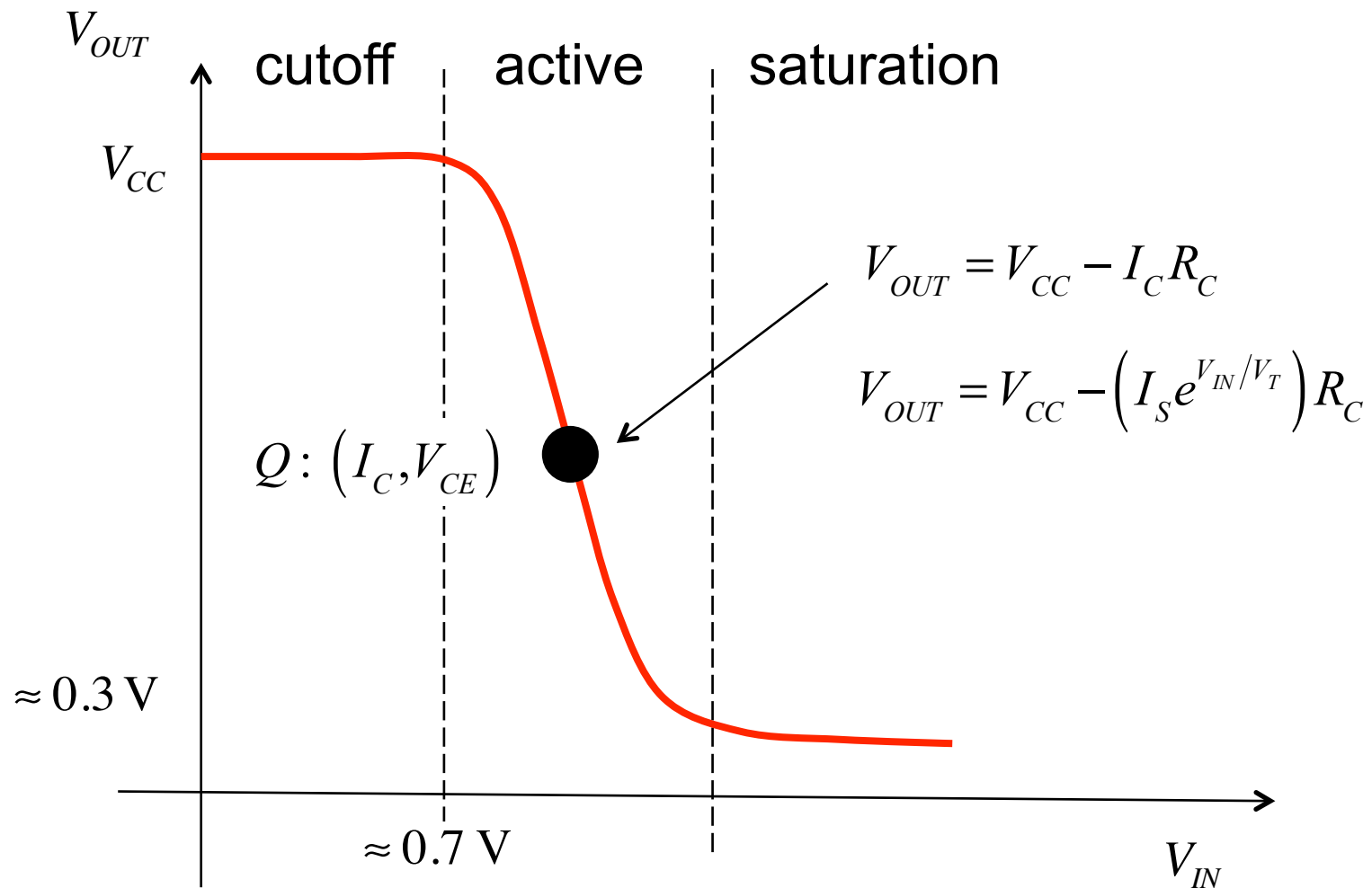


What is V_{OUT} vs. V_{IN} ?

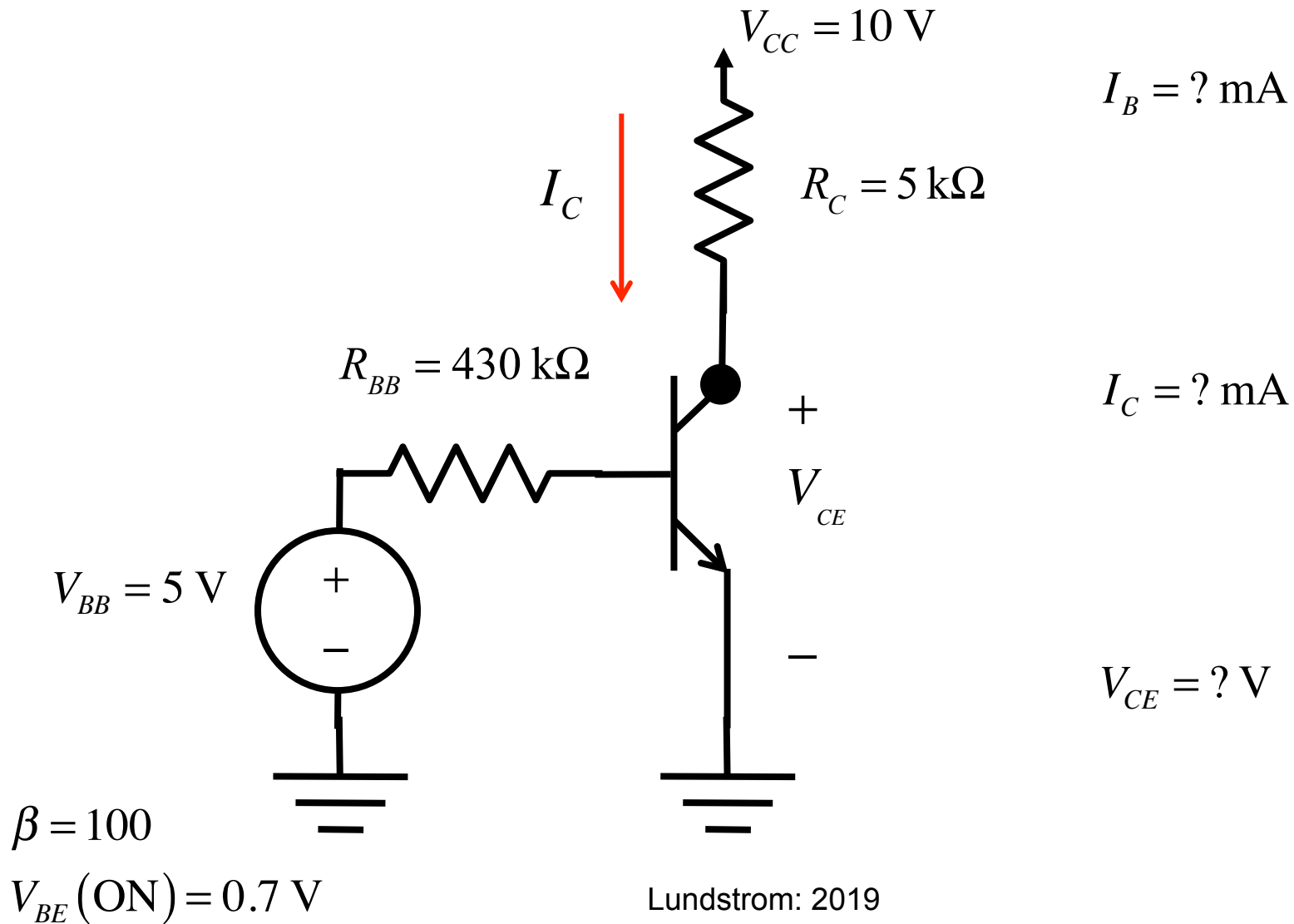
VTC



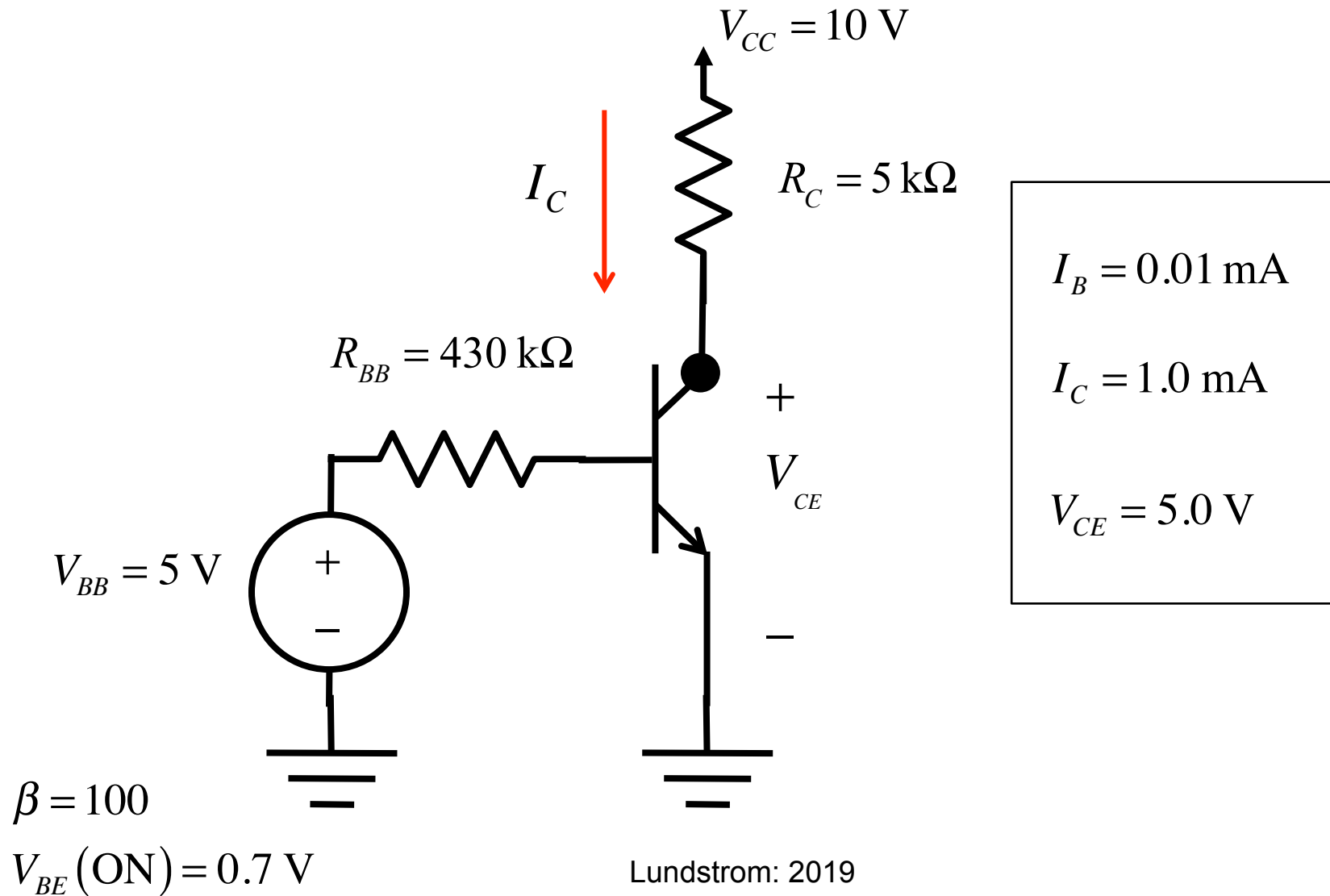
Biasing in the active region



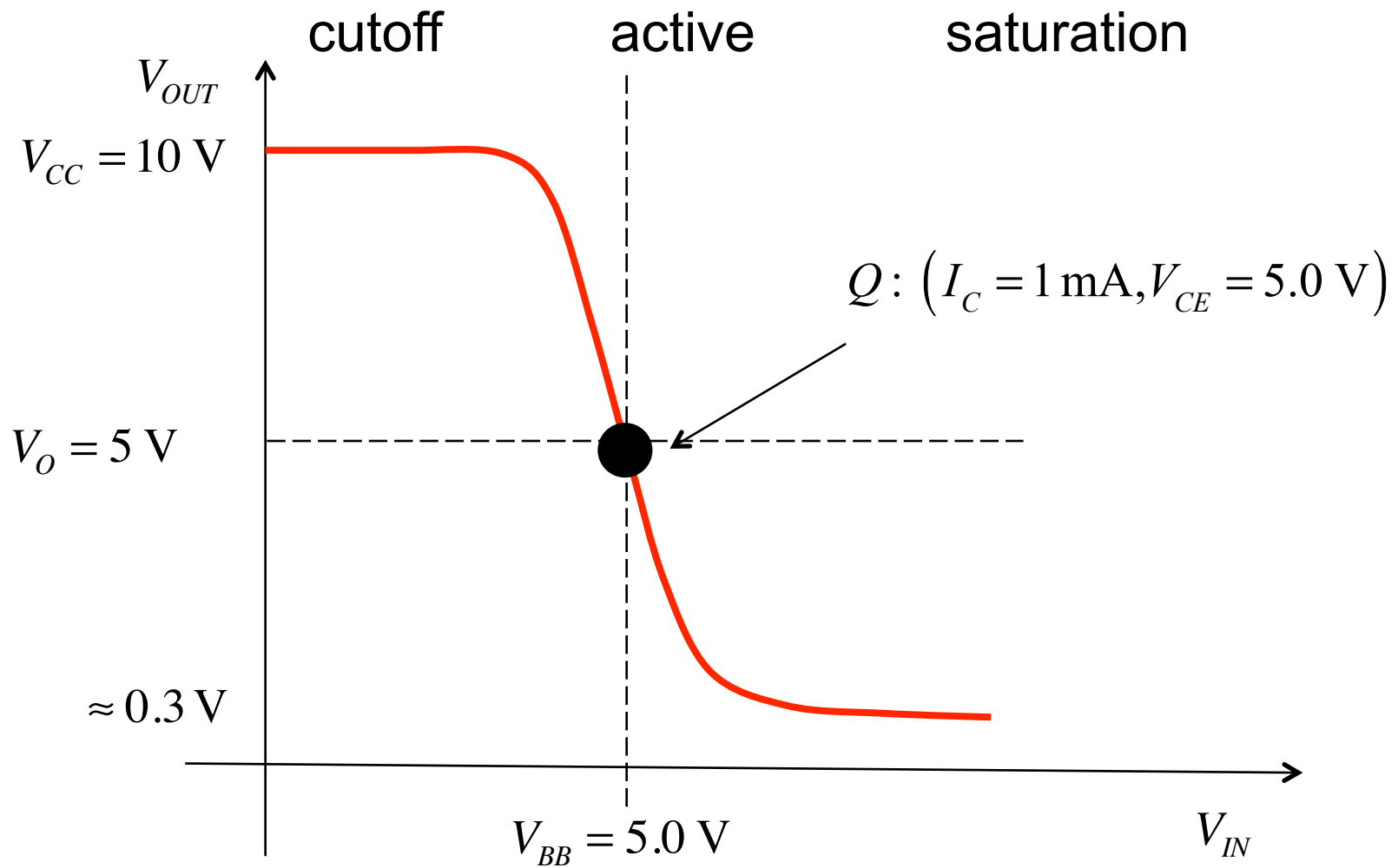
Bias Circuit



Bias Circuit

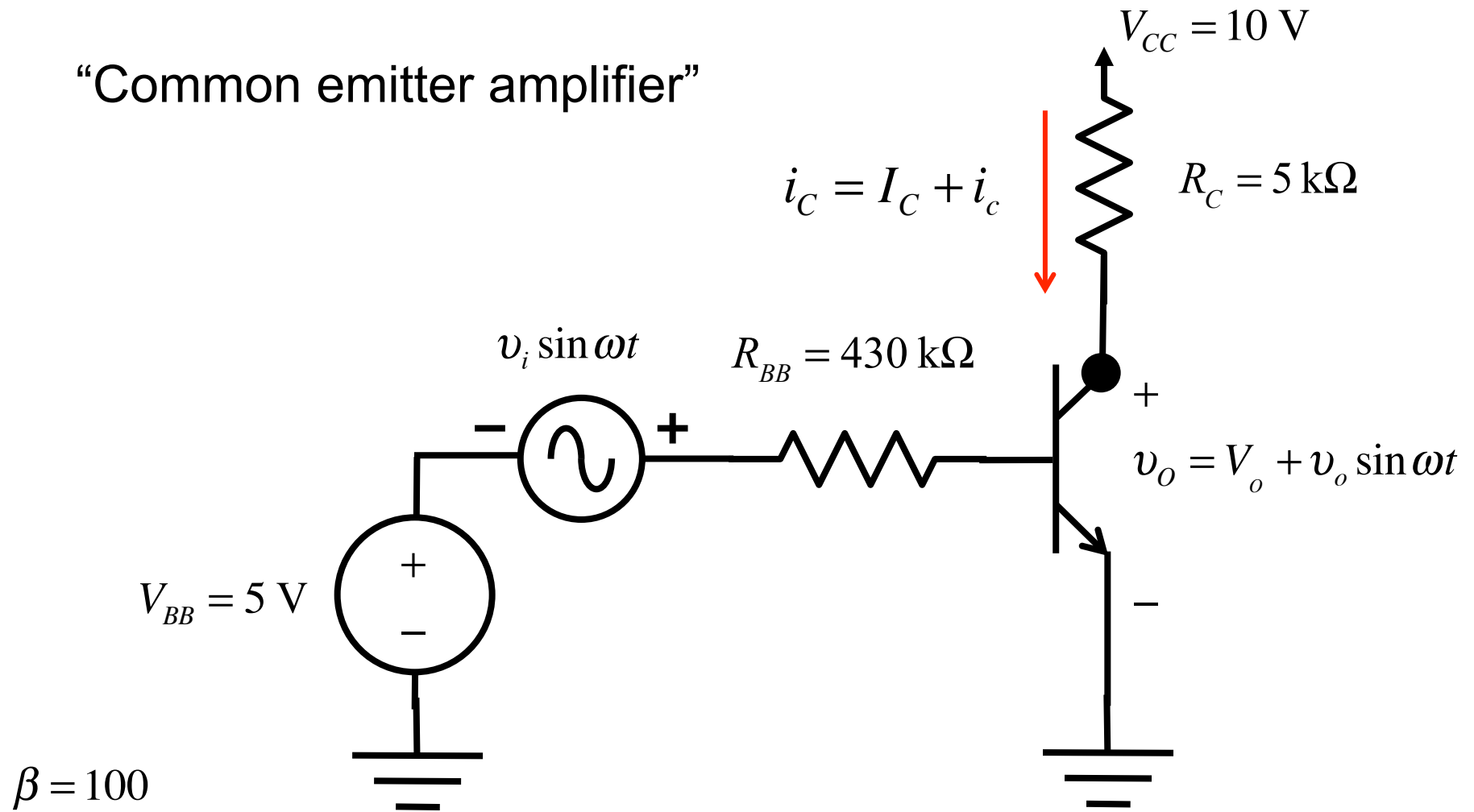


Biasing



Add ac small signal

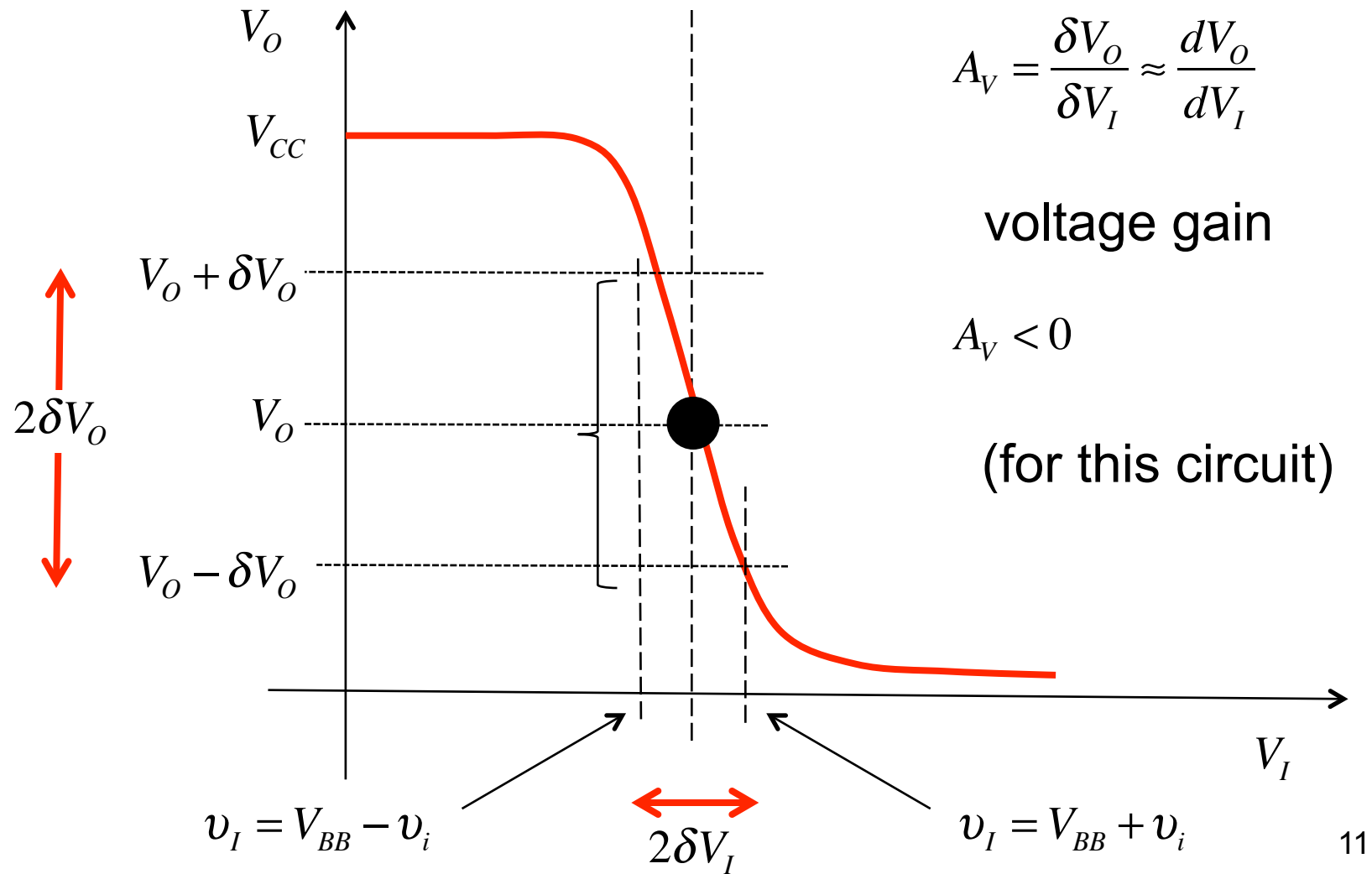
“Common emitter amplifier”



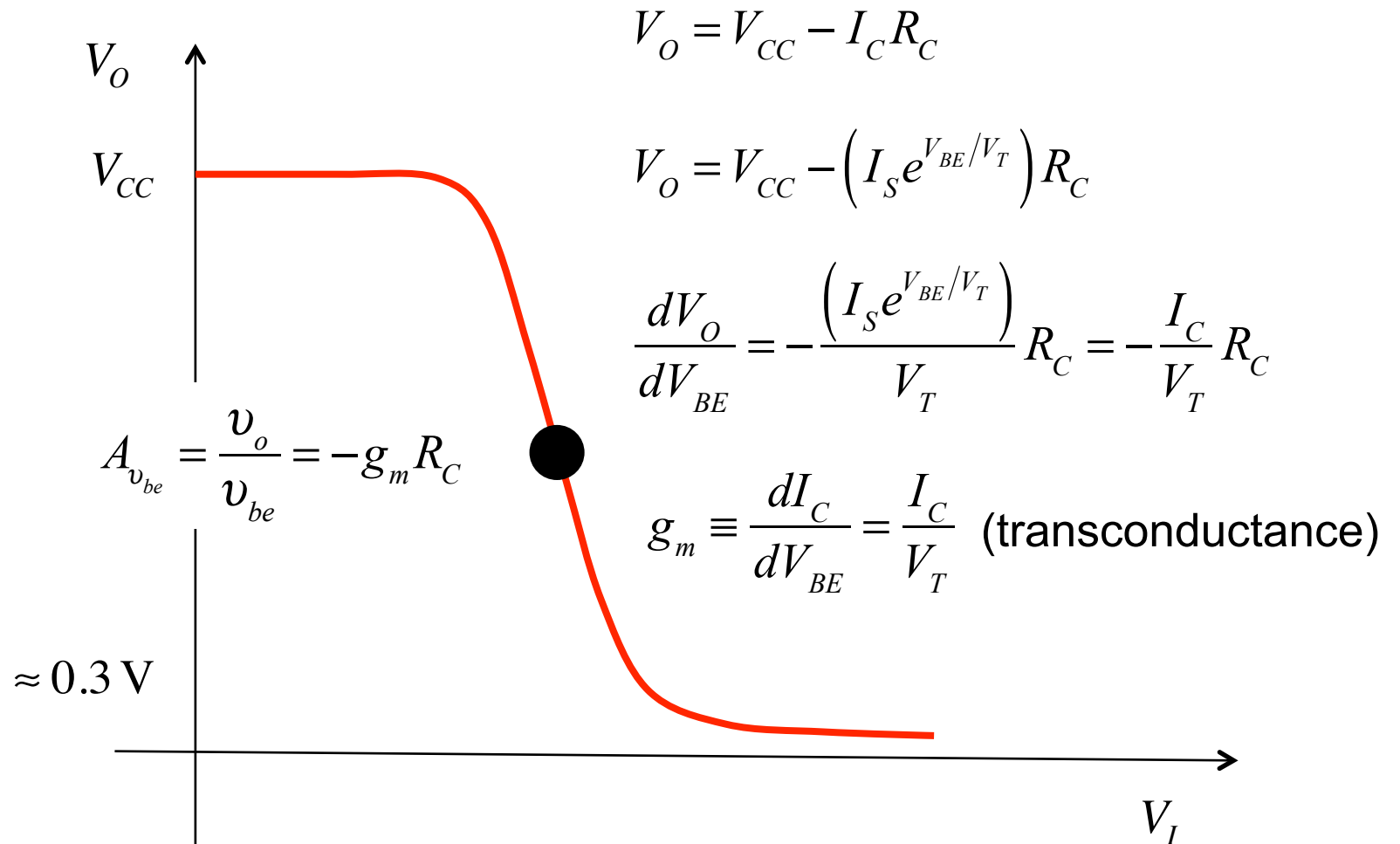
$$\beta = 100$$

$$V_{BE}(\text{ON}) = 0.7\text{ V}$$

Qualitative



Qualitative

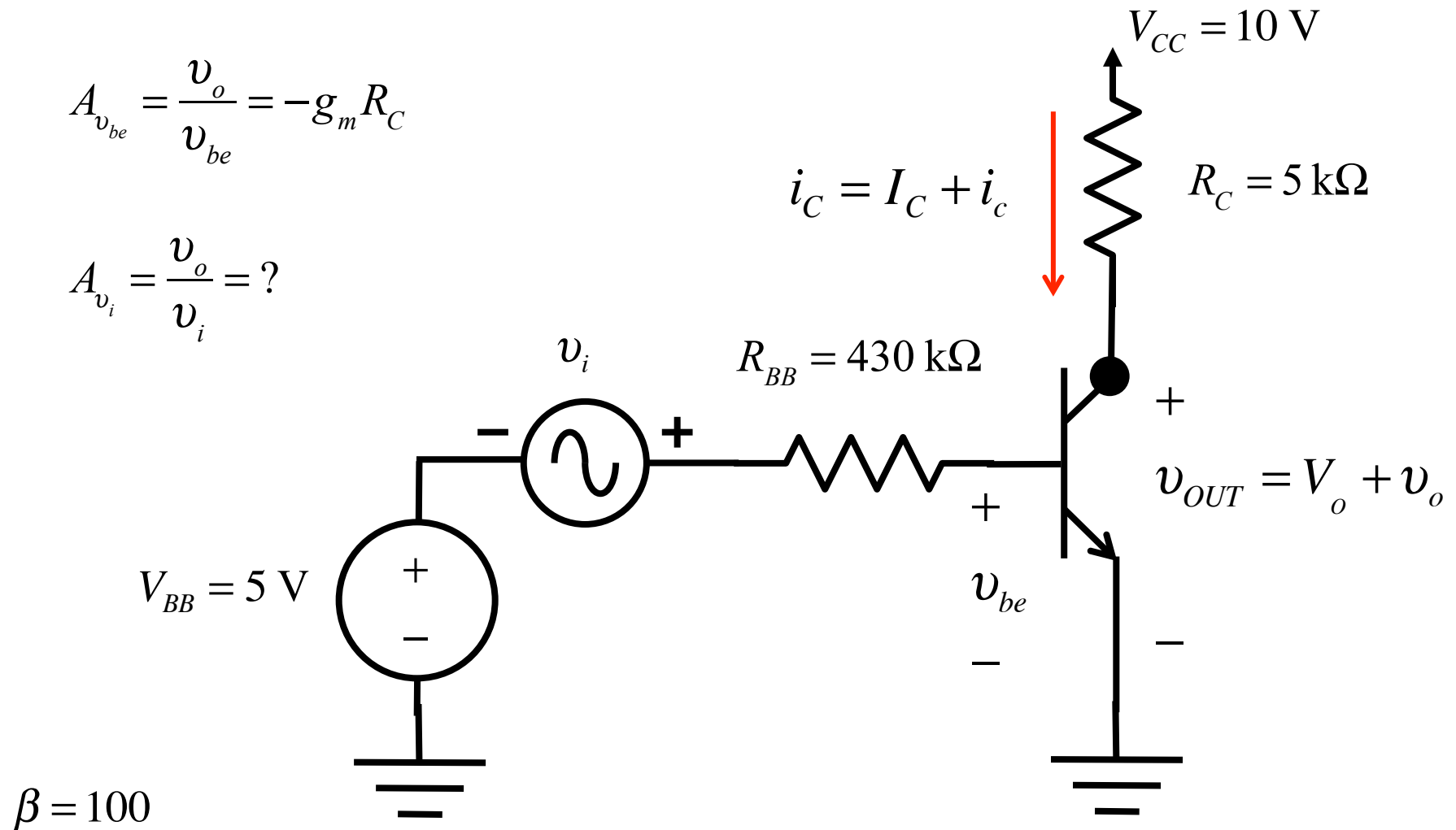


Voltage gain of CE amplifier

$$A_{v_{be}} = \frac{v_o}{v_{be}} = -g_m R_C$$

$$A_{v_i} = \frac{v_o}{v_i} = ?$$

$$i_C = I_C + i_c$$

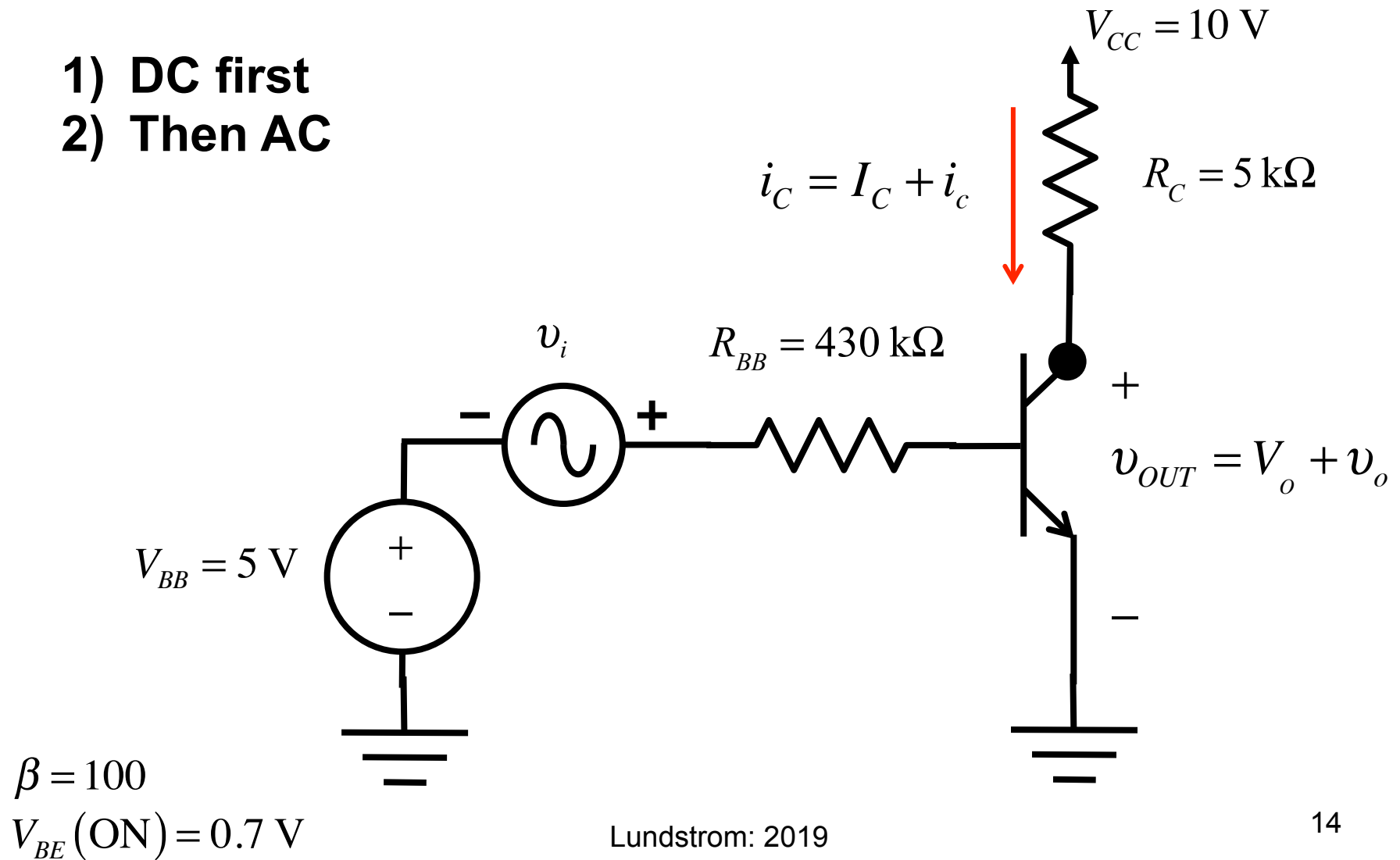


$$\beta = 100$$

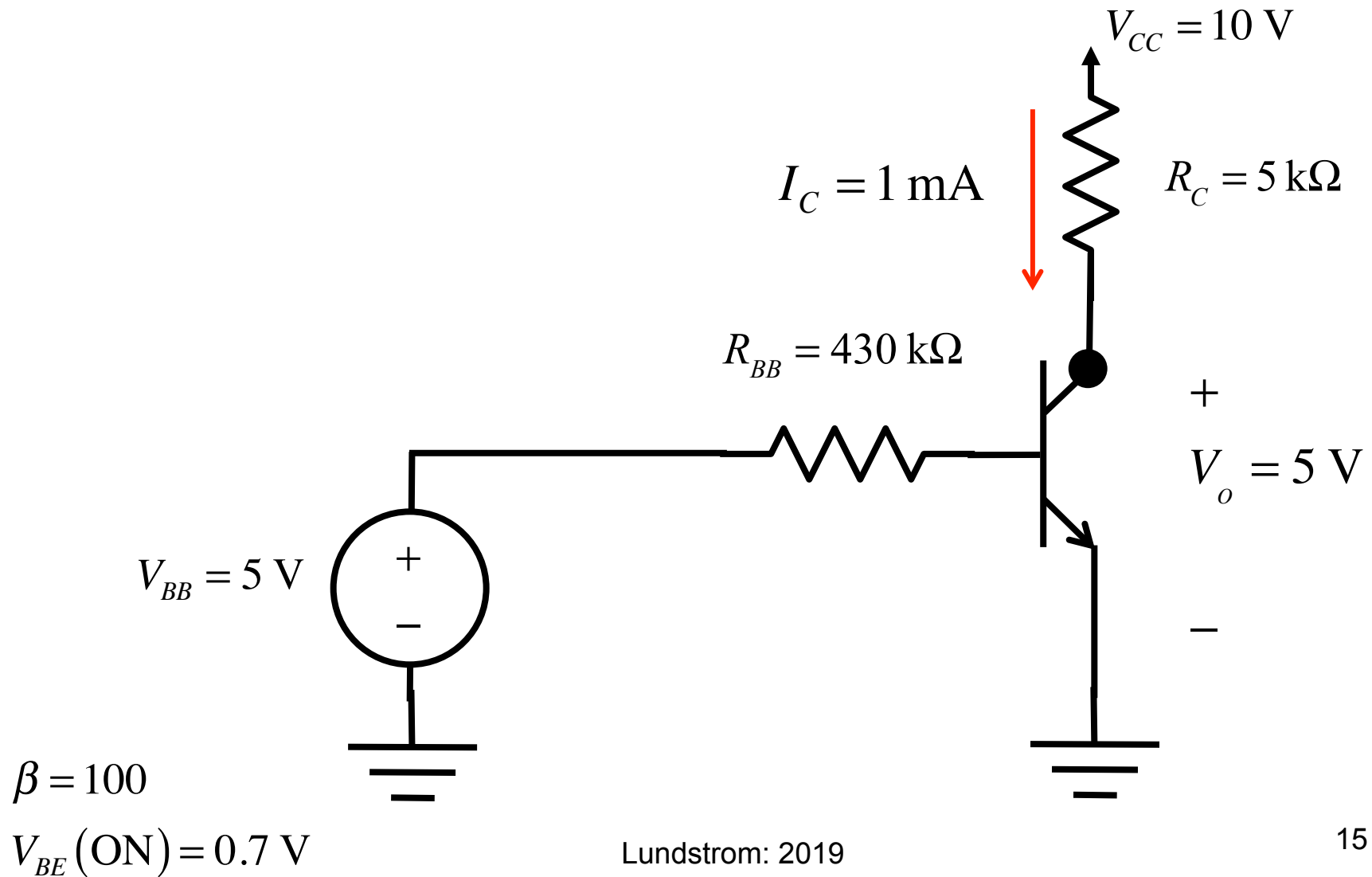
$$V_{BE}(\text{ON}) = 0.7 \text{ V}$$

Analysis by superposition

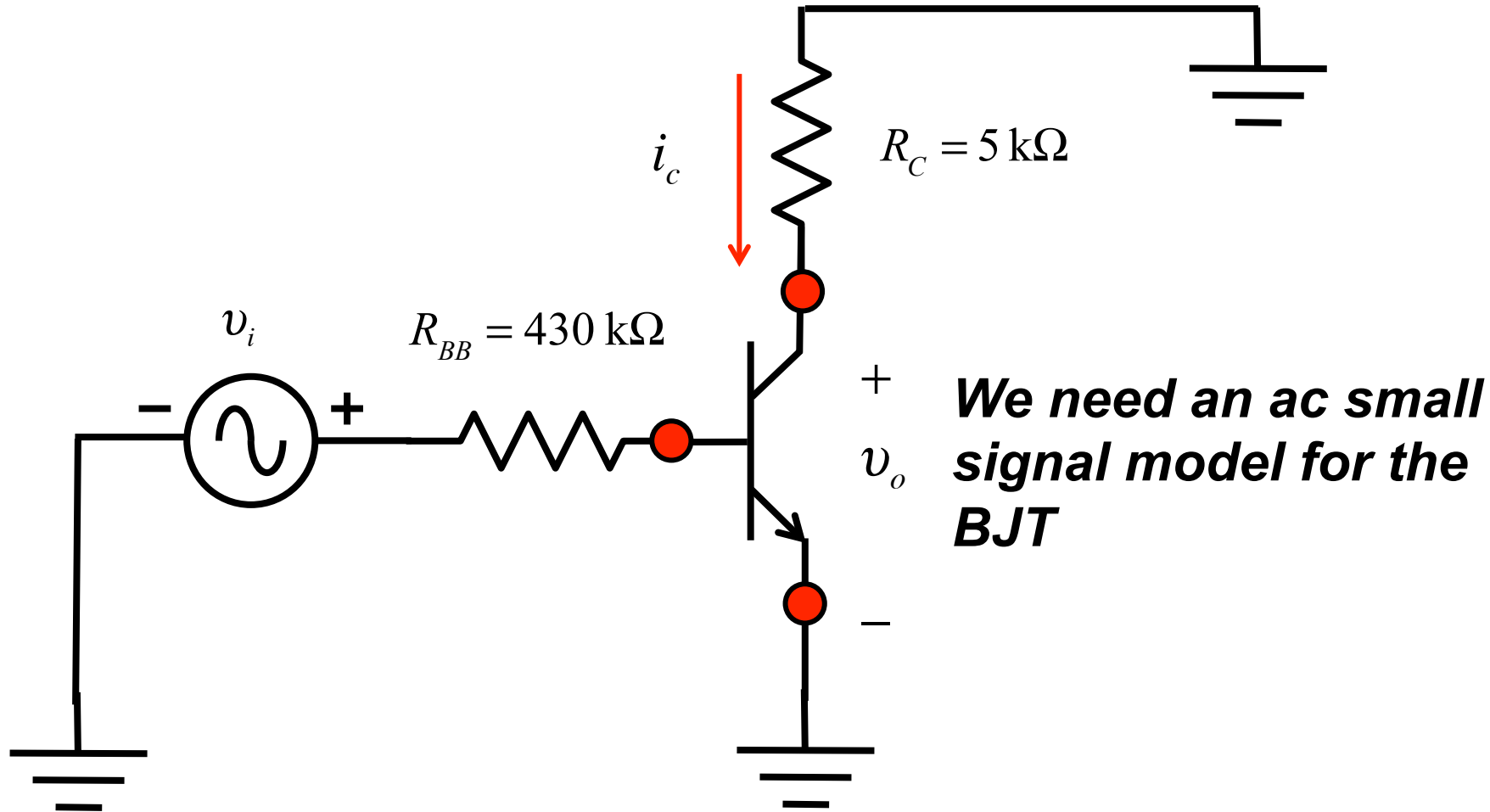
- 1) DC first
- 2) Then AC



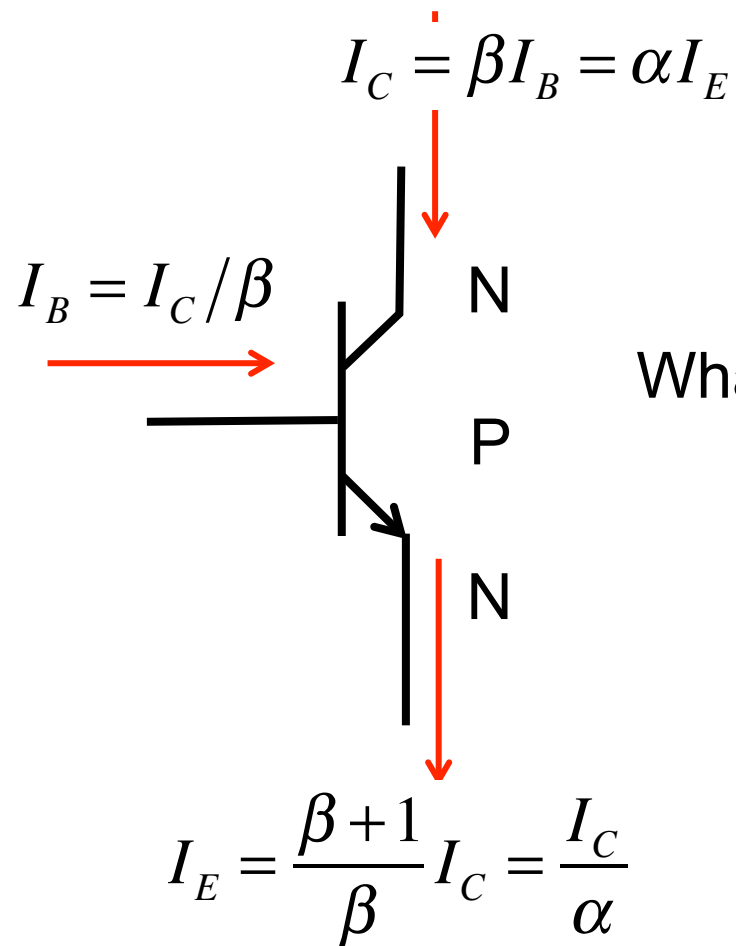
1) DC analysis



2) AC analysis

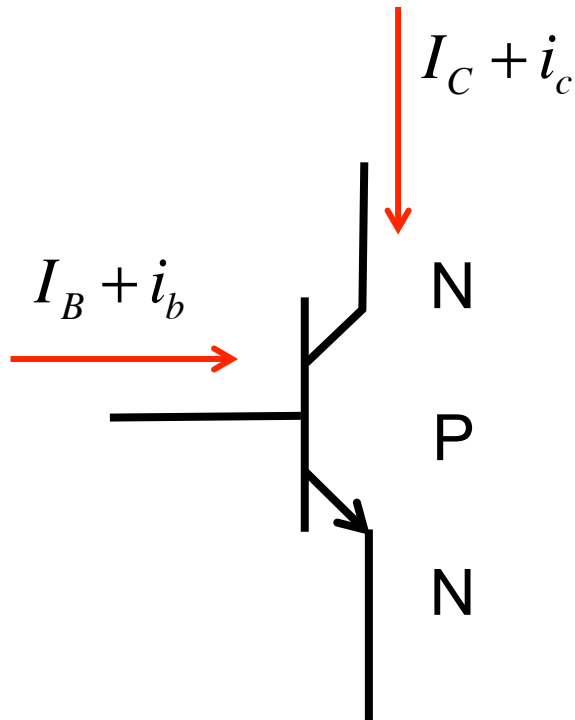


Recall: DC currents



What is the a.c. small signal model?

ac collector current



$$I_C + i_c = I_S e^{V_{BE}/V_T} \rightarrow I_C + i_c = I_S e^{(V_{BE} + v_{be})/V_T}$$

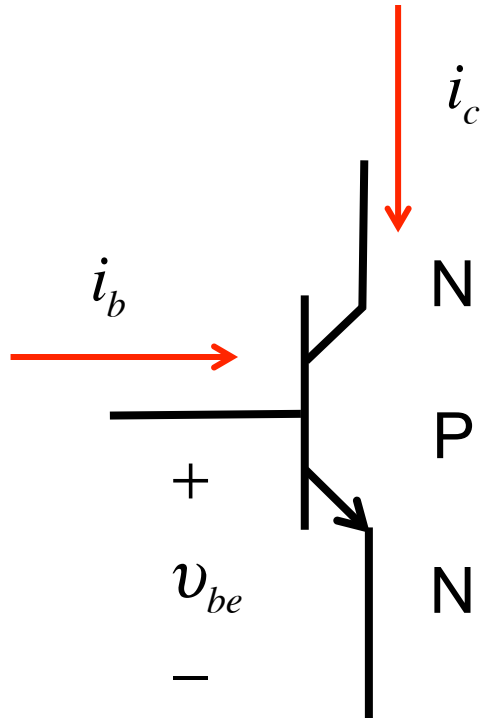
$$I_C + i_c = I_S e^{V_{BE}/V_T} e^{v_{be}/V_T}$$

$$I_C + i_c = I_S e^{V_{BE}/V_T} (1 + v_{be}/V_T)$$

$$i_c = I_S e^{V_{BE}/V_T} (v_{be}/V_T) = I_C (v_{be}/V_T)$$

$$i_c = \frac{I_C}{V_T} v_{be} = g_m v_{be} \quad \text{Ohm's Law}$$

Summary: s.s. collector current



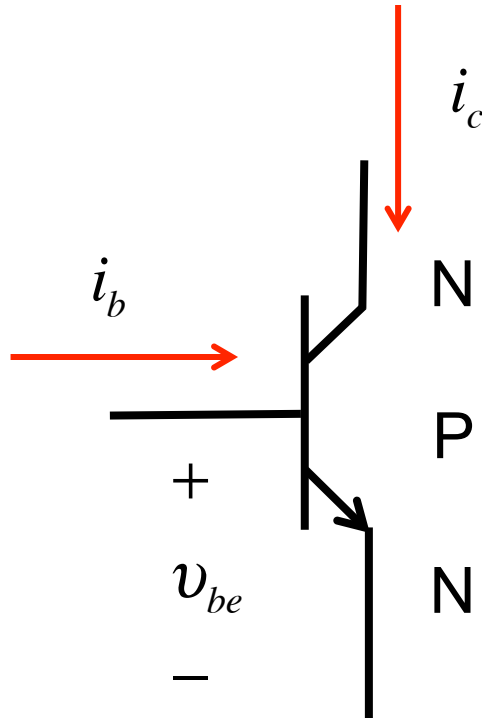
$$i_c = g_m v_{be}$$

$$g_m = \frac{I_C}{V_T}$$

“transconductance”

Note that the ac model parameter, g_m , depends on the dc bias current, I_C .

s.s. base current



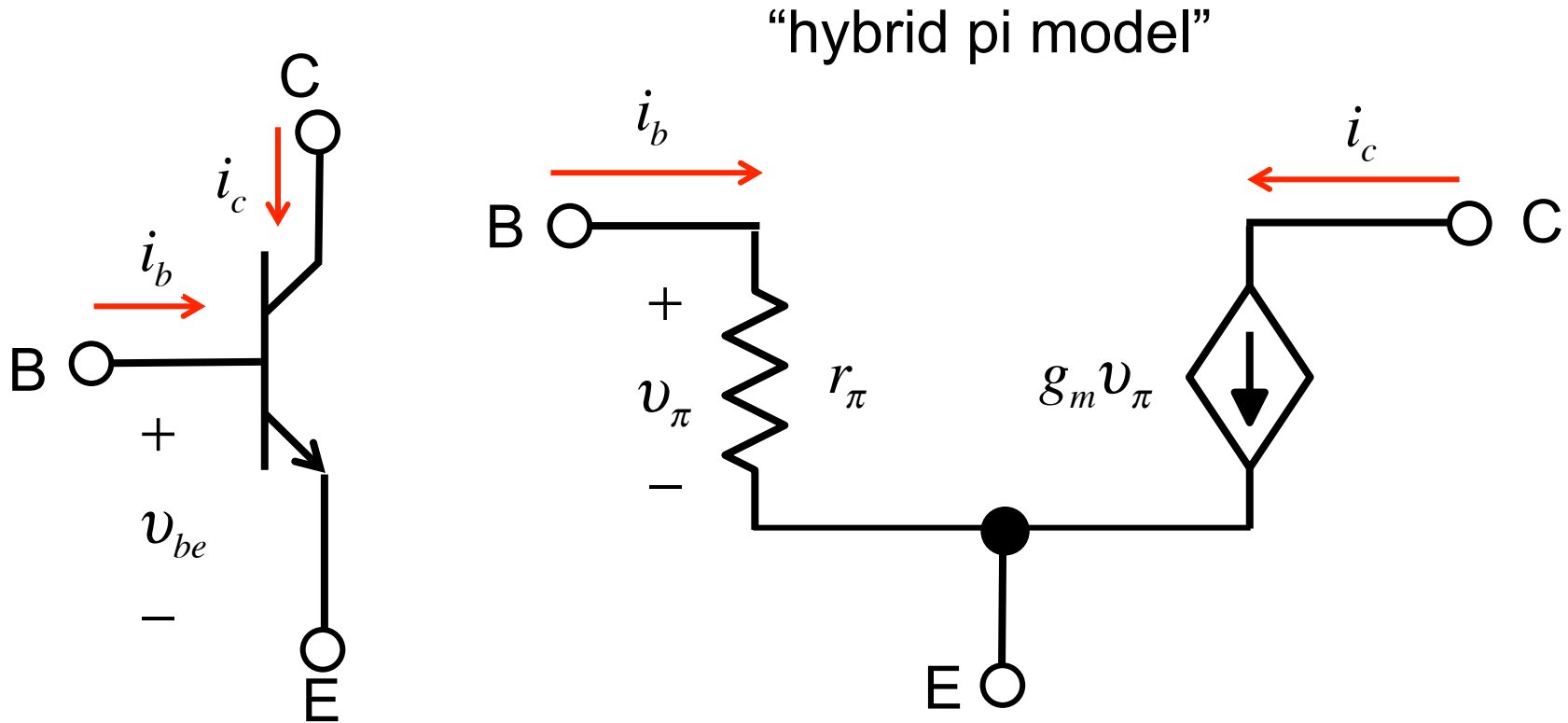
$$i_b = \frac{i_c}{\beta} = \frac{g_m}{\beta} v_{be} = \frac{v_{be}}{\beta/g_m} = \frac{v_{be}}{r_\pi}$$

$$i_b = \frac{v_{be}}{r_\pi}$$
$$g_m r_\pi = \beta$$

$$i_c = g_m v_{be}$$

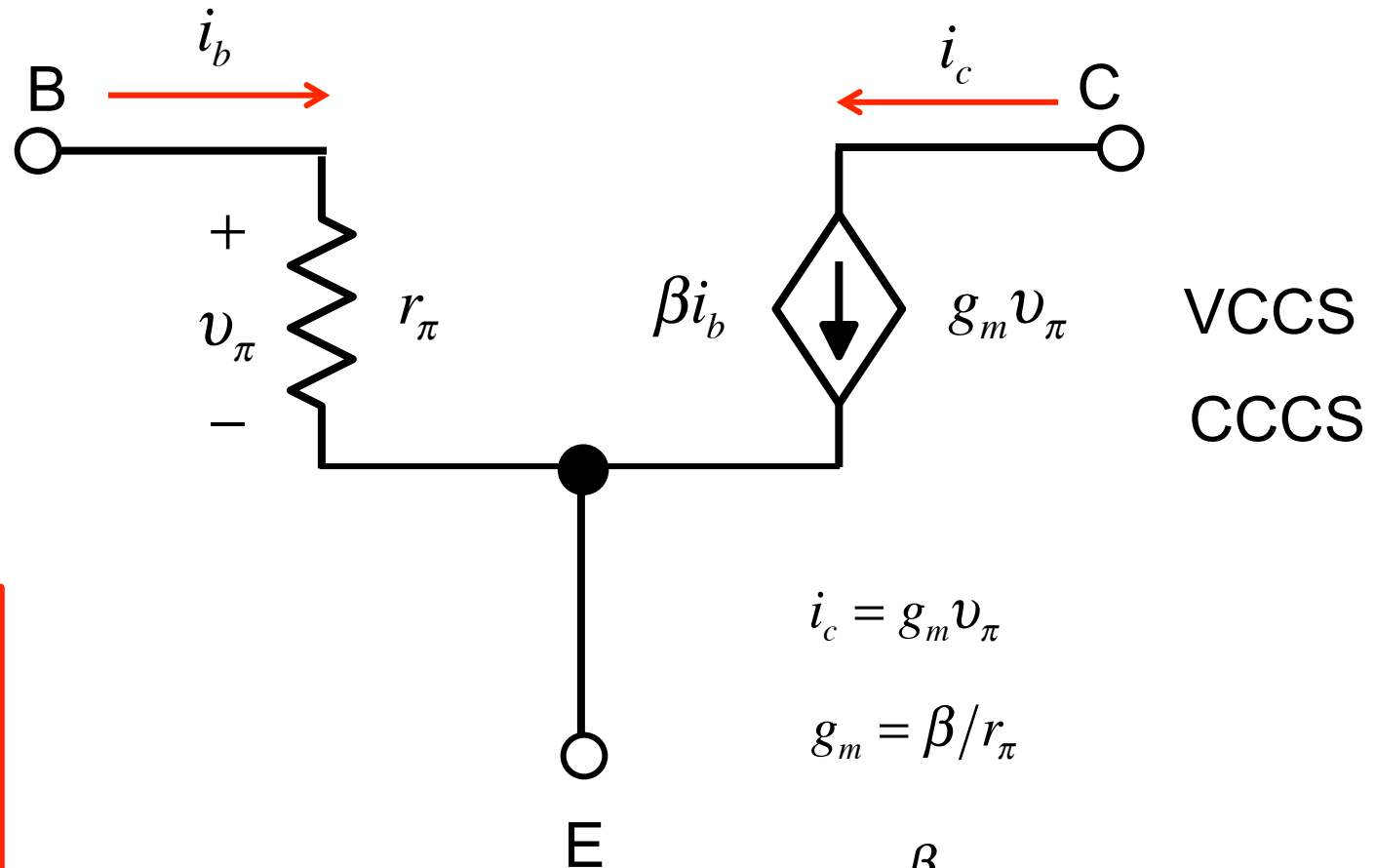
$$g_m = \frac{I_C}{V_T}$$

s.s. eqv. circuit model: g_m form



$$g_m = I_C / V_T \quad g_m r_\pi = \beta$$

s.s. eqv. circuit model: beta form



$$g_m = \frac{I_C}{V_T}$$

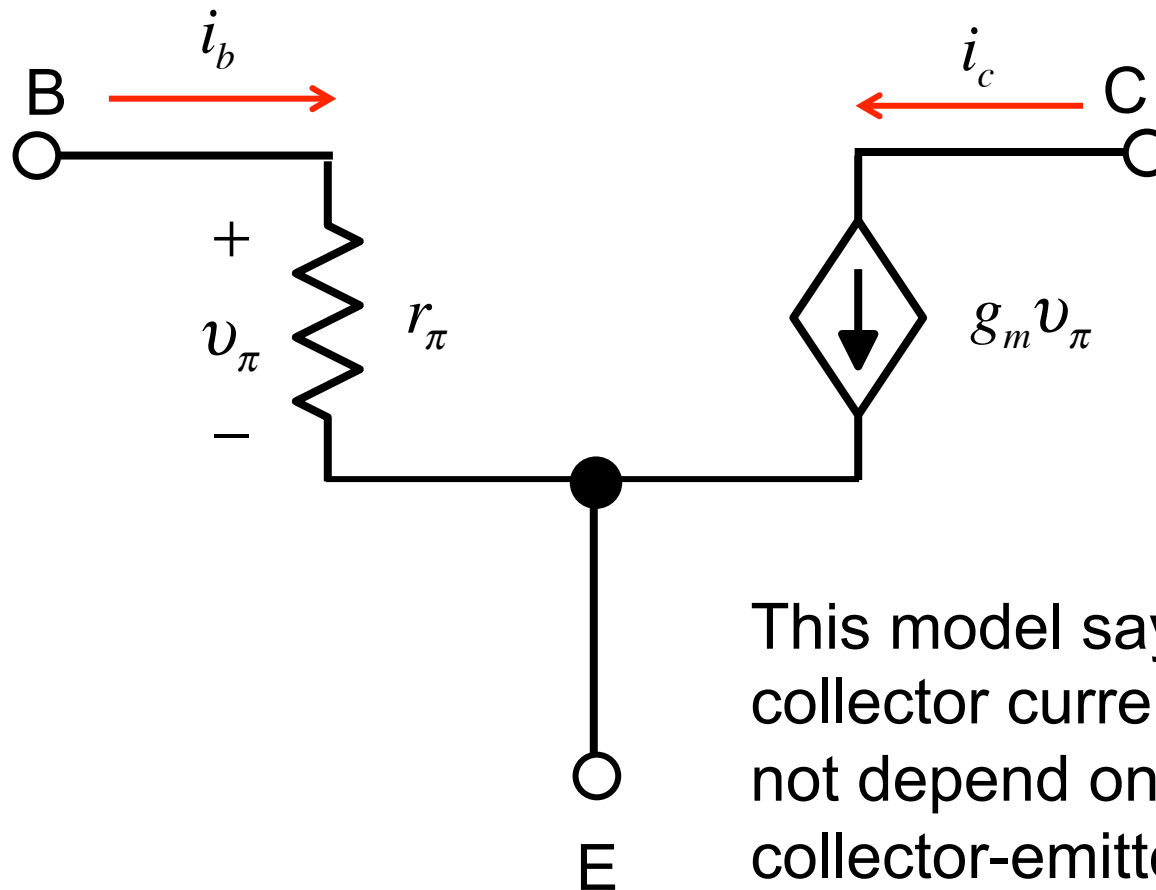
$$g_m r_\pi = \beta$$

$$i_c = g_m v_\pi$$

$$g_m = \beta / r_\pi$$

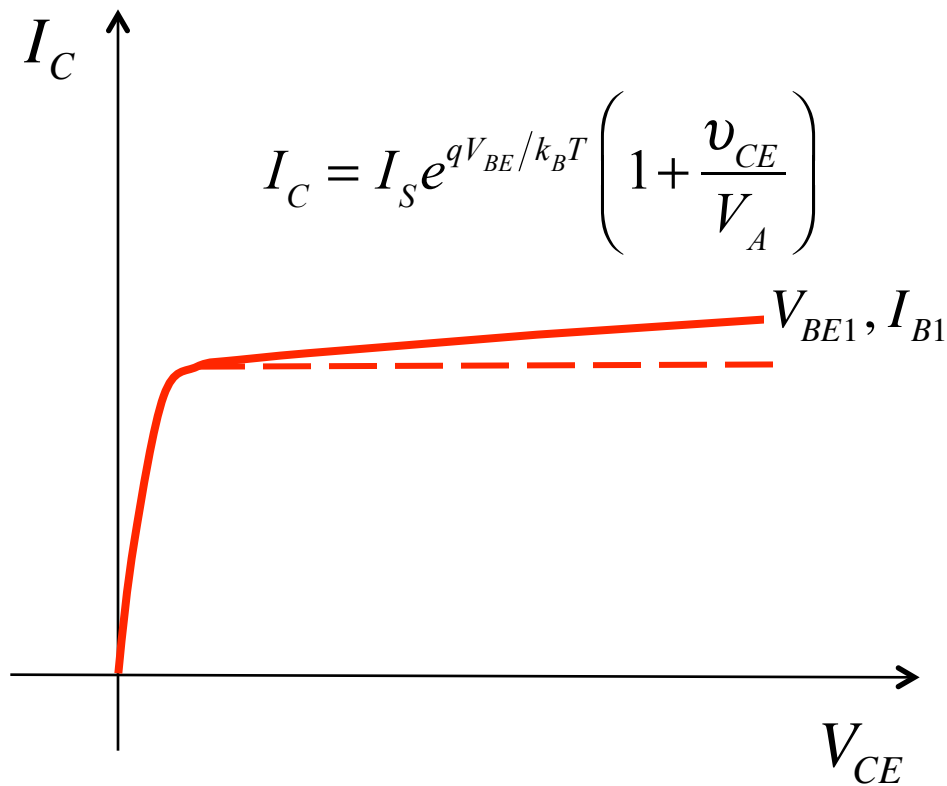
$$i_c = \frac{\beta}{r_\pi} v_\pi = \beta i_b$$

s.s. eqv. circuit model: v_{ce} dependence



This model says that the collector current does not depend on the collector-emitter voltage.

Output resistance



$$\frac{dI_C}{dV_{CE}} = I_S e^{qV_{BE}/k_B T} \left(\frac{1}{V_A} \right) = \frac{I'_C}{V_A}$$

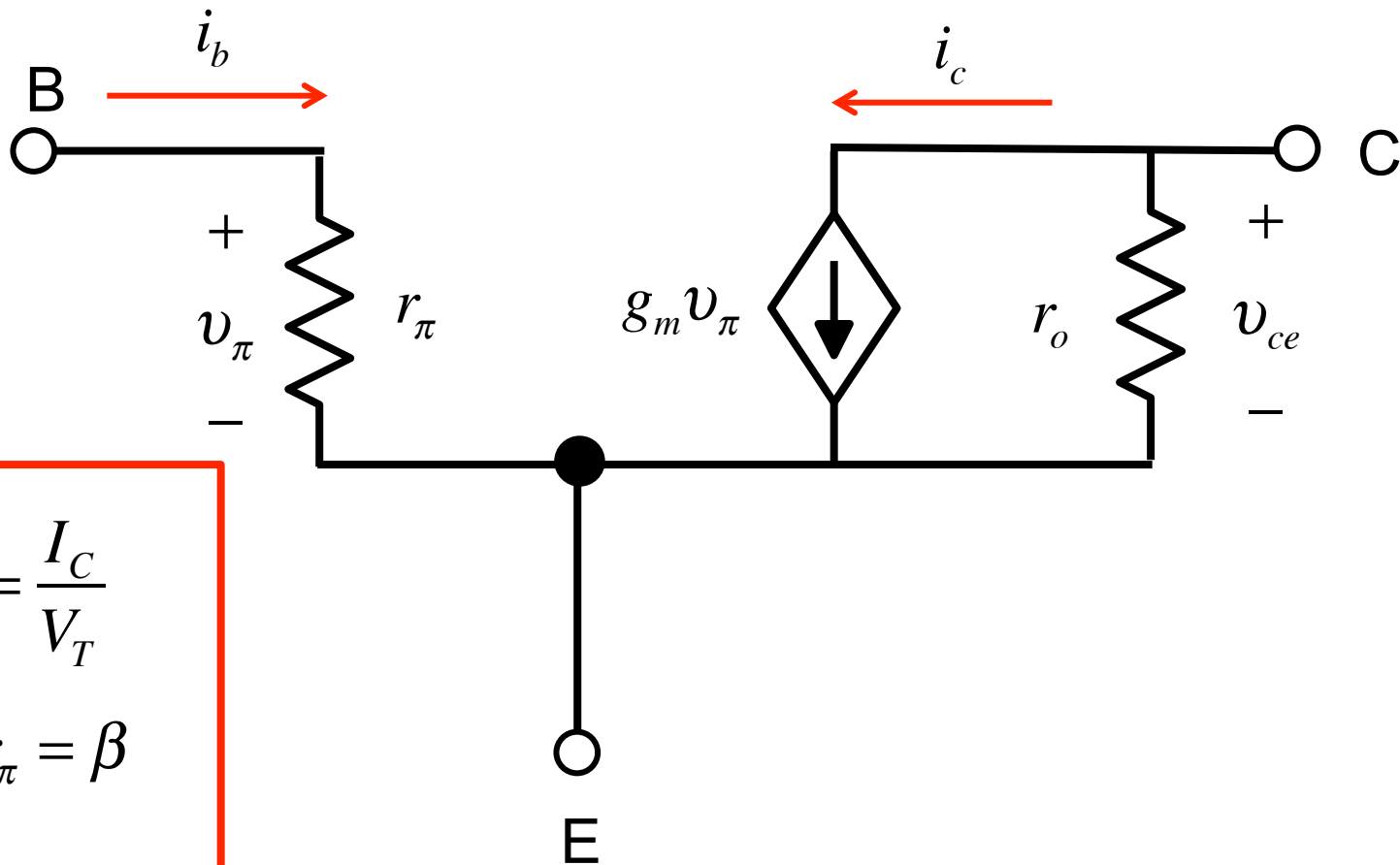
$$\frac{dI_C}{dV_{CE}} \approx \frac{i_c}{v_{ce}}$$

$$i_c = \frac{v_{ce}}{r_o}$$

$$r_o \approx \frac{V_A}{I_C}$$

V_A = "Early Voltage"

Hybrid pi model

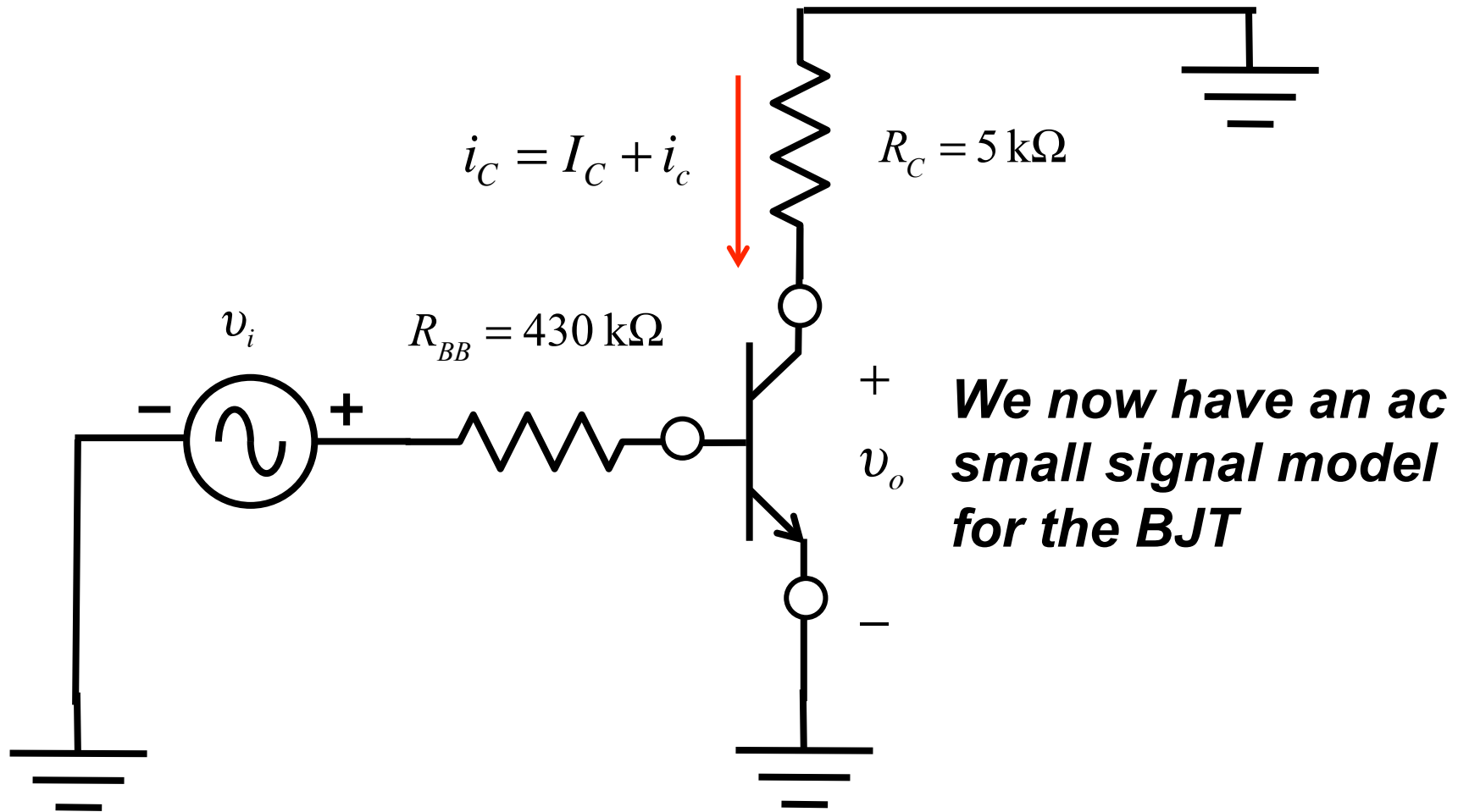


$$g_m = \frac{I_C}{V_T}$$

$$g_m r_\pi = \beta$$

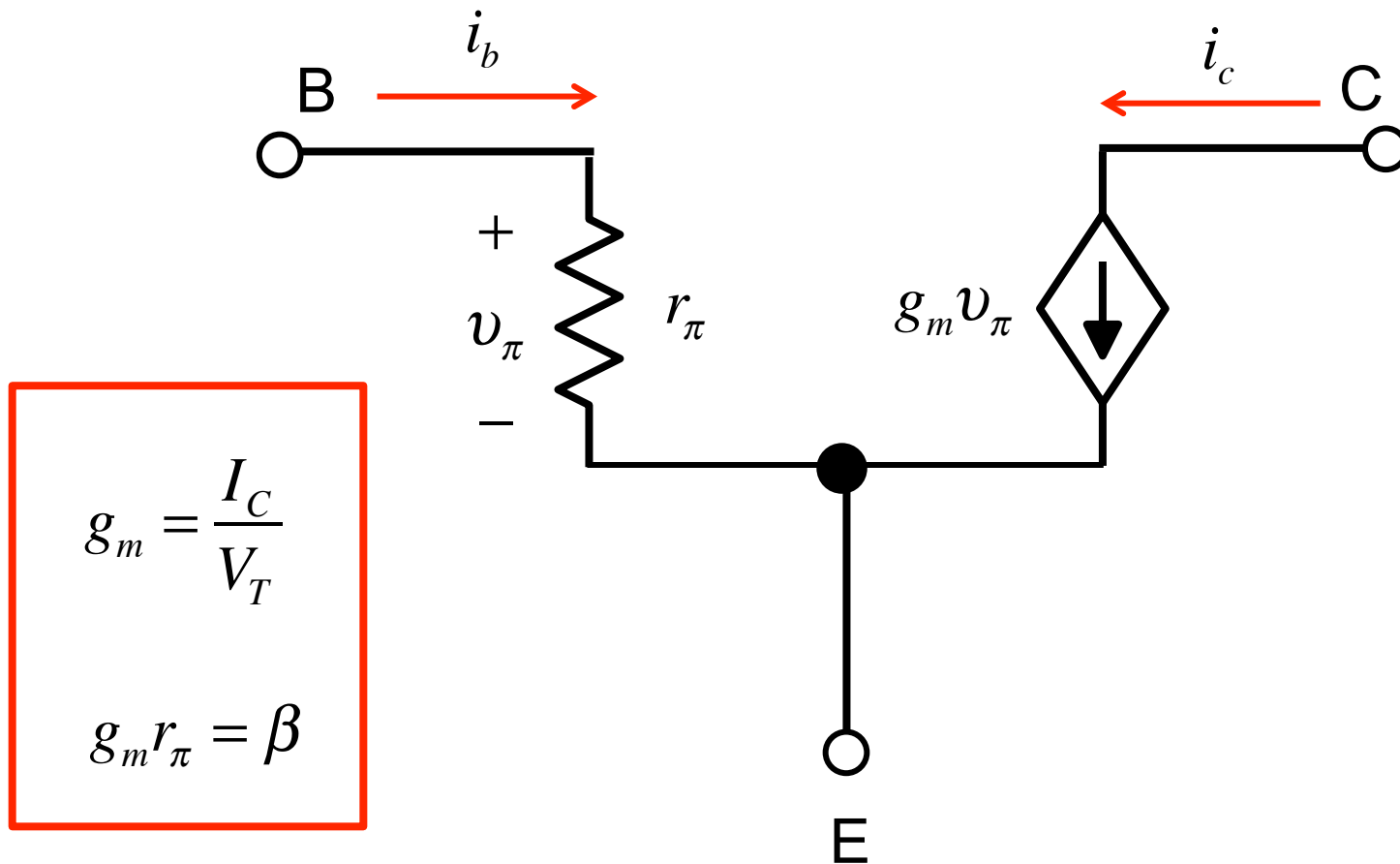
$$r_o = V_A / I_C$$

Small signal circuit

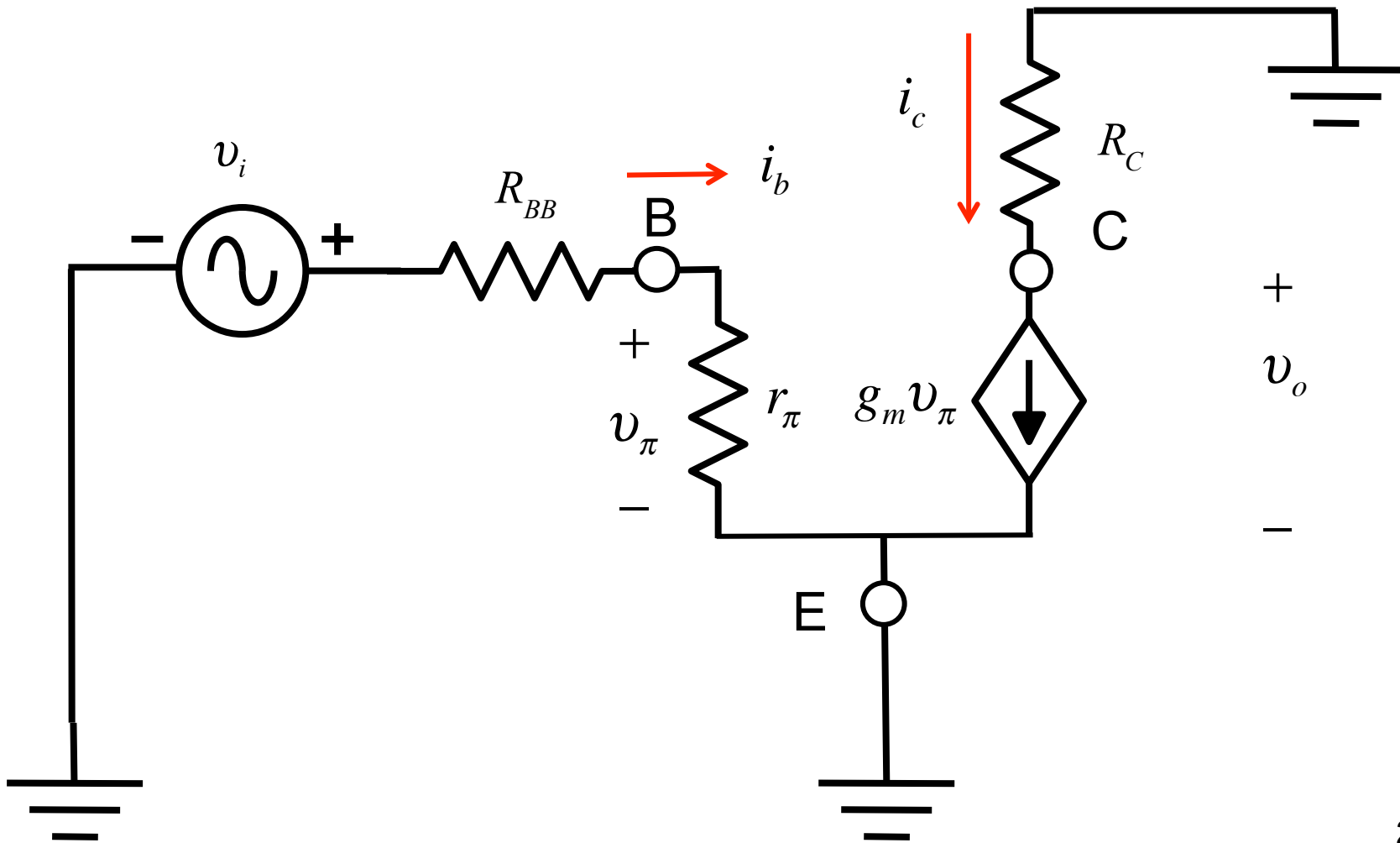


We now have an ac small signal model for the BJT

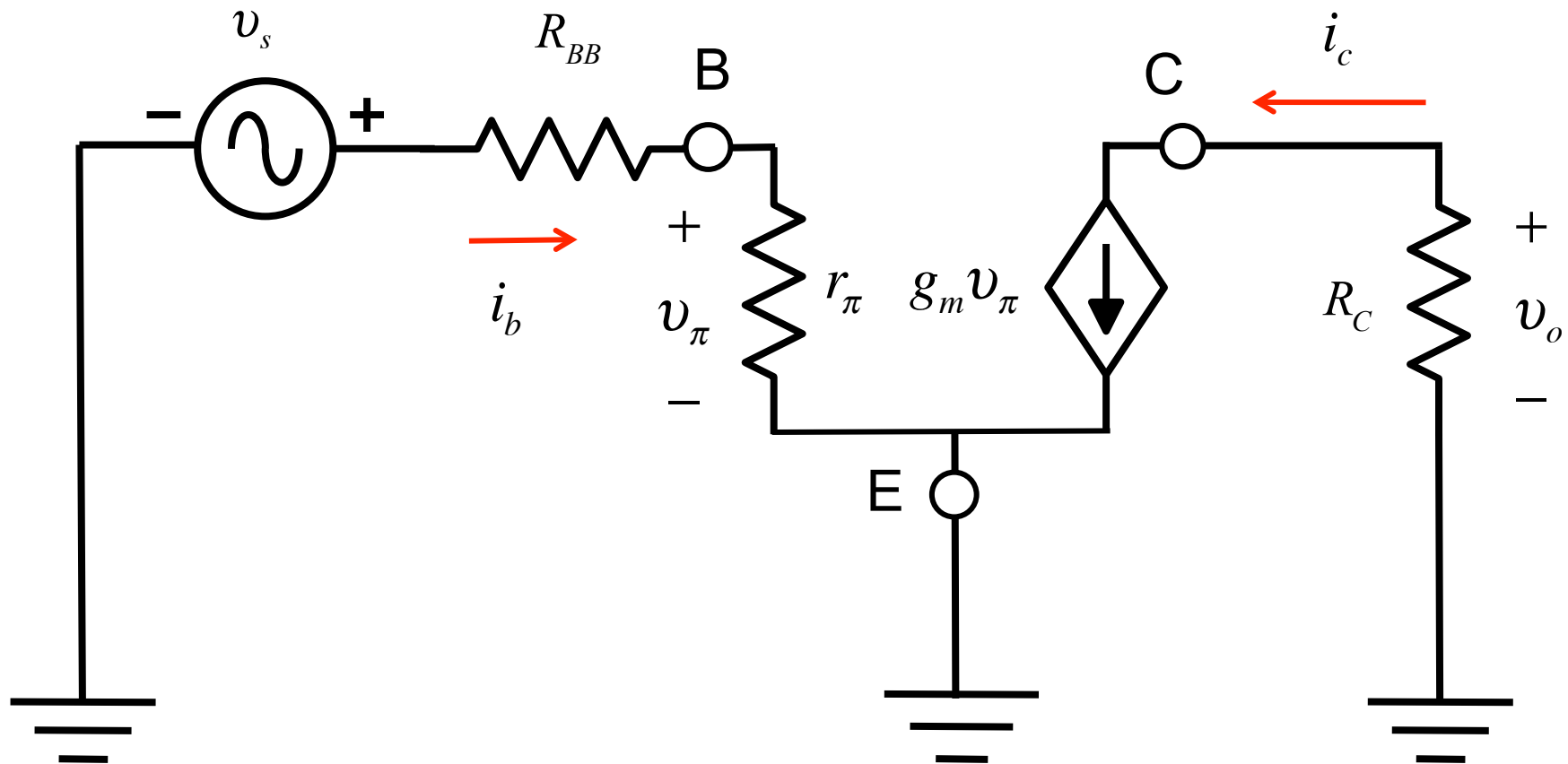
Simple s.s. model



ac analysis



Same circuit



ac analysis

$$v_o = -g_m v_\pi R_C$$

$$v_\pi = \frac{r_\pi}{r_\pi + R_{BB}} v_s$$

$$v_o = -\left(\frac{r_\pi}{r_\pi + R_{BB}}\right) g_m R_C v_s$$

$$\frac{v_o}{v_s} = A_{v_s} = -\left(\frac{r_\pi}{r_\pi + R_{BB}}\right) g_m R_C$$

$$\beta = 100$$

$$R_{BB} = 430 \text{ k}\Omega$$

$$I_C = 1 \text{ mA}$$

$$R_C = 5 \text{ k}\Omega$$

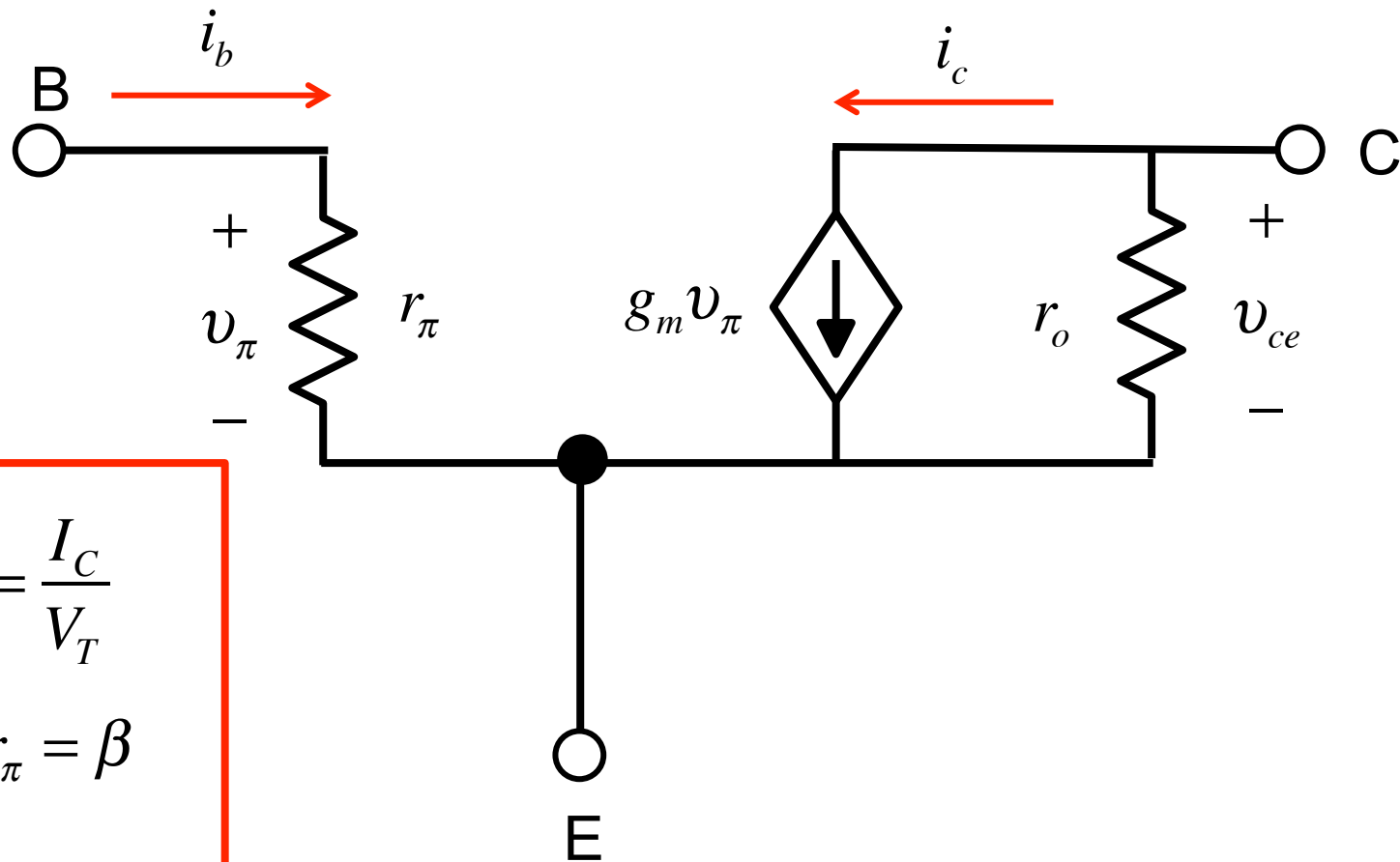
$$g_m = \frac{I_C}{V_T} = \frac{1 \text{ mA}}{0.026 \text{ V}} = 38.5 \text{ mS}$$

$$g_m r_\pi = \beta$$

$$r_\pi = \beta / g_m = 100 / 0.039 = 2.6 \text{ k}\Omega$$

$$A_{v_s} = -1.2$$

Hybrid pi model with output resistance

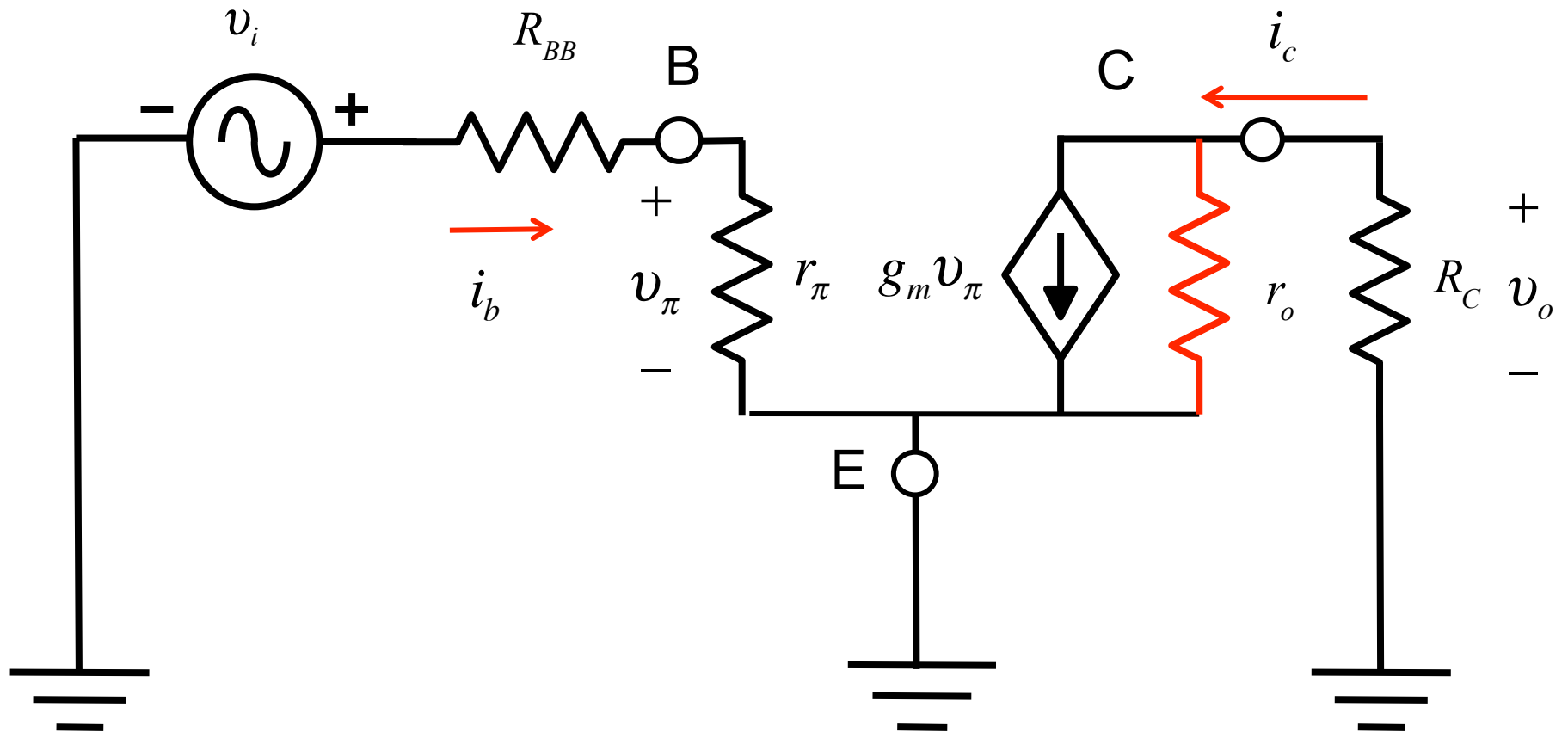


$$g_m = \frac{I_C}{V_T}$$

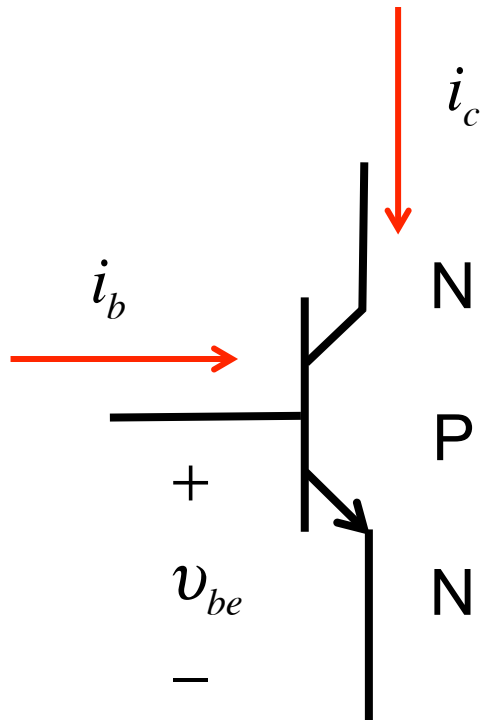
$$g_m r_\pi = \beta$$

$$r_o = V_A / I_C$$

Circuit with output resistance



Transistor parameters



$$\beta = 100$$

$$V_{BE}(\text{ON}) = 0.7 \text{ V}$$

$$V_A = 100 \text{ V}$$

$$r_o = \frac{V_A}{I_C} = \frac{100 \text{ V}}{1 \text{ mA}} = 100 \text{ k}\Omega$$

2) AC analysis with output resistance

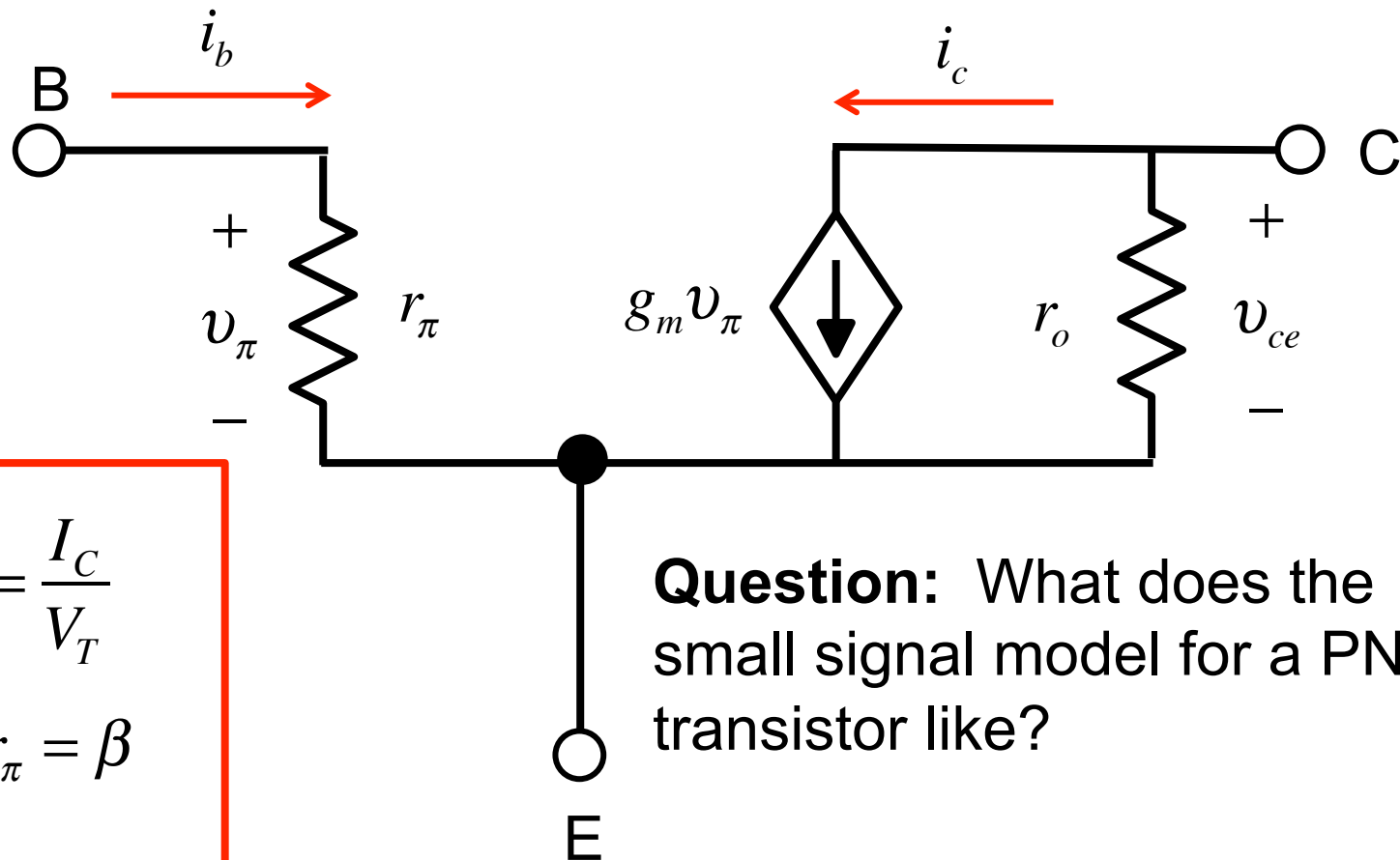
$$\frac{v_o}{v_i} = A = -\left(\frac{r_\pi}{r_\pi + R_{BB}}\right) g_m R_C \quad (\text{without } r_o)$$

$$\frac{v_o}{v_i} = A = -\left(\frac{r_\pi}{r_\pi + R_{BB}}\right) g_m R_C \parallel r_o \quad (\text{with } r_o)$$

$$5 \text{ k}\Omega \parallel 100 \text{ k}\Omega = 4.76 \text{ k}\Omega$$

$$A_v = -1.2 \rightarrow A_v = -1.1$$

Hybrid pi model of BJT



$$g_m = \frac{I_C}{V_T}$$

$$g_m r_\pi = \beta$$

$$r_o = V_A / I_C$$

Question: What does the small signal model for a PNP transistor look like?

Summary

The DC bias circuit places the operating point in the portion of the Voltage Transfer Characteristics where the output voltage changes rapidly with input voltage.

The small signal model of a BJT consists of two resistors and one voltage-controlled current source. The values of the ac model parameters are determined by the dc bias current.

Circuit analysis consists of two steps: 1) dc analysis to determine the OP, and 2) ac small signal analysis using the ac circuit model.

Transistor Amplifiers

- 1) Voltage transfer characteristic (VTC)
- 2) Small signal model for BJT
- 3) Small signal analysis

