

# **ECE-305: Spring 2018**

## **Metal-Semiconductor Diodes**

Pierret, *Semiconductor Device Fundamentals* (SDF)  
Chapter 14 (pp. 487-496)

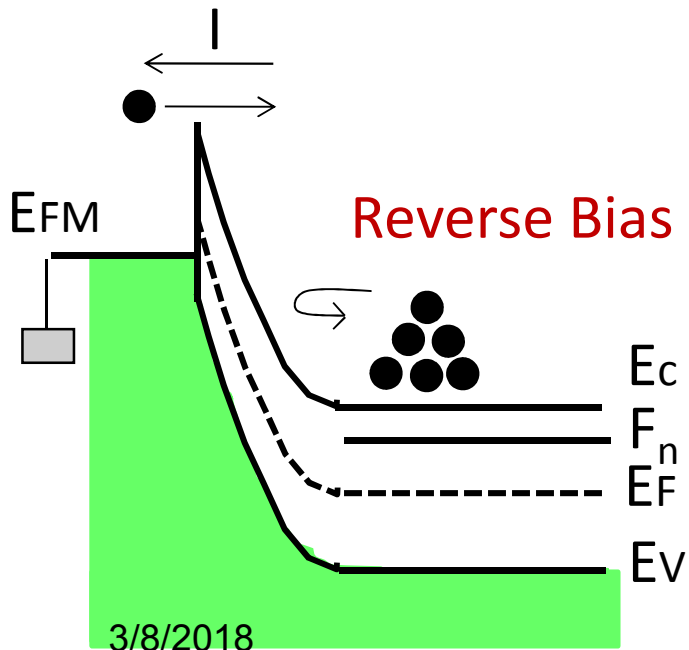
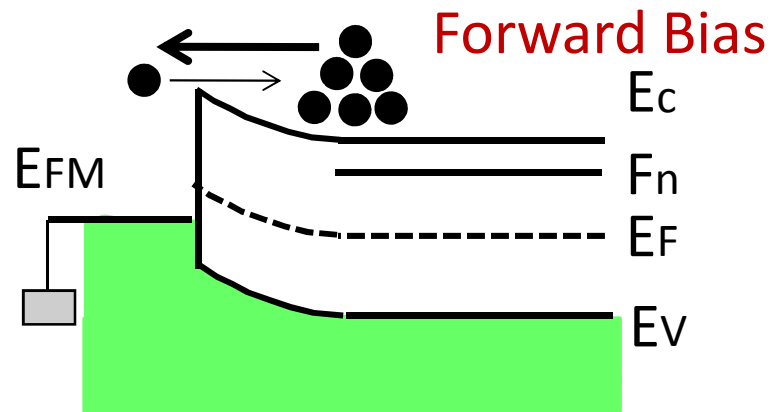
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# outline

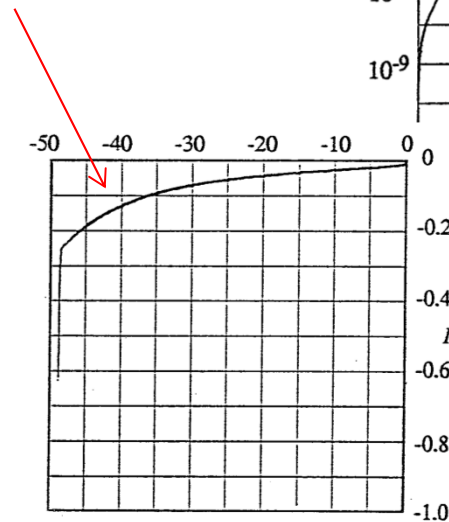
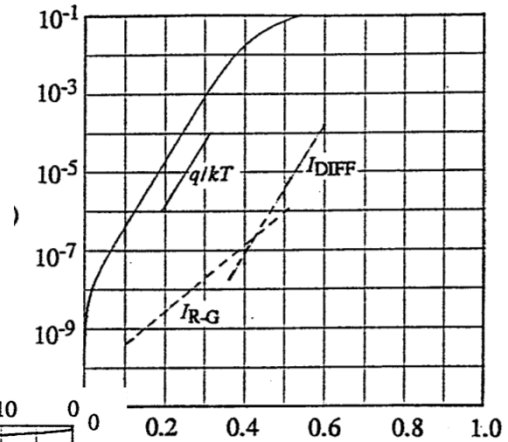
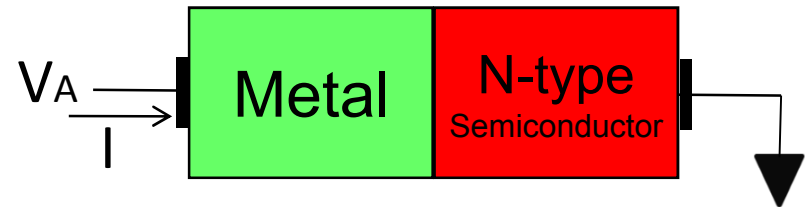
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- 1) DC Thermionic current, nonideal current
- 2) AC small signal and large-signal response

# I-V Characteristics

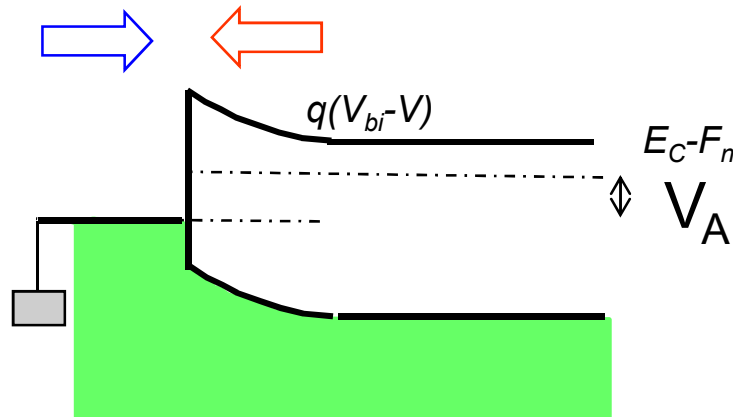


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# Left Boundary Condition



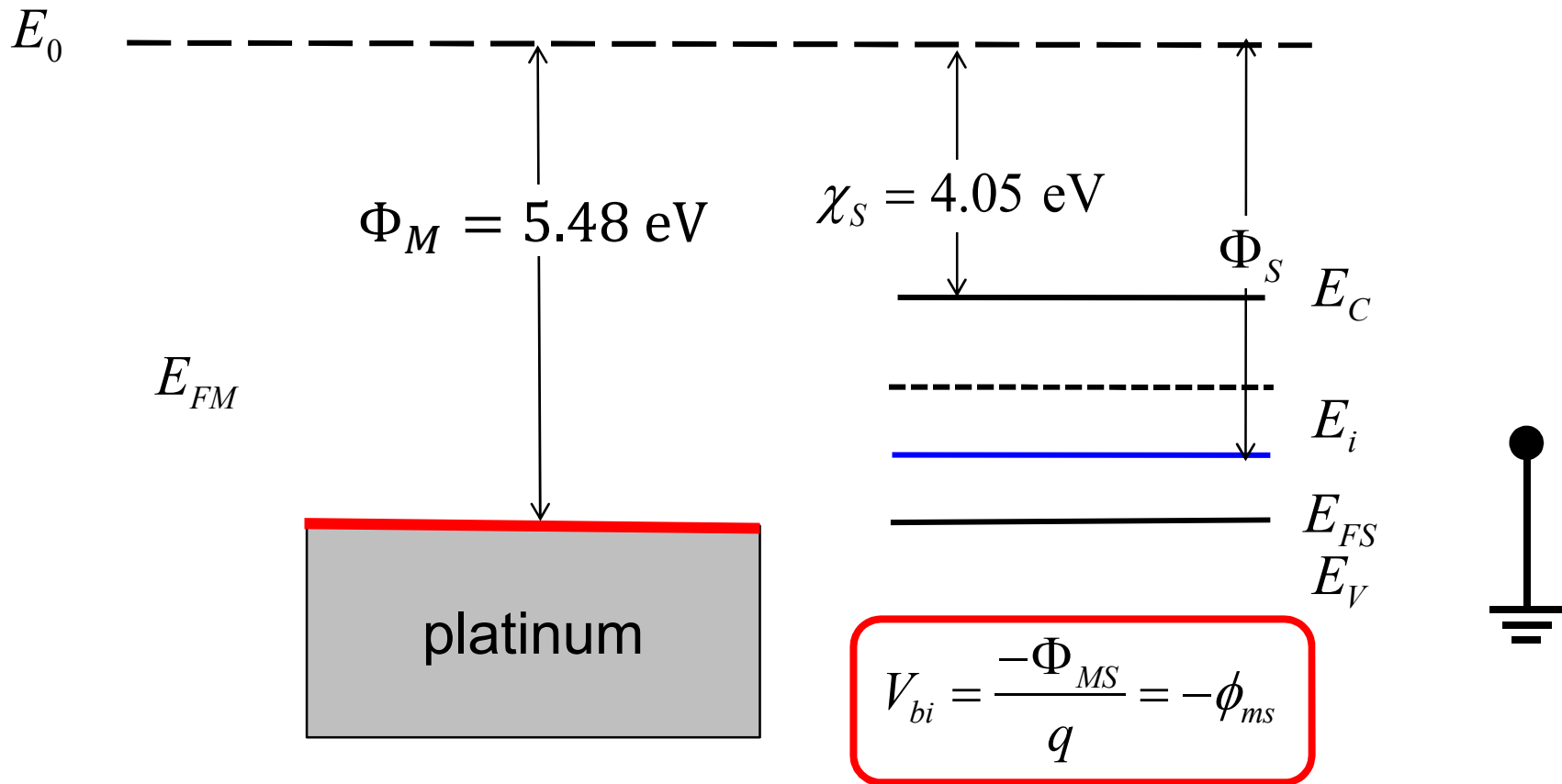
$$J_T(V_A) = J_{m \rightarrow s}(V_A) - J_{s \rightarrow m}(V_A)$$

$$J_T(V_A = 0) = 0 = J_{m \rightarrow s}(0) - J_{s \rightarrow m}(0)$$

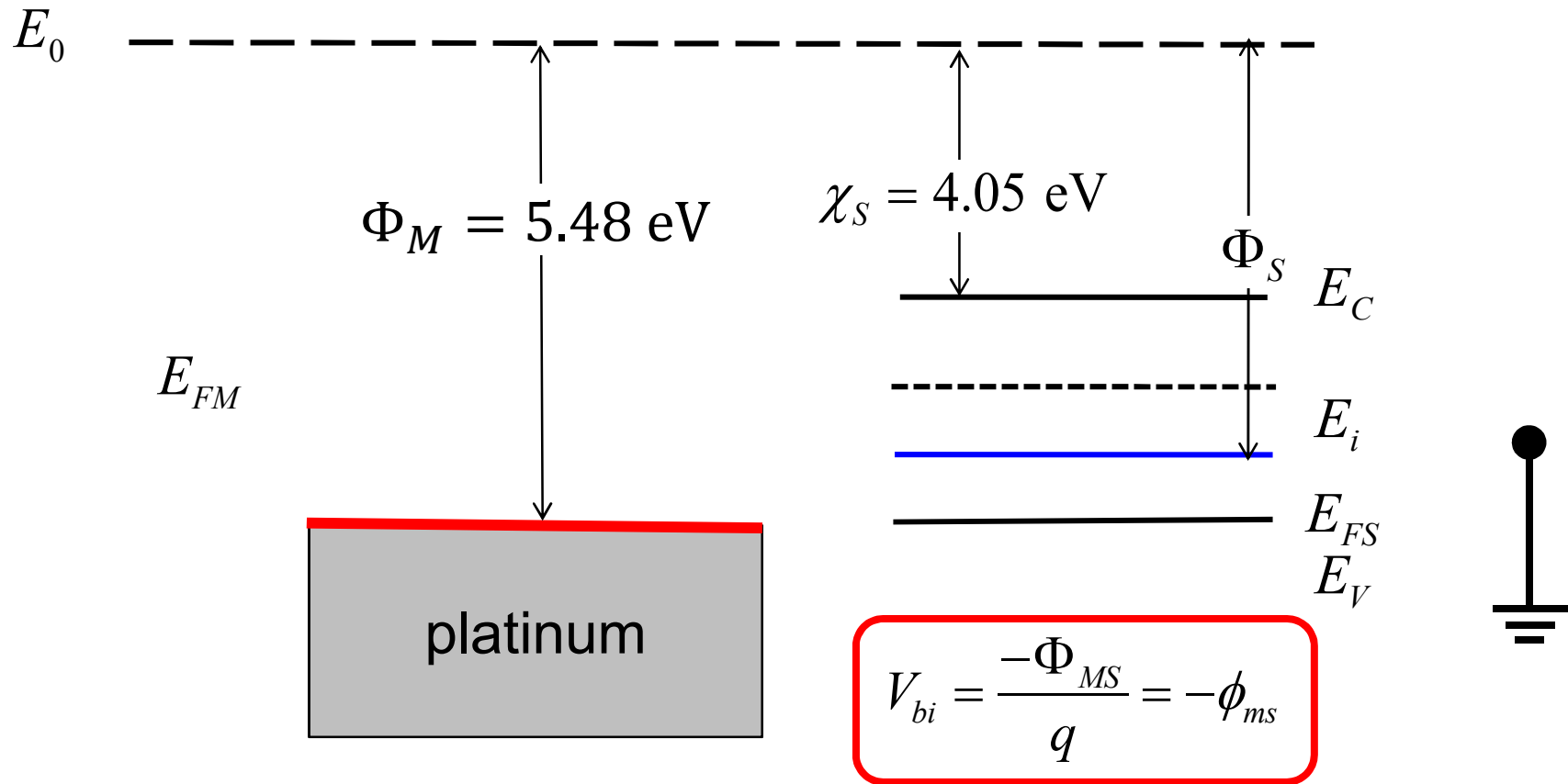
$$\Rightarrow J_{m \rightarrow s}(0) = J_{s \rightarrow m}(0)$$

$$J_T(V_A) = J_{s \rightarrow m}(0) - J_{s \rightarrow m}(V_A)$$

# Example: MS junction band structure



# Example: MS junction built-in potential



$$qV_{bi} = (E_{FM} - E_{FS}) = (\Phi_S - \Phi_M) = -(\Phi_M - \Phi_S) = -\Phi_{MS}$$

# Example: MS junction built-in potential

Platinum metal and p-type Si

$$N_A = 10^{16} \text{ cm}^{-3}$$

$$p_0 = N_V e^{(E_V - E_{FS})/k_B T} \text{ cm}^{-3}$$

$$E_{FS} - E_V = k_B T \ln\left(\frac{N_V}{N_A}\right)$$

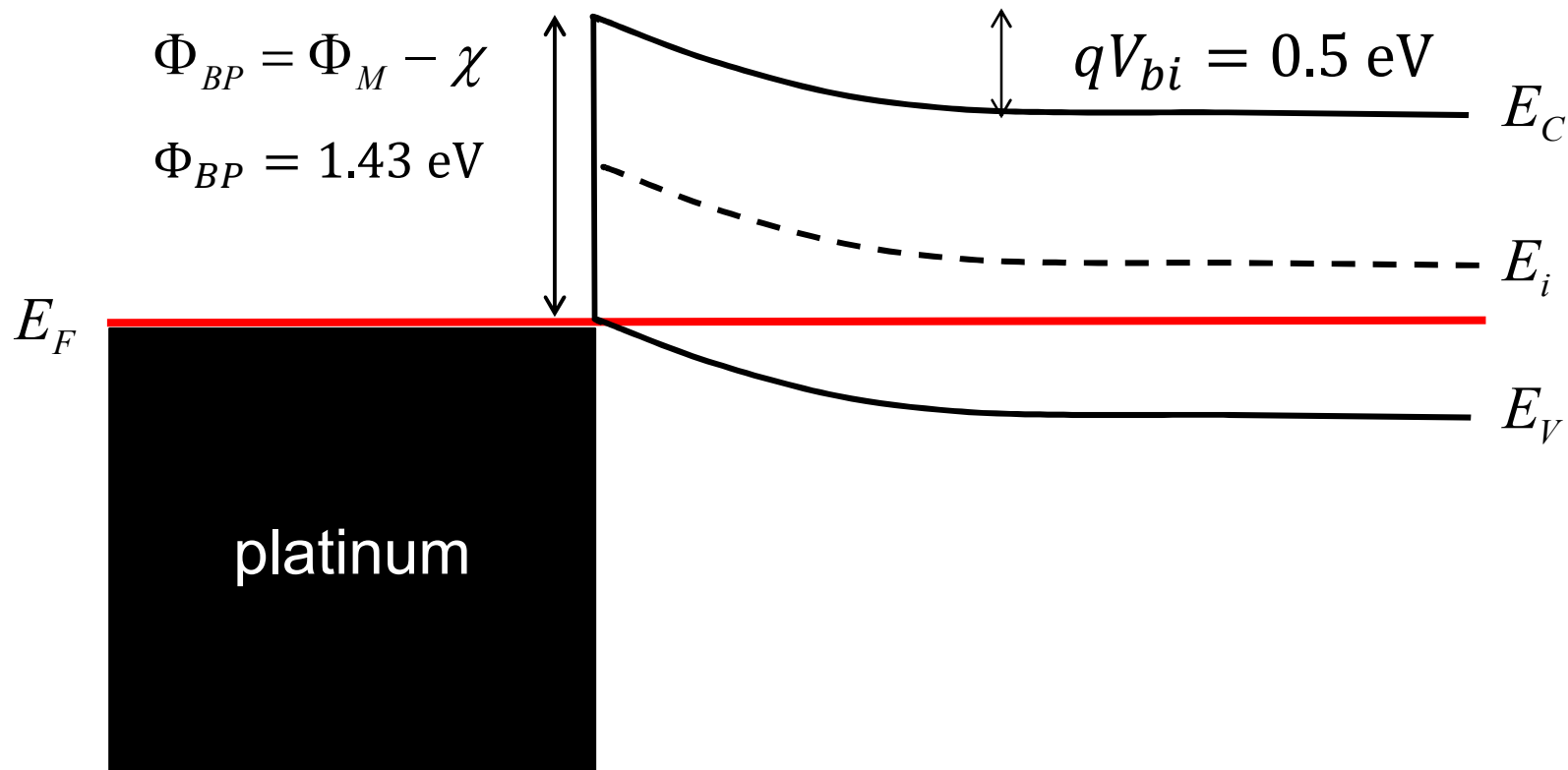
$$N_V = 2 \left[ \frac{(m_p^* k_B T)}{2\pi\hbar^2} \right]^{3/2} = 1.83 \times 10^{19} \text{ cm}^{-3}$$

$$\Phi_M = 5.48 \text{ eV}$$

$$\Phi_S = \chi_S + E_G - (E_{FS} - E_V)/q$$

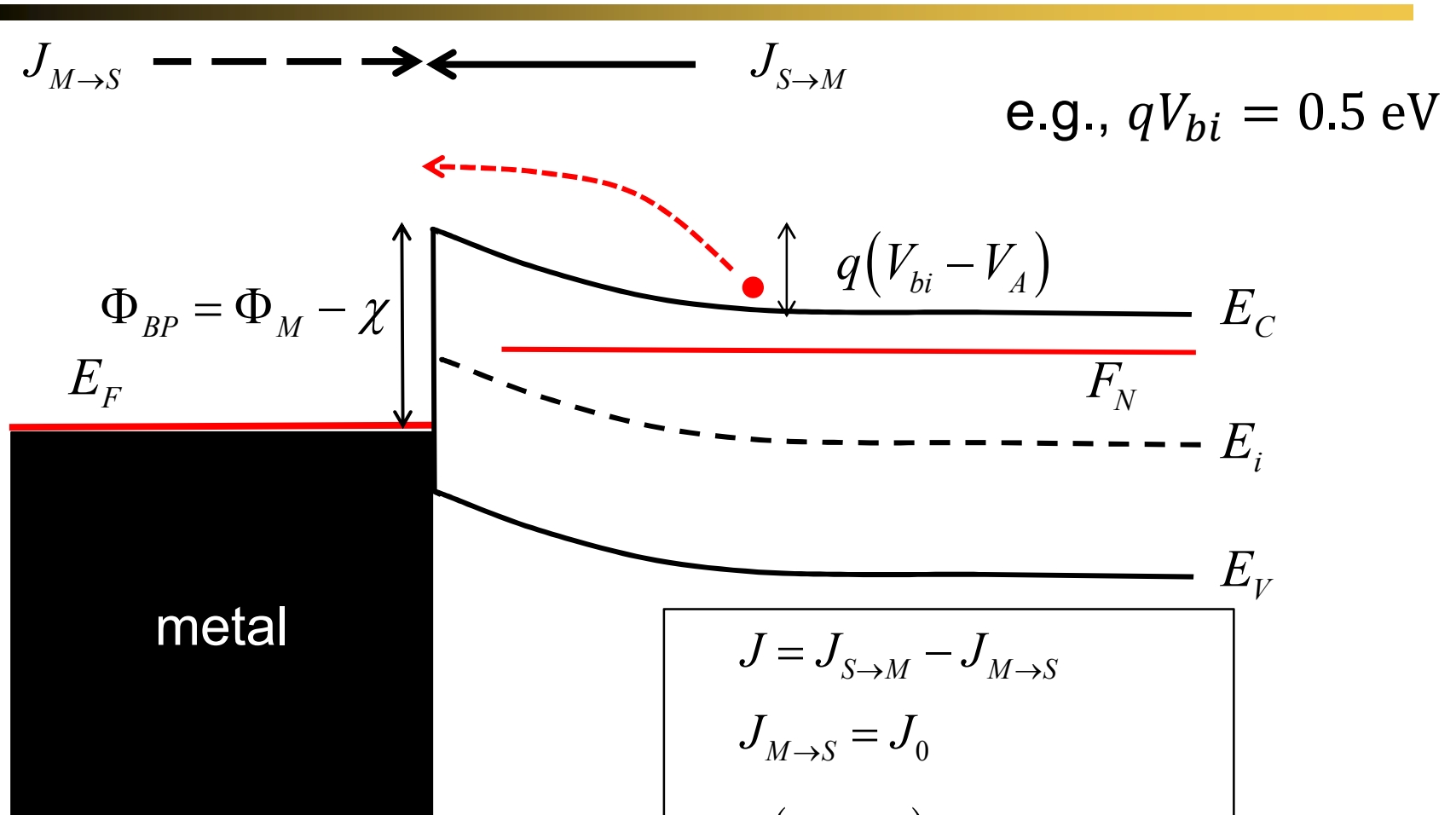
$$\Phi_S = 4.97 \text{ eV}$$

# Example: MS junction band diagram





# Non-equilibrium band diagram



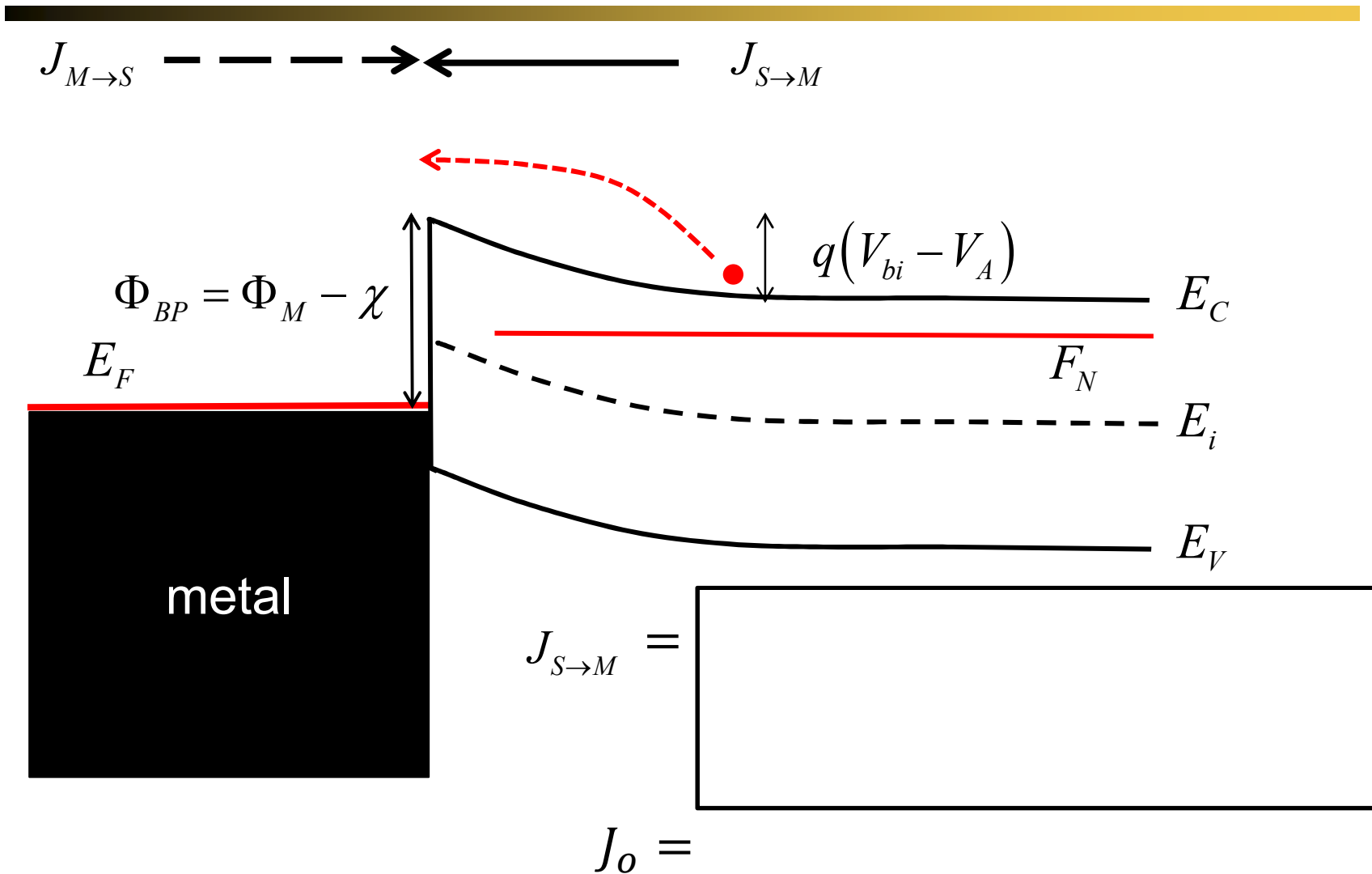
$$J = J_{S \rightarrow M} - J_{M \rightarrow S}$$

$$J_{M \rightarrow S} = J_0$$

$$J(V_A = 0) = J_0$$

$$J_{S \rightarrow M}(V_A > 0) = J_0 e^{qV_A/k_B T}$$

# Non-equilibrium band diagram



# Schottky barriers

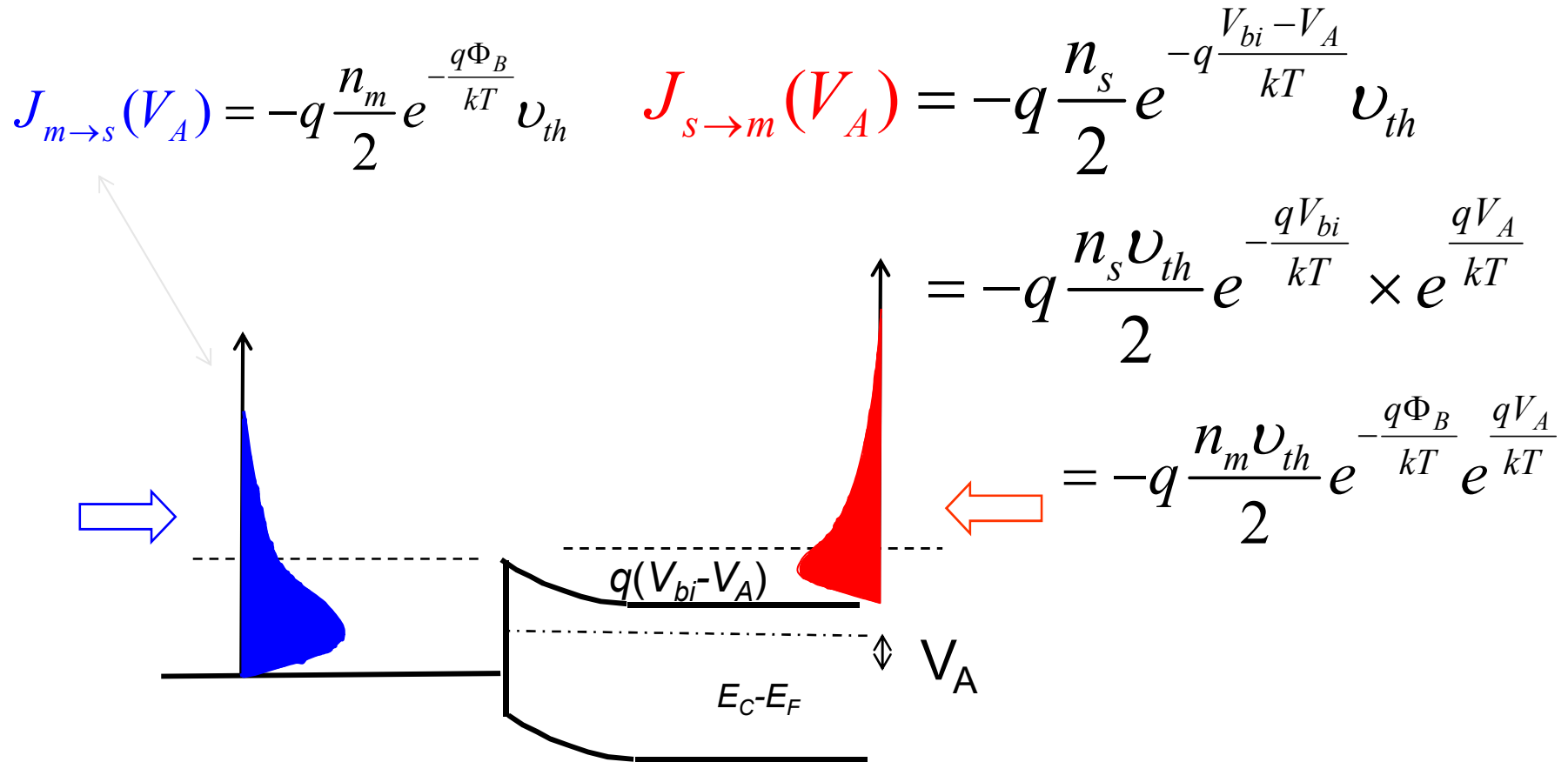
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The most reliable way to work is to take the Schottky barrier as the known (measured) quantity.

Computing Schottky barriers from workfunctions and electron affinities can lead to large errors and interface effects can affect the Schottky barrier height.

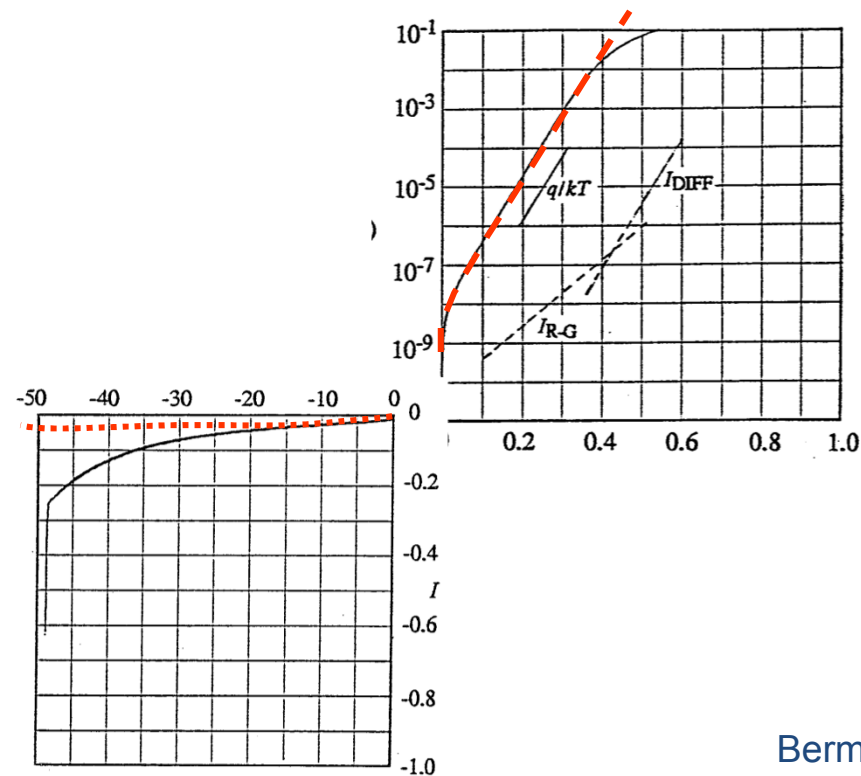
<http://www.cleanroom.byu.edu/ohmic-schottky.phtml>

# Semiconductor to Metal Flux



# Total Flux...

$$J_T = J_{s \rightarrow m}(0) - J_{s \rightarrow m}(V_A) = \frac{qn_m v_{th}}{2} e^{\frac{-q\Phi_m}{kT}} \left[ e^{\frac{qV_A}{kT}} - 1 \right]$$



## saturation current density, $J_0$

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$$J_0 = qN_D \frac{v_T}{2} e^{-qV_{bi}/k_B T}$$

$$qV_{bi} = \Phi_{BN} - (E_C - E_{FN}) \Big|_{x \rightarrow \infty}$$

$$n_0 = N_D = N_C e^{(E_{FN} - E_C)/k_B T}$$

$$qV_{bi} = \Phi_{BN} - k_B T \ln \left( \frac{N_C}{N_D} \right)$$

$$J_0 = qN_C \frac{v_T}{2} e^{-\Phi_{BN}/k_B T}$$

## saturation current density, $J_0$

---

$$J_0 = qN_C \frac{v_T}{2} e^{-\Phi_{BN}/k_B T}$$

$$N_C = 2 \left[ \frac{(m_n^* k_B T)}{2\pi\hbar^2} \right]^{3/2}$$

$$v_T = \sqrt{\frac{2k_B T}{\pi m_n^*}}$$

## saturation current

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$$J = J_0 \left( e^{qV_A/k_B T} - 1 \right)$$

$$J_0 = A^* T^2 e^{-\Phi_{BP}/k_B T}$$

$$A^* = \frac{4\pi q m_n^* k_B^2}{h^3}$$

$$A = \frac{4\pi q m_0 k_B^2}{h^3} = 120 \frac{\text{A}}{\text{cm}^2 \cdot \text{K}^2}$$

$$A^* = \frac{m_n^*}{m_0} A$$

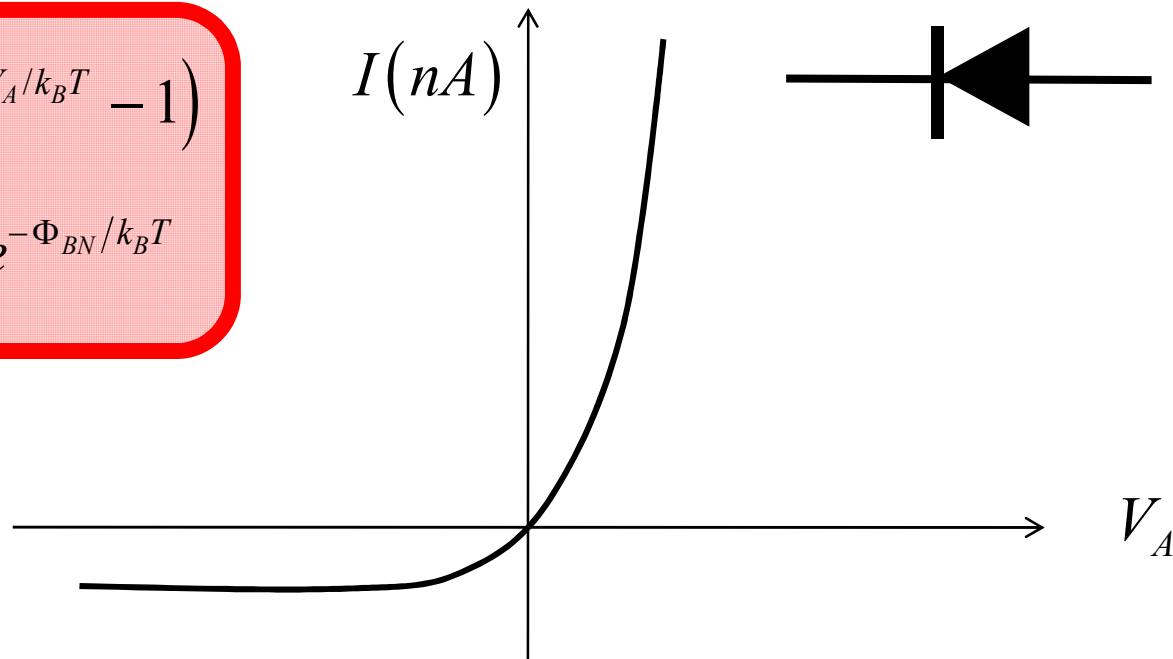
“Richardson’s constant”



# IV characteristics of a Schottky diode

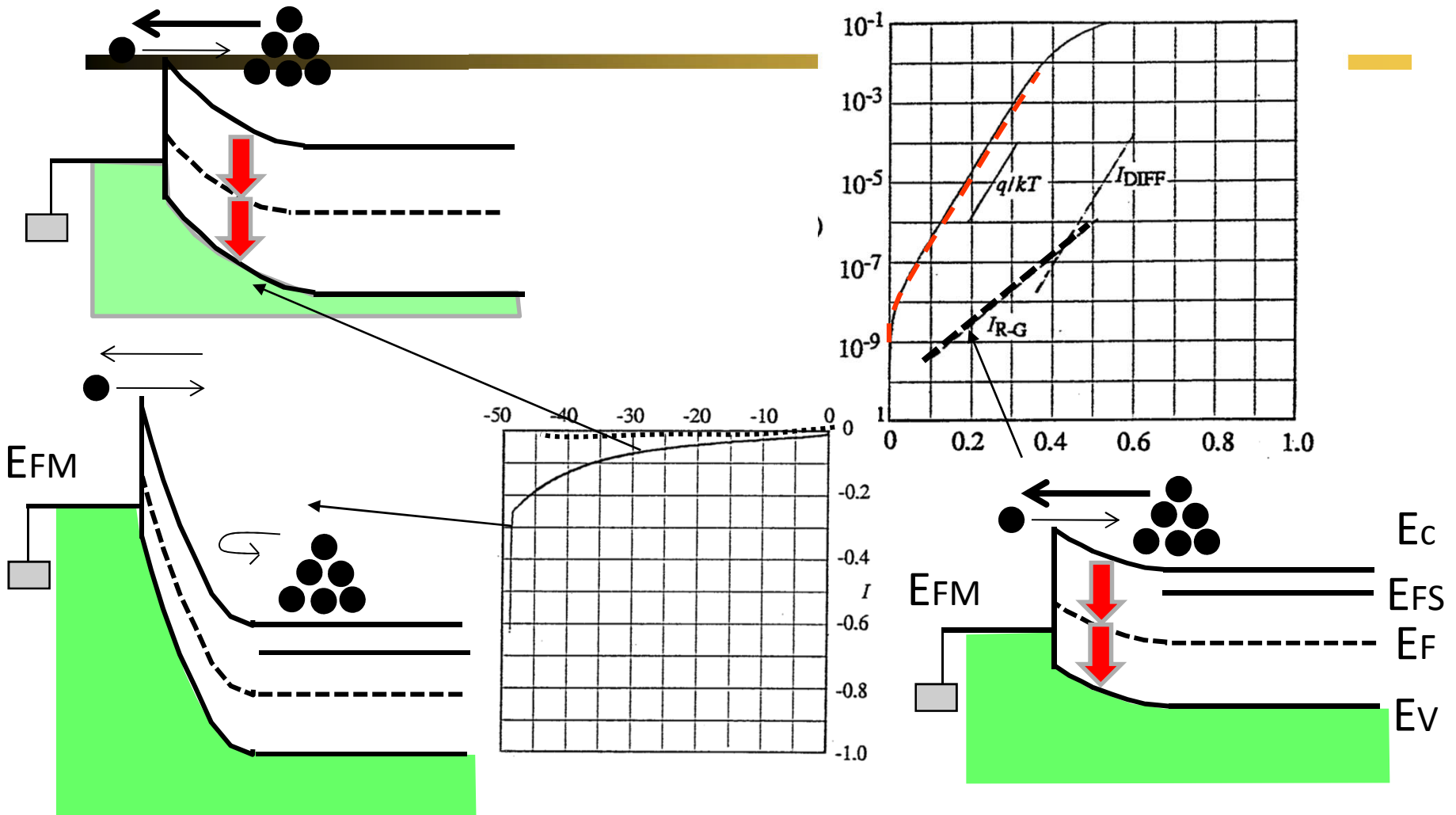
$$J = J_0 \left( e^{qV_A/k_B T} - 1 \right)$$

$$J_0 = A^* T^2 e^{-\Phi_{BN}/k_B T}$$



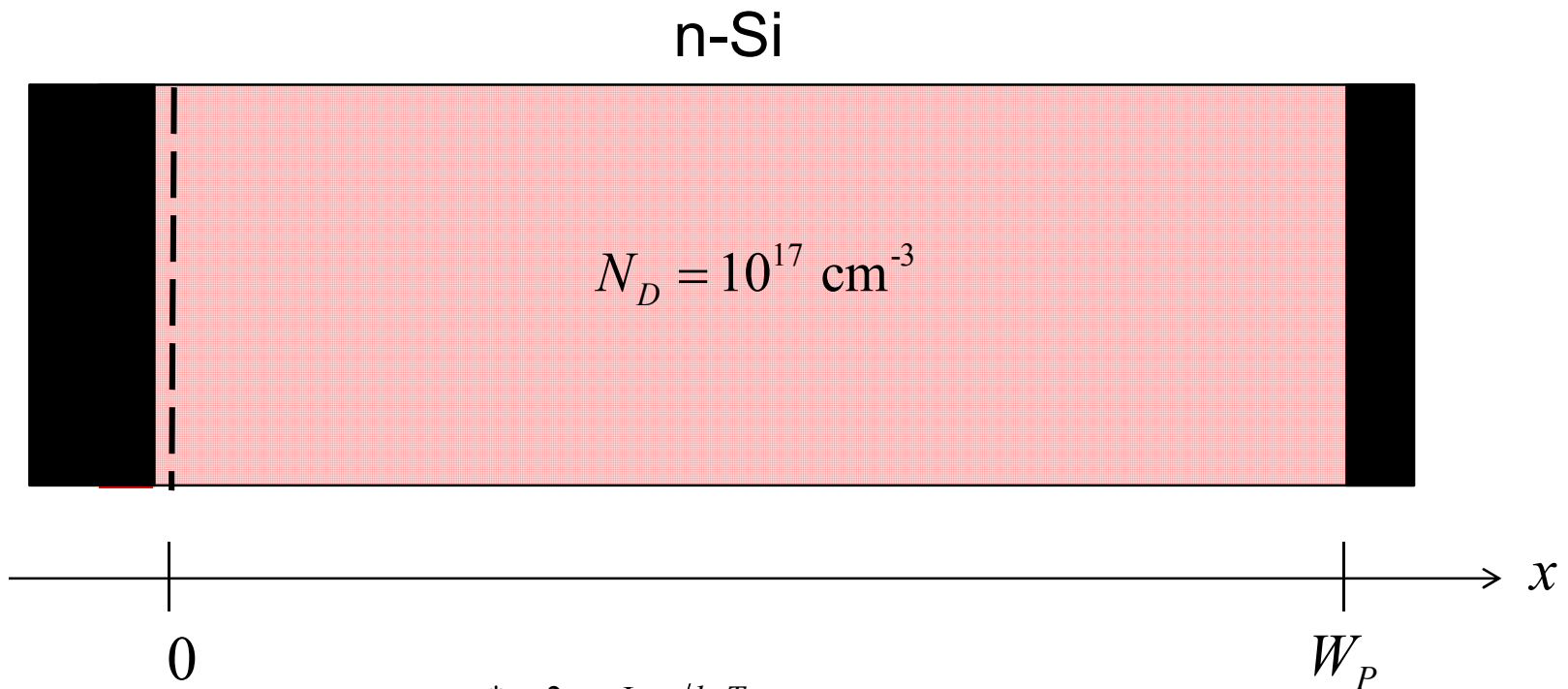
How does an MS diode compare to an NP junction?

# Recombination/Generation/Impact-ionization



**SAME technique as in p-n junction except integrate to  $x_p$  only**

# example: MS junction



$$J_0 = A^* T^2 e^{-\Phi_{BP}/k_B T}$$

$$m_n^*/m_0 = 1.18$$

$$\Phi_{BN} \approx 0.65 \text{ eV}$$

$$I_D = I_0 \left( e^{qV_A/k_B T} - 1 \right)$$

$$I_0 = 0.2 \times 10^{-3} \text{ A}$$

# application

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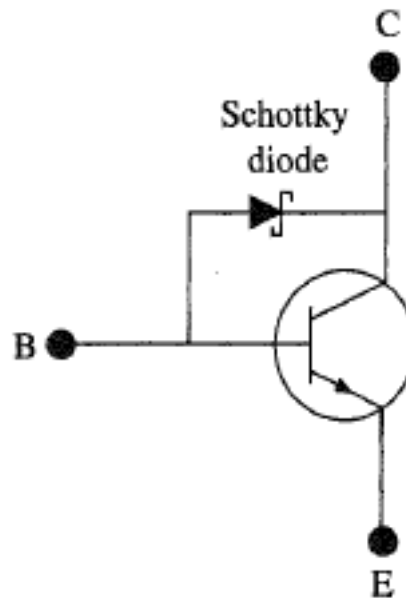


Fig. 14.9 Pierret, Semiconductor Device Fundamentals



# CSD01060

## Silicon Carbide Schottky Diode

### ZERO RECOVERY® RECTIFIER

$V_{RRM}$	=	600 V
$I_F (T_c=135^\circ\text{C})$	=	2 A
$Q_c$	=	3.3 nC

#### Features

- 600-Volt Schottky Rectifier
- Zero Reverse Recovery Current
- Zero Forward Recovery Voltage
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on  $V_f$

#### Benefits

- Replace Bipolar with Unipolar Rectifiers
- Essentially No Switching Losses
- Higher Efficiency
- Reduction of Rectifier Heat Sink
- Parallel Devices Without Thermal Runaway

#### Applications

- Switch Mode Power Supplies
- Power Factor Correction
  - Typical PFC  $P_{out}$  : 100W-200W
- Motor Drives
  - Typical Power : 0.25HP-0.5HP

#### Package



TO-252-2

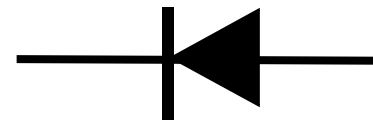
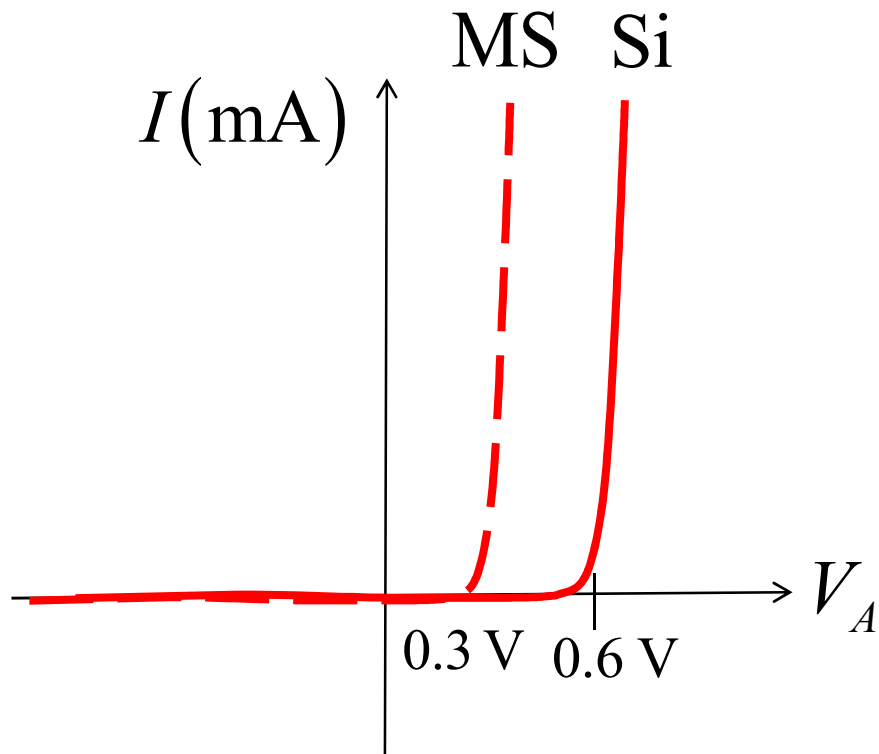


TO-220-2



Part Number	Package	Marking
CSD01060A	TO-220-2	CSD01060
CSD01060E	TO-252-2	CSD01060

# IV characteristics



$$J = J_0 \left( e^{qV_A/k_B T} - 1 \right)$$


$$J_0 = A^* T^2 e^{-\Phi_{BN}/k_B T}$$

$$J_0(MS) \gg J_0(NP)$$

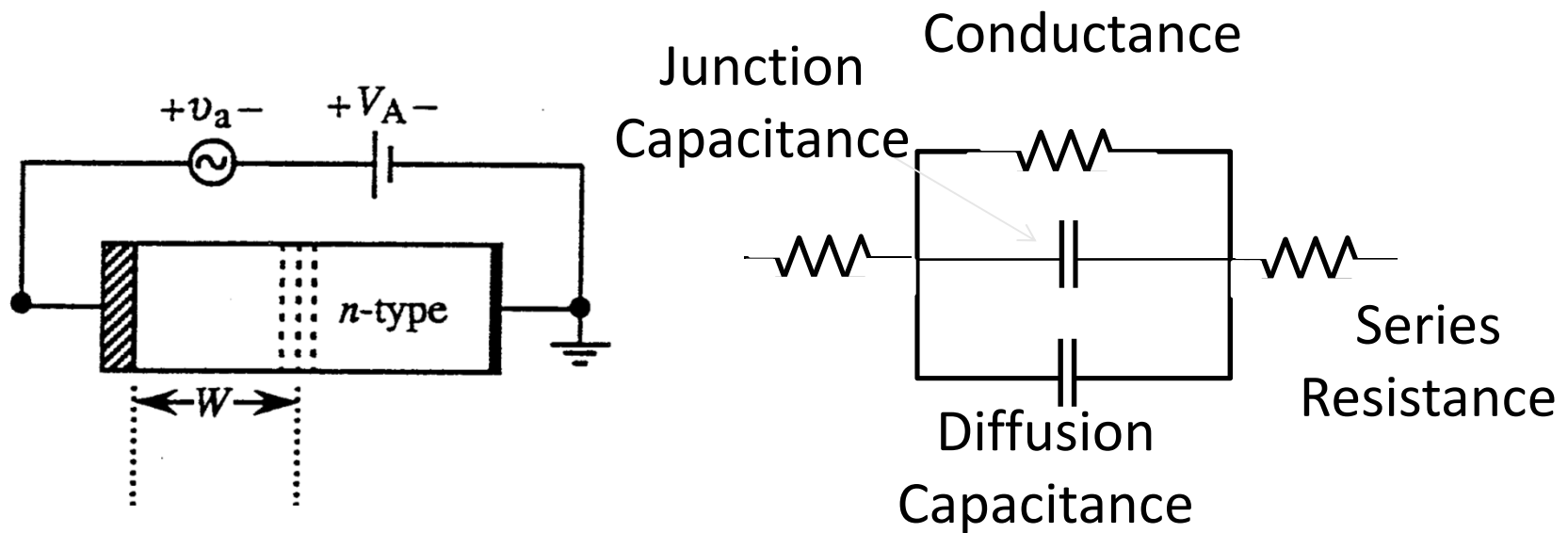
Strongly controlled by SB height.

# Topic Map

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	Equilibrium	DC	<b>Small signal</b>	Large Signal	Circuits
Diode					
<b>Schottky</b>					
BJT/HBT					
MOSFET					

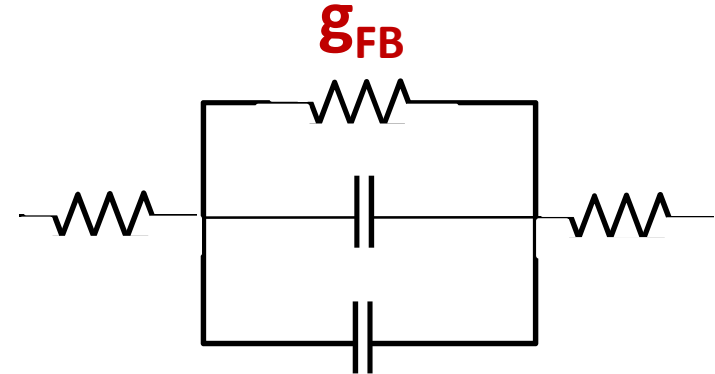
# AC response





# Forward Bias Conductance

$$I = I_o \left( e^{q(V_A - R_S I) / mk_B T} - 1 \right)$$

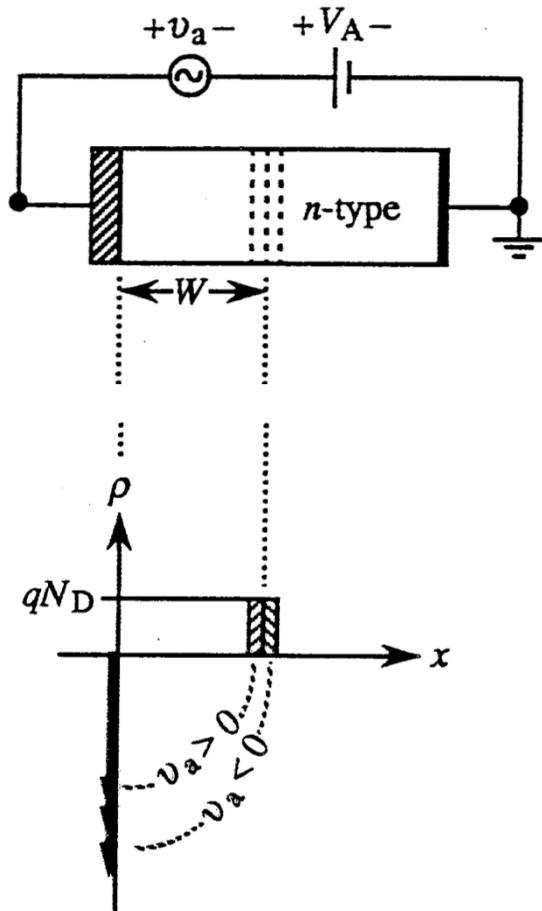


$$\ln \left( \frac{I + I_o}{I_o} \right) = q(V_A - R_S I) / mk_B T$$

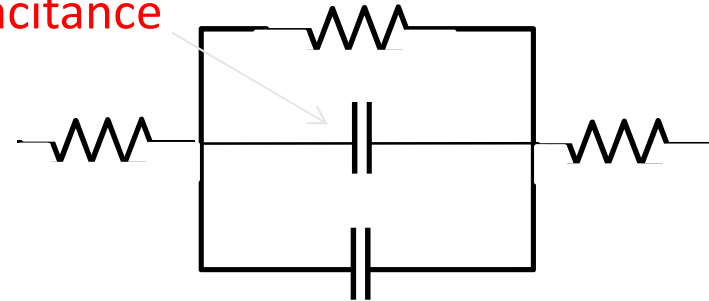
$$\frac{mk_B T}{q(I + I_o)} = \frac{dV_A}{dI} - R_S$$

$$\frac{1}{g_{FB}} = R_S + \frac{m}{q\beta(I + I_o)}$$

# Junction Capacitance (Majority Carriers)



Junction  
Capacitance

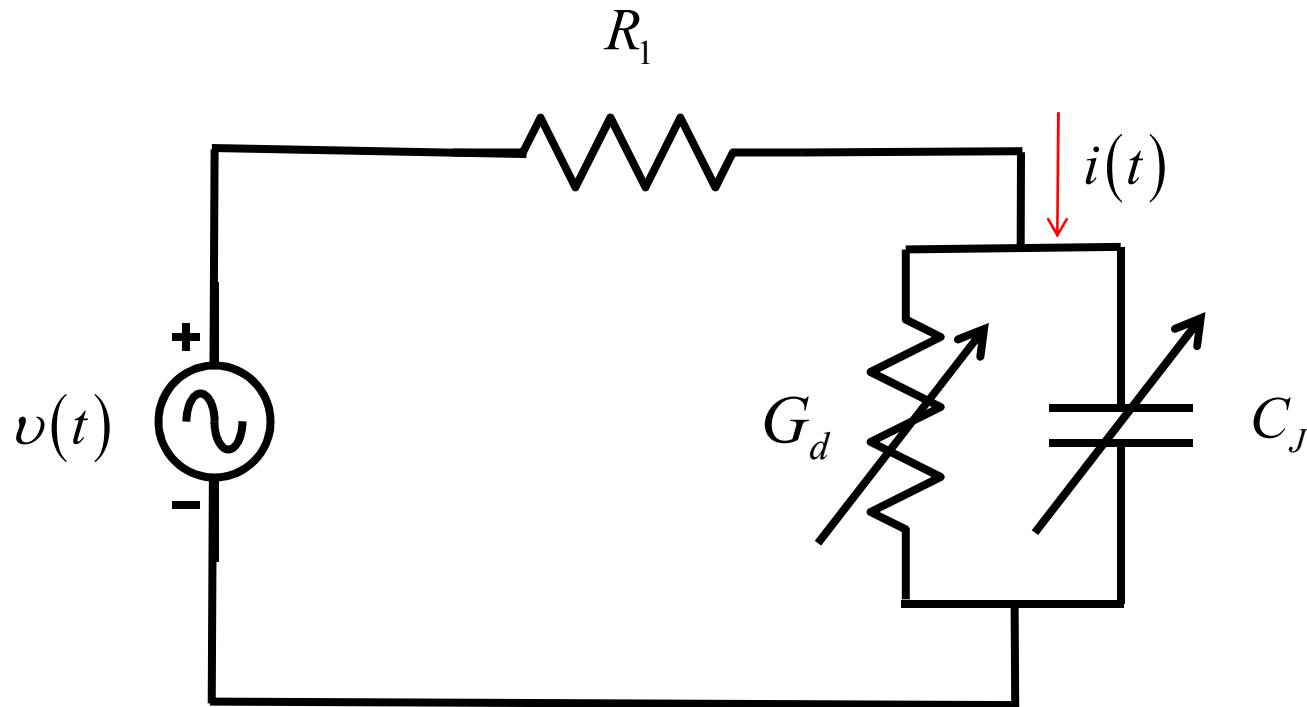


$$C_J = \frac{\kappa_s \epsilon_0 A}{W}$$

$$C_J = \frac{\kappa_s \epsilon_0 A}{\sqrt{\frac{2\kappa_s \epsilon_0}{qN_D} (V_{bi} - V_A)}}$$

# small signal model (MS diode)

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**No diffusion capacitance!**

$$G_d = \frac{dI_D}{dV_D}$$

$$C_J(V_A) = \frac{K_S \epsilon_0 A}{W(V_A)}$$

# summary

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- 1) A Schottky diode is a metal-semiconductor majority carrier device
- 2) It is similar to a one-sided p-n junction transistor, since the electron concentration in metals is very high.
- 3) Current is calculated using thermionic emission
- 4) M-S diodes typically have much higher saturation current and no diffusion capacitance, making them very fast