NAME:

PUID:\_\_\_\_\_

# ECE 305 - Spring 2018

Exam 4 – Thursday, April 12, 2018

This is a closed book exam. You may use a calculator and the formula sheet at the end of this exam. Following the ECE policy, the calculator **must** be a Texas Instruments TI-30X IIS scientific calculator.

To receive full credit, you must **show your work** (scratch paper is attached).

The exam is designed to be taken in 60 minutes (or less). Be sure to fill in your name and Purdue student ID at the top of the page. DO NOT open the exam until told to do so, and stop working immediately when time is called. The last 2 pages are equation sheets, which you may remove, if you want.

## 100 points possible,

- I) 40 points (8 points per question)
- II) 30 points
- III) 30 points

Course policy		
If I'm caught cheating, I'll earn an F in the course & be reported to the Dean of Students.		
I repeat:		
Gradar		

### Part I: Answer the 5 multiple choice questions below by entering them on your IDP-15 Scantron.

1 (8 points). Gradually introducing a limited number of mobile positive charges into the gate oxide of a MOS-capacitor would:

- a. Make the oxide highly conductive
- b. Erode the oxide over time, leaving an empty gap between the gate and channel
- c. Dope the silicon substrate
- d. Change the threshold voltage over time
- e. Change the flatband voltage with time

2 (8 points). For a polycrystalline Si gate MOS capacitor, changing the doping in the crystalline Si substrate would change the threshold voltage as

- a.  $V_{FB}$  changes
- **b.**  $\Phi_F$  changes
- c.  $\Delta V_{OX}$  changes
- d. a. and c. only
- e. All of the above

3 (8 points). When  $V_G=V_T$  for an n-MOSFET, what is the value of the semiconductor potential  $\Phi_S$ ?

- a. 0.6 Φ<sub>F</sub>
- b. 1.2 Φ<sub>F</sub>
- c. 1.5 Φ<sub>F</sub>
- d. 2Φ<sub>F</sub>
- e. 2.4 Φ<sub>F</sub>

4 (8 points) How does the saturated current of a n-channel MOSFET in the long-channel regime vary with the gate voltage  $V_{GS}$ ?

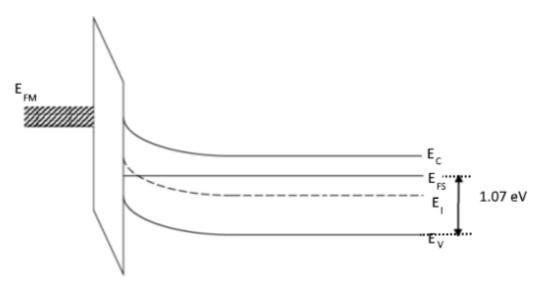
- a.  $(V_{GS} V_T)^2$
- b.  $(V_{GS} V_T)^{1.5}$
- c.  $(V_{GS} V_T)^{1.0}$
- d.  $(V_{GS} V_T)^{0.5}$
- e.  $(V_{GS} V_T)^{-2}$

5 (8 points). When minority free carriers are found near the oxide-Si interface of a MOS device, what is the bias condition?

- a. Depletion
- b. Inversion
- c. Flatband
- d. Deep accumulation
- e. Accumulation

#### Part II (Free Response, 30 points)

Consider an MOS capacitor made of crystalline silicon described by the band diagram below. Note that unmarked values may not be to scale. Assume that  $|\Delta V_{OX}| = 0.9 \text{ V}$ ,  $t_{ox} = 3.0 \text{ nm}$ ,  $K_{ox} = 3.9$ ,  $K_{Si} = 11.7$ , and  $E_q = 1.12 \text{ eV}$ .

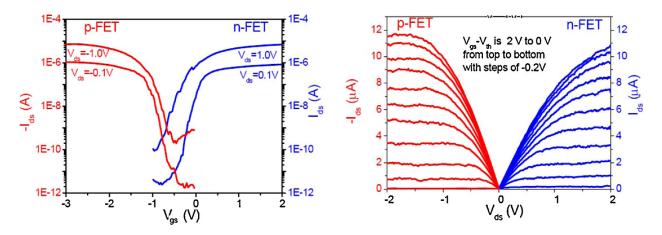


- a. What is the magnitude of the electric field in the **semiconductor** right next to the oxide  $(x = 0^+)$ ?
- b. What is the magnitude of the electrostatic potential voltage of the gate electrode with respect to the semiconductor, assuming the flat band voltage V<sub>FB</sub> is 0? Assume that  $N_D = 4.7 \cdot 10^{18}$  cm<sup>-3</sup>.

c. For the above diagram, which regime is the capacitor operating in? If the gate voltage is kept constant, but the oxide thickness is reduced gradually, eventually the operating regime changes. What would be the new operating regime?

#### Part III (Free Response, 30 points)

Consider a pair of p-channel (left) and n-channel (right) carbon nanotube-based MOSFETs, characterized in terms of drain current as a function of gate voltage and drain voltage.



- a. Estimate the saturation threshold voltage  $(V_{T,SAT})$  for both types of logic. Justify your answer.
- b. Estimate the drain-induced barrier lowering for both types of logic.
- c. How can the asymmetry in the output characteristics for comparable  $V_G V_T$  values be explained?
- d. Find and mention the best performing device here in terms of subthreshold slope. State the subthreshold slope and the operating conditions to obtain that optimum performance.
- e. Is this carbon nanotube MOSFET superior to a conventional crystalline Si MOSFET? Justify your answer.

#### ECE 305 Exam 4 Formula Sheet (Spring 2018)

You may remove these pages from the exam packet, and take them with you.

Physical Constants	Silicon parameters ( $T = 300$ K)
$h/2\pi = \hbar = 1.055 \times 10^{-34} \text{ J} \cdot \text{s}$	$N_C = 3.23 \times 10^{19} \mathrm{cm}^{-3}$
$m_0 = 9.109 \times 10^{-31} \text{ kg}$	$N_V = 1.83 \times 10^{19} \text{ cm}^{-3}$
$k_B = 1.38 \times 10^{-23} \text{ J/K}$	$n_i = 1.1 \times 10^{10} \text{ cm}^{-3}$
$q = 1.602 \times 10^{-19} \mathrm{C}$	$K_{s} = 11.8$
$\epsilon_0 = 8.854 \times 10^{-12} \text{ F/m}$	$E_g = 1.12 \text{ eV}; \ \chi = 4.03 \text{ eV}$

Density of states  $g_{\mathcal{C}}(E) = \frac{(m_n^*)^{3/2}\sqrt{2(E-E_{\mathcal{C}})}}{\pi^2 \hbar^3}$ Miller Indices: (hkl) {hkl} [hkl] <hkl> Intrinsic carrier concentration  $n_i = \sqrt{N_C N_V} e^{-E_g/2kT}$ Fermi function  $f(E) = \frac{1}{1 + e^{(E-E_E)/kT}}$ Equilibrium carrier densities:  $N_C = \frac{1}{4} \left(\frac{2m_n^*kT}{\pi\hbar^2}\right)^{3/2}$   $N_V = \frac{1}{4} \left(\frac{2m_p^*kT}{\pi\hbar^2}\right)^{3/2}$  $n_0 = N_C e^{(E_F - E_C)/kT} = n_i e^{(E_F - E_i)/kT}$  $p_0 = N_V e^{(E_V - E_F)/kT} = n_i e^{(E_i - E_F)/kT}$ Space charge neutrality:  $p - n + N_D^+ - N_A^- = 0$  Law of Mass Action:  $n_0 p_0 = n_i^2$ Non-equilibrium carriers:  $n = N_C e^{(F_N - E_C)/kT}$   $p = N_V e^{(E_V - F_P)/kT}$   $np = n_i^2 e^{(F_N - F_P)/kT}$ **Conductivity/resistivity**:  $\sigma = \sigma_n + \sigma_n = q(n\mu_n + p\mu_n) = 1/\rho$ **Drift-diffusion current equations:**  $J_n = nq\mu_n \mathcal{E}_x + qD_n \frac{dn}{dx} = n\mu_n \frac{dF_n}{dx}$  $\frac{D_n}{\mu_n} = \frac{kT}{a}$  $J_p = pq\mu_p \mathcal{E}_x - qD_p \frac{dp}{dx} = p\mu_p \frac{dF_p}{dx}$  $\frac{D_p}{u_n} = \frac{kT}{q}$  $\frac{\partial n}{\partial t} = +\nabla \cdot \left(\frac{J_n}{a}\right) + G_n - R_n$ Carrier conservation equations:  $\frac{\partial p}{\partial t} = -\nabla \cdot \left(\frac{J_p}{a}\right) + G_p - R_p$  $\nabla \cdot (\epsilon E) = 0$ **Poisson's equation:**  $R = \Delta n / \tau_n$  or  $R = \Delta p / \tau_n$ SRH carrier recombination: Minority carrier diffusion equation:  $\frac{\partial \Delta n}{\partial t} = D_n \frac{\partial^2 \Delta n}{\partial x^2} - \frac{\Delta n}{\tau_n} + G_L$  $L_{D,n} = \sqrt{D_n \tau_n}$  $V_{bi} = \frac{kT}{a} \ln \left( \frac{N_D N_A}{n_i^2} \right) \qquad \qquad \frac{d\varepsilon}{dx} = \frac{\rho(x)}{K_c \epsilon_o}$ PN homojunction electrostatics:  $W = \sqrt{\frac{2K_s \epsilon_o V_{bi}}{q} \left(\frac{N_A + N_D}{N_A N_D}\right)} \qquad x_n = \left(\frac{N_A}{N_A + N_D}\right) W \qquad x_p = \left(\frac{N_D}{N_A + N_D}\right) W \qquad \mathcal{E}(0) = \sqrt{\frac{2qV_{bi}}{K_s \epsilon_o} \left(\frac{N_A N_D}{N_A + N_D}\right)}$ 

ECE 305 Exam 4 – Prof. Peter Bermel

**PN diode current:**  $\Delta n(0) = \frac{n_i^2}{N_A} \left( e^{qV_A/kT} - 1 \right) \qquad \Delta p(0) = \frac{n_i^2}{N_B} \left( e^{qV_A/kT} - 1 \right)$  $J_D = J_o \left( e^{qV_A/kT} - 1 \right) \qquad J_o = q \left( \frac{D_n}{L_n} \frac{n_i^2}{N_A} + \frac{D_p}{L_n} \frac{n_i^2}{N_D} \right) \text{ (long)} \qquad J_o = q \left( \frac{D_n}{W_n} \frac{n_i^2}{N_A} + \frac{D_p}{W_n} \frac{n_i^2}{N_D} \right) \text{ (short)}$ Non-ideal diodes:  $I = I_o (e^{q(V_A - IR_s)/kT} - 1)$   $J_{gen} = -q \frac{n_i}{2\tau_o} W$ Photovoltaics:  $V_{oc} = \frac{nkT}{a} \ln \left( \frac{J_{sc}}{L} \right) \qquad J_{PV} = J_o \left( e^{qV_A/kT} - 1 \right) - J_{sc}$ Small signal model:  $G_d = \frac{I_D + I_o}{kT/q}$   $C_J(V_R) = \frac{K_S \epsilon_o A}{\sqrt{\frac{2K_S \epsilon_o V_{bi}}{qN_s}}} = A_{\sqrt{\frac{qK_S \epsilon_o N_A}{2V_{bi}}}}$   $C_D = G_d \tau_n$ 

**MS diode properties:**  $qV_{bi} = |\Phi_M - \Phi_S|$   $\Phi_{BP} = \chi + E_G - \Phi_M$   $\Phi_{BN} = \Phi_M - \chi$  $J_D = J_o \left( e^{qV_A/kT} - 1 \right) \qquad J_o = A^* T^2 e^{-\Phi_B/kT} \qquad A^* = \frac{4\pi q m^* k_B^2}{h^3} = 120 \frac{m^*}{m_o} \frac{A}{\mathrm{cm}^2 \mathrm{K}^2}$ 

**MOS capacitors:**  $W = \sqrt{\frac{2K_s \epsilon_o \phi_s}{qN_A}}$   $\mathcal{E}_s = \sqrt{\frac{2qN_A \phi_s}{K_s \epsilon_o}}$  (for p-type substrates)  $Q_B = -qN_AW(\phi_s) = -\sqrt{2qK_s\epsilon_oN_A\phi_s} \frac{C}{cm^2}$  $V_G = V_{FB} + \phi_s + \Delta \phi_{ox} = V_{FB} + \phi_s - \frac{Q_s(\phi_s)}{C_{ox}}$  $C_{ox} = K_o \epsilon_o / x_o \qquad \qquad V_{FB} = \Phi_{ms} / q - Q_F / C_{ox}$  $C = C_{ox} / \left[ 1 + \frac{K_o W(\phi_s)}{K_s x_o} \right] \qquad V_T = -Q_B (2\phi_F) / C_{ox} + 2\phi_F \qquad Q_n = -C_{ox} (V_G - V_T)$  $I_D = -WQ_n(y=0)\langle v_y(y=0)\rangle$ **MOSFETs:** 

 $I_{D} = \frac{W}{L} \mu_{n} C_{or} (V_{GS} - V_{T}) V_{DS} \qquad I_{D} = W C_{or} v_{sat} (V_{GS} - V_{T})$ 

Square Law (for 
$$V_{GS} \ge V_T$$
):  $I_D = \begin{cases} \frac{W}{L} \mu_n C_{ox} [(V_{GS} - V_T) V_{DS} - V_{DS}^2/2], & 0 \le V_{DS} \le V_{GS} - V_T \\ \frac{W}{2L} \mu_n C_{ox} (V_{GS} - V_T)^2, & V_{DS} \ge V_{GS} - V_T \end{cases}$