

# Temperature Profile Inside Microscale Thermoelectric Module Acquired Using Near-Infrared Thermoreflectance

Rajeev Singh, Joachim Nurnus, Zhixi Bian, James Christofferson, and Ali Shakouri

**Abstract**—We present high-resolution thermal images of the electrical contacts inside an active thermoelectric micromodule that are acquired through the silicon substrate using near-infrared thermoreflectance. The temperature distribution of the contacts induced by the Peltier and Joule effects are separated. This technique can be utilized as a nondestructive means to evaluate metallization and contact resistance of micrometer-scale thermoelectric modules. By determining localized sources of Joule heating, one can identify manufacturing errors and generate design rules that can improve the cooling performance of the thermoelectric device.

**Index Terms**—Contacts, infrared (NIR) imaging, thermoelectric devices, thermo-optic effects, wafer bonding.

## I. INTRODUCTION

AS THE transistor densities and clock speeds of integrated circuits increase, their high power dissipation densities demand stringent cooling requirements. In particular, the nonuniform power distributions and the large heat flux densities of modern integrated circuits necessitate cost-effective cooling solutions capable of large cooling power densities at high spatial resolutions. Bulk thermoelectric cooling modules cannot effectively target localized regions of heating in integrated circuits due to their large surface areas and relatively low cooling power densities on the order of  $1\text{--}5\text{ W}\cdot\text{cm}^{-2}$ , mainly limited by their long element lengths [1]. Thermoelectric micromodules can efficiently solve these problems by incorporating smaller cold-side areas for localized cooling with shorter leg lengths for greater cooling power densities on the order of  $100\text{ W}\cdot\text{cm}^{-2}$ . In addition, thermoelectric micromodules have a fast transient response on the order of  $10^{-3}$  seconds and can therefore provide high-frequency cooling for specific applications [2].

A thermoelectric module's coefficient of performance (COP) is a strong function of its element length due to the parasitic

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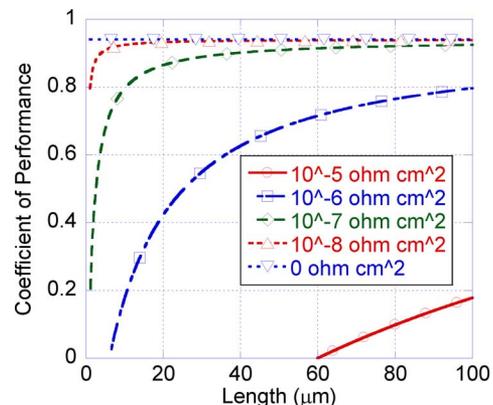


Fig. 1. COP of an n-element  $\text{Bi}_2\text{Te}_3$ -based thermoelectric module as a function of element length for different contact resistivities and for the maximum COP case of zero contact resistance.

effect of contact and metallization resistance. Fig. 1 is a plot of the calculated COP versus element length for a thermoelectric module composed of conventional  $\text{Bi}_2\text{Te}_3$  material (n- and p-type material properties are assumed identical other than opposite Seebeck coefficient polarity) for cold- and hot-side temperatures of 275 and 300 K, respectively, and for contact resistivities ranging from  $10^{-5}\ \Omega\text{cm}^2$  to  $10^{-8}\ \Omega\text{cm}^2$  [3]. The calculation only considers the parasitic effect of finite contact resistivity, and the maximum COP for the case of zero contact resistance is also plotted.

We utilize near-infrared (NIR) thermoreflectance through silicon substrate to determine the temperature distribution on the cold-side electrical contacts of an active thermoelectric module whose elements are  $20\ \mu\text{m}$  long. Lock-in detection at the first and second harmonics of the device excitation frequency permit the extraction of the temperature distributions due to the Peltier and Joule effects. This technique can be useful for nondestructive characterization of the metallization inside thermoelectric modules by detecting localized regions of excessive ohmic heating and can also be used to determine Peltier cooling uniformity. Since the thermoelectric micromodules are fabricated using a wafer-scale approach, NIR thermoreflectance can be used as a diagnostic tool to improve fabrication processes to maximize the COP of the devices.

## II. THERMOELECTRIC MICROMODULE FABRICATION

The fabrication of thermoelectric micromodules is accomplished using a wafer-scale approach to maximize yield and reduce costs [4]. Micrometer-scale thermoelectric modules are

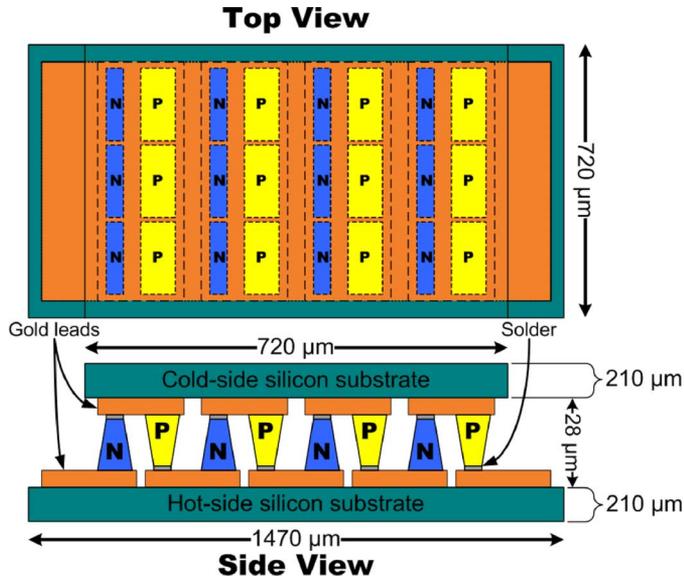


Fig. 2. Schematic of an eight element thermoelectric micromodule in top-view (cutaway) and side view (not to scale). The length of the thermoelectric elements is  $20 \mu\text{m}$ .

fabricated by deposition of complementary contacts for many modules onto standard 4-in semi-insulating silicon wafers to form the two sides of the modules (an n-type side and a p-type side). N- and p-type thermoelectric materials based on  $\text{Bi}_2\text{Te}_3$  are then deposited via sputtering on the contacts of the corresponding wafers. A solder material is then deposited onto the thermoelectric material before the layers are dry-etched to form elements. Next, the wafers are diced to form the halves of many modules. Finally, the n-type half-module and the p-type half-module are joined via soldering. A schematic of an eight-element thermoelectric micromodule designed for size-matched cooling of lasers and sensors is shown in Fig. 2. The length of the thermoelectric elements in the investigated module is  $20 \mu\text{m}$ . Although the module used in the experiments presented here is from an older design generation that is capable of a maximum cooling temperature difference of approximately 39 K at a hot-side temperature of 300 K, current state-of-the-art modules are reported to achieve a maximum cooling temperature difference of over 60 K at 300 K [5].

### III. THERMOREFLECTANCE MICROSCOPY

Thermoreflectance microscopy is a technique frequently applied to measure surface temperature maps of active semiconductor devices with high spatial and temperature resolutions. Thermoreflectance utilizes the finite temperature dependence of the surface reflectivity of materials to determine the surface temperature. By measuring the change of the illumination intensity reflected from a surface due to a temperature change, the thermal image can be obtained. The first-order approximation of the relationship between the change in surface temperature and the change in surface reflectivity is given as

$$\Delta T = \left( \frac{1}{R} \frac{\partial R}{\partial T} \right)^{-1} \frac{\Delta R}{R} = \kappa^{-1} \frac{\Delta R}{R} \quad (1)$$

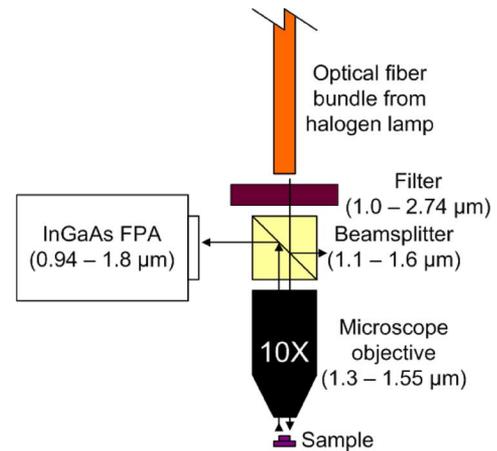


Fig. 3. Diagram of the NIR thermoreflectance experiment showing all optical components and their greater than 50% transmission windows. The indicated FPA window is for a detector responsivity greater than 0.05 A/W.

where  $\Delta T$  is the change in surface temperature in units K,  $R$  is the reflected illumination intensity from the surface of the sample, and  $\kappa$  is the thermoreflectance coefficient in units  $\text{K}^{-1}$  [6]. The value of  $\kappa$  depends on the sample's surface material, the wavelengths of the illumination source, and the experimental conditions such as the numerical aperture of the microscope objective used for magnification [7]. The  $\kappa$  for a particular experimental arrangement can be determined by measuring  $\Delta R$  for a known  $\Delta T$ . Although the thermoreflectance technique is reported to achieve a 10-mK temperature resolution and a 250-nm spatial resolution using illumination at 467 nm [8], there is little published work in the application of thermoreflectance at wavelengths in the infrared regime.

### IV. NIR THERMOREFLECTANCE

Longer wavelength thermoreflectance can be useful due to the ability to image through semiconductor material using illumination wavelengths whose energies are below the bandgap of the material. Many state-of-the-art CMOS integrated circuits are flip-chip bonded to their package leads, prohibiting visible access to the device contacts. In this case, thermoreflectance can be applied at wavelengths whose energies are below the bandgap of the device substrate to image the temperature distribution on the contacts through the backside of the device. An initial report of backside thermoreflectance imaging at wavelengths in the NIR regime using coherent sources of illumination is complicated by Fabry-Perot effects inside the substrate and is limited to scanned point measurements [9]. Subsequent work on NIR thermoreflectance utilizes broadband incoherent illumination and an InGaAs focal-plane array (FPA) imager along with a numerical aperture-increasing lens (NAIL) to solve many problems associated with NIR thermoreflectance [10]. The work presented here utilizes incoherent NIR illumination along with a FPA imager to conduct thermoreflectance experiments on an active thermoelectric micromodule. The simple experimental configuration is shown in Fig. 3 along with the greater than 50% transmission windows of all optical components and the FPA window for detector responsivity greater than 0.05 A/W. A halogen lamp coupled to a fiber optic bundle is used as the illumination source.

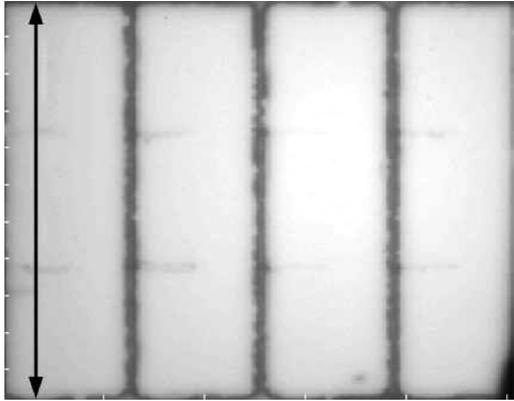


Fig. 4. DC image of the top of the cold-side gold contacts of the microthermoelectric device acquired through the cold-side silicon substrate. The arrow indicates the position of the cross-sectional line-scan shown in Fig. 6.

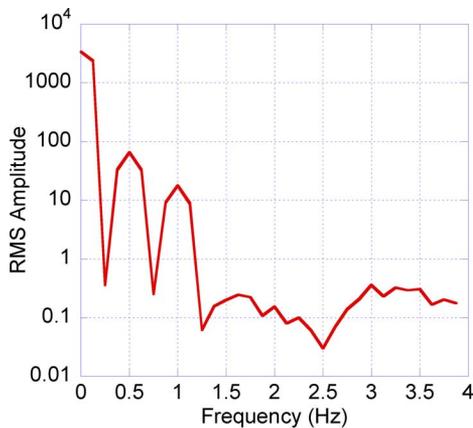


Fig. 5. FFT rms amplitude spectrum of the thermoreflectance signal from a point on the device cold-side gold contact due to a sinusoidal current at 0.5 Hz of 861-mA amplitude with no dc offset. This current amplitude corresponds to Peltier- and Joule-induced  $\Delta T$ s of 58 and 31 K, respectively. The FPA camera is operated at a frame rate of 8 Hz with a measurement time constant of 8 s.

Subsequent filtration of the halogen source removes the visible components from its spectrum. The thermoelectric sample device is mounted on a *xyz* translation stage for positioning and focusing. The sample stage is heated to 320 K in order to avoid condensation of water on the thermoelectric device due to the low cold-side temperatures that it can achieve relative to ambient in the cooling mode.

## V. RESULTS

Fig. 4 is a dc image of the cold-side gold contacts of the microthermoelectric device acquired through the silicon substrate. The four individual gold contacts are clearly seen in the image. In order to acquire the ac signal resulting from steady-state temperature variations due to the Peltier and Joule effects, the device is activated with an ac current at a frequency of 0.5 Hz at various amplitudes with no dc component. The FPA camera is operated at a frame rate of 8 Hz with an 8-s measurement time constant. Lock-in detection is used to determine the thermoreflectance signal amplitude at the first (Peltier) and second (Joule) harmonics. Fig. 5 is the fast Fourier transform (FFT) spectrum of the thermoreflectance signal from a point on the device's contact metallization. The spectrum contains peaks due

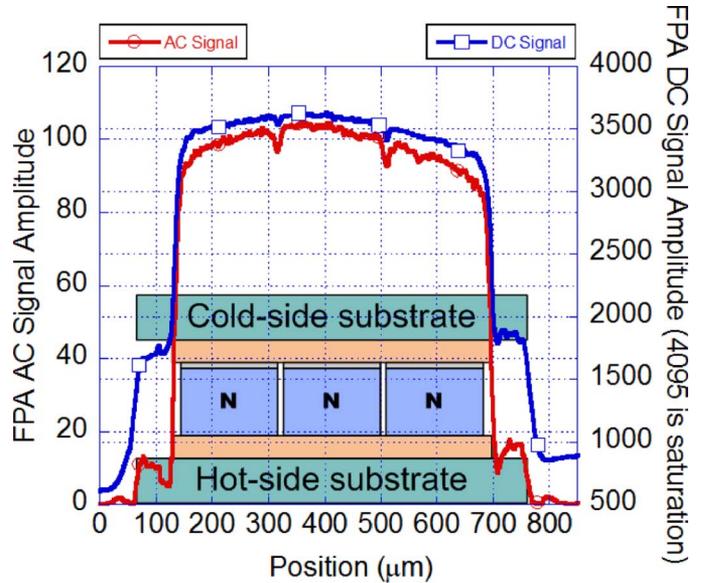


Fig. 6. Cross-section of the dc and ac thermoreflectance signal amplitudes due to the Peltier effect for a sinusoidal current amplitude of 861 mA, corresponding to a  $\Delta T$  of 58 K. The average noise floor of the FPA is approximately 72.

to Peltier and Joule effect-induced temperature variations. The arrow in Fig. 4 indicates the position of the scan presented in Fig. 6 that shows the dc and ac thermoreflectance signal at the first harmonic acquired across the device. It is clear from Fig. 6 that there is a significant amount of reflection from the silicon. Although dc reflection is subject to variations in surface illumination, the measured dc contrast ratio between a point on the substrate above a gold contact and a point on the substrate outside of the gold contact area is 2.13. Calibration of the effective  $\kappa$  above the buried gold-silicon interface is achieved by modulating the device temperature at 0.5 Hz in order to ensure isothermal conditions between the gold contacts and the top surface of the silicon substrate where a microthermocouple of 13- $\mu\text{m}$ -diameter leads is placed. A value of  $4.54 \times 10^{-4} \text{ K}^{-1}$  is obtained for the effective  $\kappa$  of the device above the gold contact. Assuming isothermal conditions on the device cold side, the calculated effective  $\kappa$  of the substrate outside of the gold contact area is  $1.01 \times 10^{-4} \text{ K}^{-1}$ . The average surface temperature of the four gold contacts due to the Peltier and Joule effects versus applied sinusoidal device current is shown in Fig. 7. The measured temperatures are in excellent agreement with the data obtained using a microthermocouple placed on top of the cold-side substrate. The temperature change measured with the microthermocouple due to the Peltier and Joule effects are extracted using dc currents of opposite polarities as follows:

$$\Delta T_{\text{Peltier}} = \left| \frac{\Delta T(+I_{DC}) - \Delta T(-I_{DC})}{2} \right| \quad (2)$$

and

$$\Delta T_{\text{Joule}} = \left| \frac{\Delta T(+I_{DC}) + \Delta T(-I_{DC})}{2} \right|. \quad (3)$$

Figs. 8 and 9 are the resulting thermal images due to the Peltier and Joule effects, respectively, for a sinusoidal device current amplitude of 861 mA at 0.5 Hz. In both images, the FPA camera

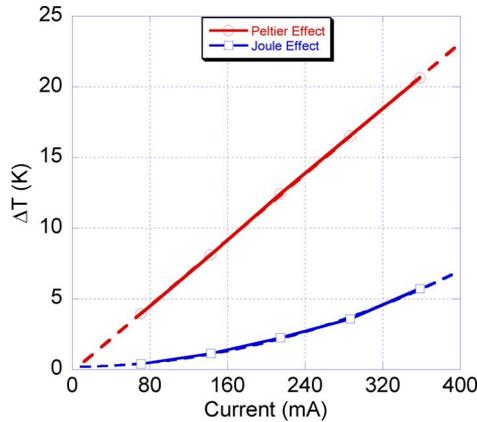


Fig. 7. Measured average thermoreflectance temperature above gold contacts versus applied sinusoidal device current at 0.5 Hz due to the Peltier and Joule effects (solid lines) for an effective  $\kappa = 4.54 \times 10^{-4} \text{ K}^{-1}$ . The  $\Delta T$  due to the Peltier and Joule effects are fitted (dashed line) to linear and second-order equations, respectively.

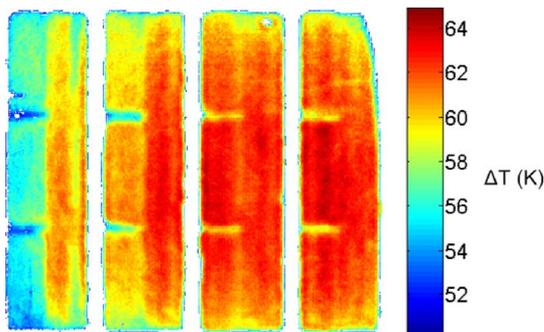


Fig. 8. Thermal image on the top of the gold contacts on the cold-side of the microthermoelectric module due to the Peltier effect for a sinusoidal current amplitude of 861 mA at 0.5 Hz.

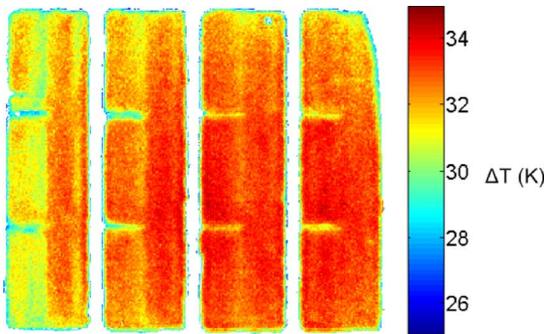


Fig. 9. Thermal image on the top of the gold contacts on the cold-side of the microthermoelectric module due to the Joule effect for a sinusoidal current amplitude of 861 mA at 0.5 Hz.

is operated in full-frame mode for a pixel count of  $320 \times 256$  which limits the acquisition rate to 20 Hz. The figures reveal a column of defective thermoelectric legs in the left-hand side of the module. For comparison, the FPA camera is windowed to  $128 \times 128$  pixels to increase the system frame rate to a maximum of 100 Hz. This enables device modulation frequencies to extend as high as approximately 25 Hz while capturing both Peltier and Joule effect-induced temperature distributions. The resulting lower spatial resolution thermal images are shown in

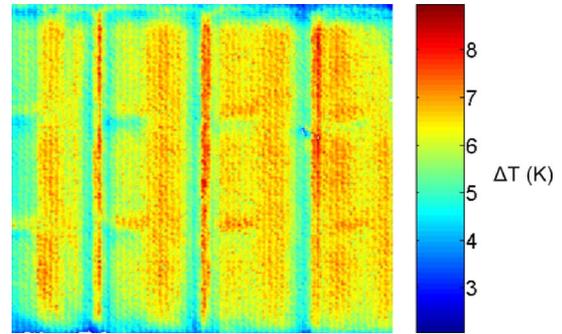


Fig. 10. Thermal image on the top of the gold contacts on the cold-side of the microthermoelectric module due to the Peltier effect for a sinusoidal current amplitude of 861 mA at 22 Hz.

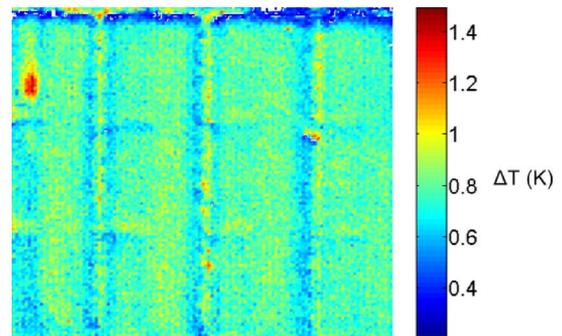


Fig. 11. Thermal image on the top of the gold contacts on the cold-side of the microthermoelectric module due to the Joule effect for a sinusoidal current amplitude of 861 mA at 22 Hz.

Figs. 10 and 11, respectively, for the Peltier and Joule effects for a measurement time constant of 2 s. For identical current amplitudes, the average surface  $\Delta T$  for device modulation at 22 Hz is substantially less than for device modulation at 0.5 Hz because the device cold-side cannot achieve the maximum  $\Delta T$  for a given current amplitude at the higher modulation frequency. As the device modulation frequency is increased, the measured  $\Delta T$  becomes increasingly localized to the source of heat flux. In this regard, we can obtain a more detailed knowledge of the sources of heat flux in the device. Six individual thermoelectric legs within the perimeter of each metal contact can be discerned in the Peltier effect-induced thermal image in Fig. 10. Although the temperature distribution shown in Fig. 11 due to the Joule effect is relatively uniform, a localized source due to parasitic Joule heating is revealed. This suggests that device processing relating to electric contact formation can be improved. In addition, the ratio of the measured  $\Delta T$  on the cold-side metallization due to the Peltier effect at 0.5 and 22 Hz is 10, while the ratio due to the Joule effect at the same frequencies is 32. Since the Peltier effect occurs at the metal-semiconductor interface (the point of thermoreflectance measurement), the frequency response of the Peltier effect-induced  $\Delta T$  will be greater than the frequency response of the Joule effect-induced  $\Delta T$  if the dominant source of Joule heating is not at the metal-semiconductor interface. Therefore, the dominant source of Joule heating in the module must either be in the bulk  $\text{Bi}_2\text{Te}_3$  material or at the hot-side metal-semiconductor interface.

## VI. CONCLUSION

We have utilized NIR thermoreflectance to image the temperature distribution on top of the cold-side metallization of a microthermoelectric module with  $20\ \mu\text{m}$  element length. The images presented are acquired through silicon substrate that is  $210\ \mu\text{m}$  thick. Incident illumination is filtered halogen light. The detector is an InGaAs FPA camera that achieves a maximum frame rate of 20 Hz for a  $320 \times 256$  pixel count and a maximum rate of 100 Hz for a  $128 \times 128$  pixel count in our acquisition system. An effective  $\kappa$  of  $4.54 \times 10^{-4}\ \text{K}^{-1}$  is measured for signals detected above the gold contact. A calculated value of the effective  $\kappa$  of the cold-side silicon substrate in regions outside of the gold area is  $1.01 \times 10^{-4}\ \text{K}^{-1}$ . By utilizing sinusoidal active device modulation in conjunction with lock-in detection, we successfully extract the temperature distributions on the embedded gold contacts due to the Peltier and Joule effects, even for device modulation frequencies as low as 0.5 Hz.

Because the COP of a thermoelectric device becomes a strong function of contact and interconnect resistance as the element length enters the micrometer regime, an important application of this technique is in the nondestructive evaluation of active micrometer-scale thermoelectric device performance.

Future work will focus on extending the NIR thermoreflectance measurement frequency response beyond what is afforded by the FPA camera through the use of heterodyne detection described in detail in [11]. Heterodyne detection at higher frequencies will permit the resolution of extremely localized heat fluxes in the device. An experimental arrangement will be devised to measure the temperature distribution on the hot-side electrical contacts of thermoelectric micromodules while the cold-side of the device is heat sunked. In addition, a high-power LED array emitting at  $1.55\ \mu\text{m}$  will be used as the illumination source in order to improve the contrast ratio between metal leads beneath the silicon and the silicon itself. The use of a narrow spectrum source centered at  $1.55\ \mu\text{m}$  can result in a more defined measurement of experimental  $\kappa$  at the metal-semiconductor interface. In this case, the use of a NAIL as described in [10] can increase the spatial resolution of the measurement.

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