

Negative Bias Temperature Instability (NBTI) in p-MOSFETs: Characterization, Material/Process Dependence and Predictive Modeling (Part 3 of 3)

Souvik Mahapatra

Department of Electrical Engineering

Indian Institute of Technology Bombay, Mumbai, India

Email: souvik@ee.iitb.ac.in; mahapatra.souvik@gmail.com

Co-contributors: M. A. Alam & A. E. Islam (Purdue), E. N. Kumar, V. D. Maheta, S. Deora, G. Kapila, D. Varghese, K. Joshi & N. Goel (IIT Bombay)

Acknowledgement: C. Olsen and K. Ahmed (Applied Materials), H. Aono, E. Murakami (Renesas), G. Bersuker (SEMATECH), CEN IIT Bombay, NCN Purdue, Applied Materials, Renesas Electronics, SEMATECH, SRC / GRC



Outline

Introduction, Basic NBTI signatures

Fast / Ultra-fast drain current degradation measurement

Estimation of pre-existing and generated defects

Transistor process / material dependence

Role of Nitrogen – Study by Ultrafast measurement

Predictive modeling

Conclusions / outlook



Part - I



Part - II



Part - III



Outline

Introduction, Basic NBTI signatures

Fast / Ultra-fast drain current degradation measurement

Estimation of pre-existing and generated defects

Transistor process / material dependence

Role of Nitrogen – Study by Ultrafast measurement

Predictive modeling

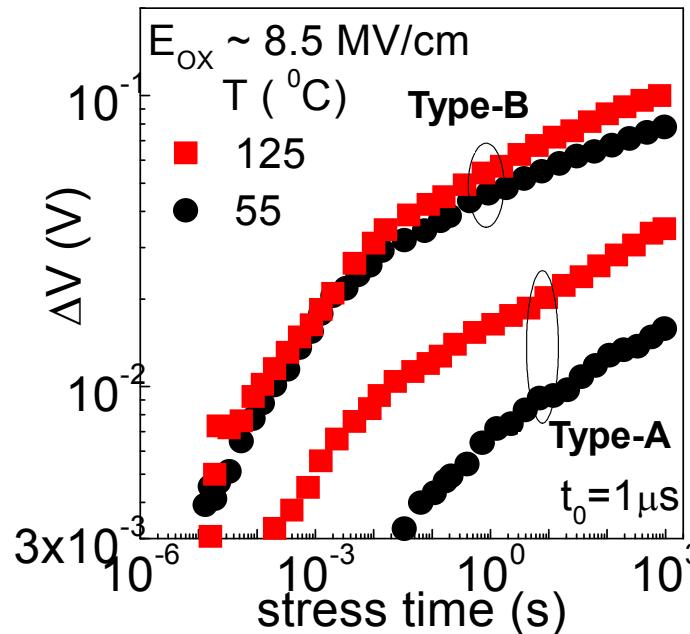
Stress ←

Recovery and AC effects

Conclusions / outlook



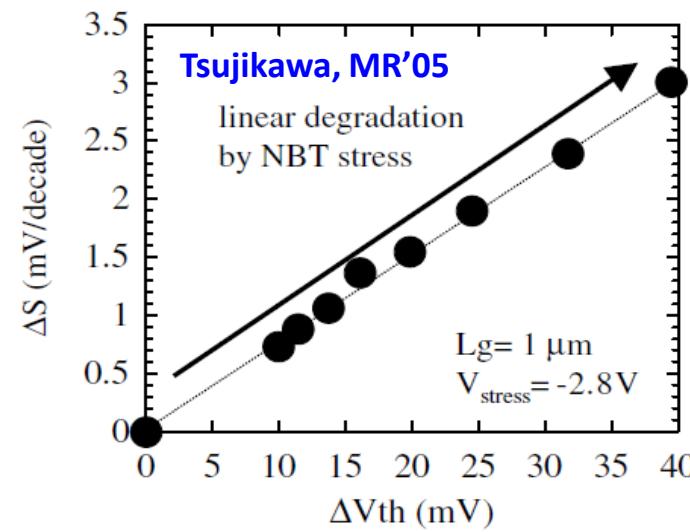
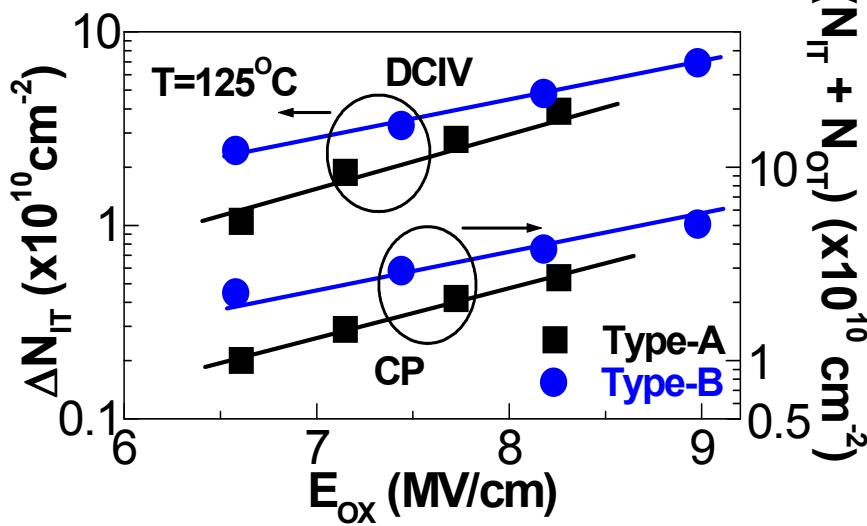
NBTI Experimental Signatures (1)



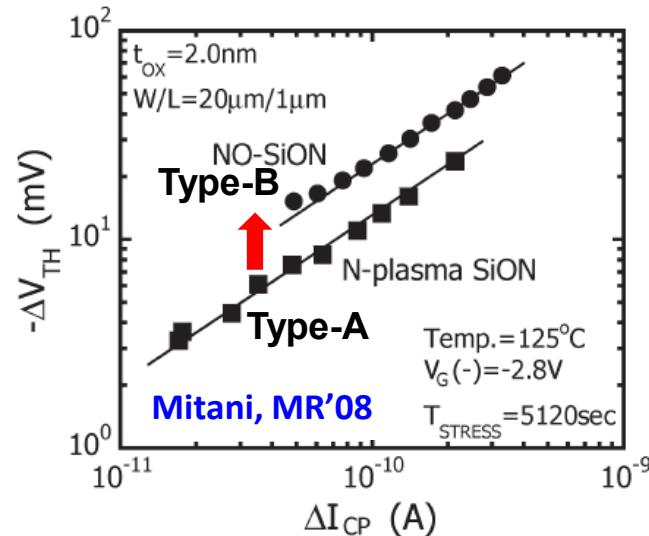
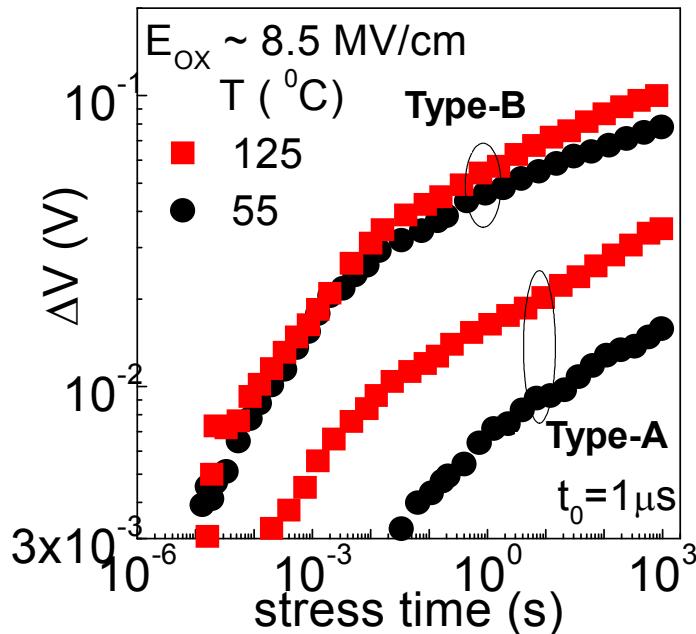
Strong gate insulator process dependence

Strong evidence of trap (interface and bulk) generation

Strong evidence of interface trap generation



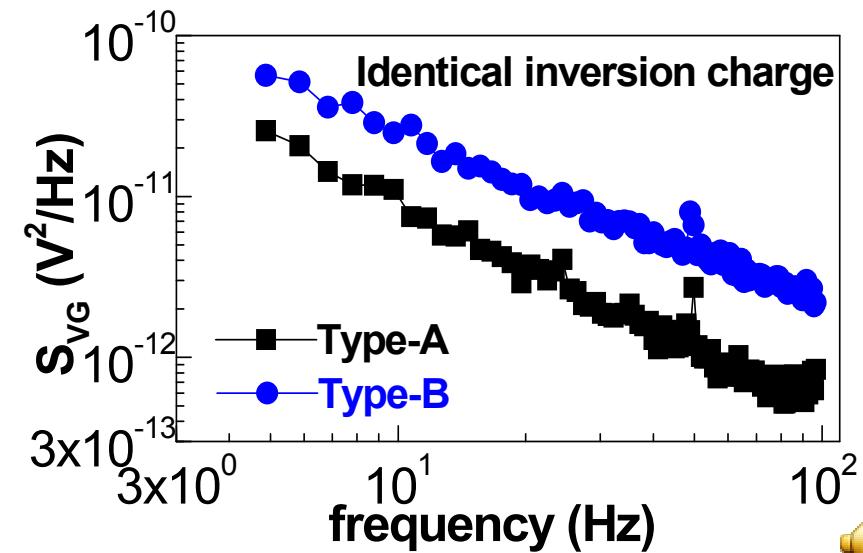
NBTI Experimental Signatures (2)



Higher V_T shift compared to trap generation for poor gate insulators

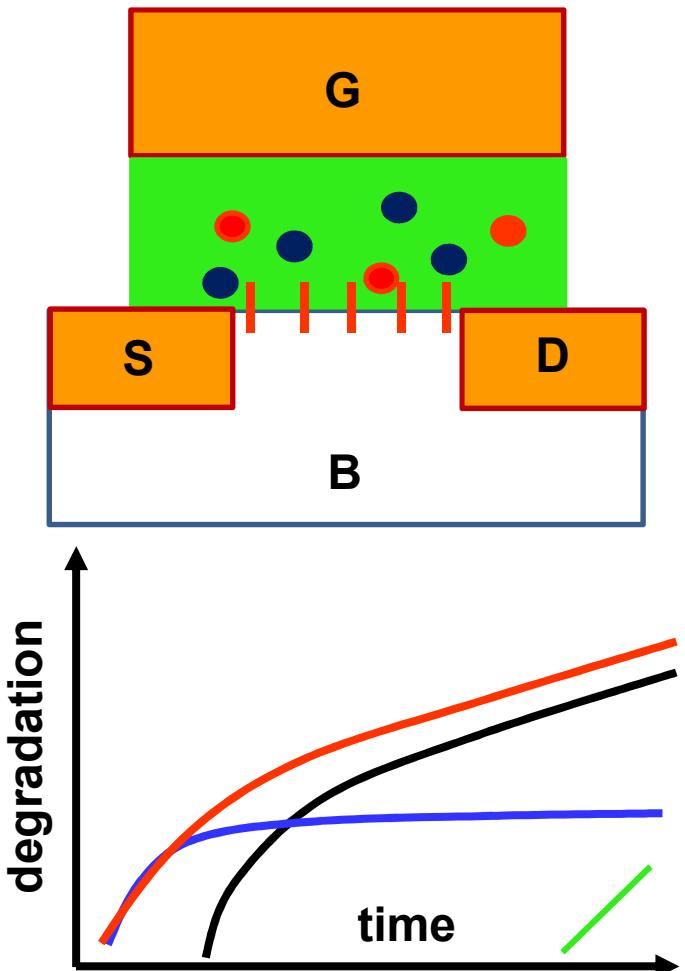
Higher as-processed bulk trap density in poor gate insulators

Strong evidence of trapping in as-processed bulk trap density



NBTI Degradation Model

Interface trap generation – inversion hole induced breaking of Si-H bonds at Si/SiON interface and subsequent diffusion of Hydrogen
(Reaction-Diffusion model) $\rightarrow \Delta V_{IT}$



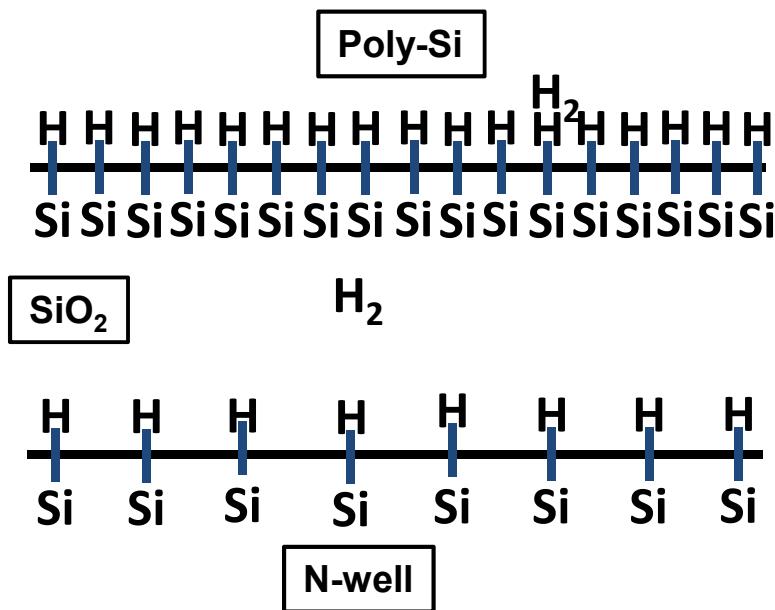
Charging of pre-existing bulk oxide traps – analytical expression to account for multiple trapping time constants; saturation at longer time

$$\Delta V_{HT} \sim (1 - \exp(-t/\tau)^{\beta})$$

Generation of bulk oxide traps (those cause TDDB failure) – analytical expression for power law time dependence: $\Delta V_{OT} \sim t^n$

Total degradation = sum of all three components

H-H₂ Reaction Diffusion (R-D) Model



Breaking of Si-H bonds at Si/SiO₂ interface by holes (Reaction)

H → H₂ transformation: Broken H reacts with another H and forms H₂

Eventual diffusion of molecular H₂

Broken H reacts with Si-H at poly-Si/SiO₂ interface and forms H₂

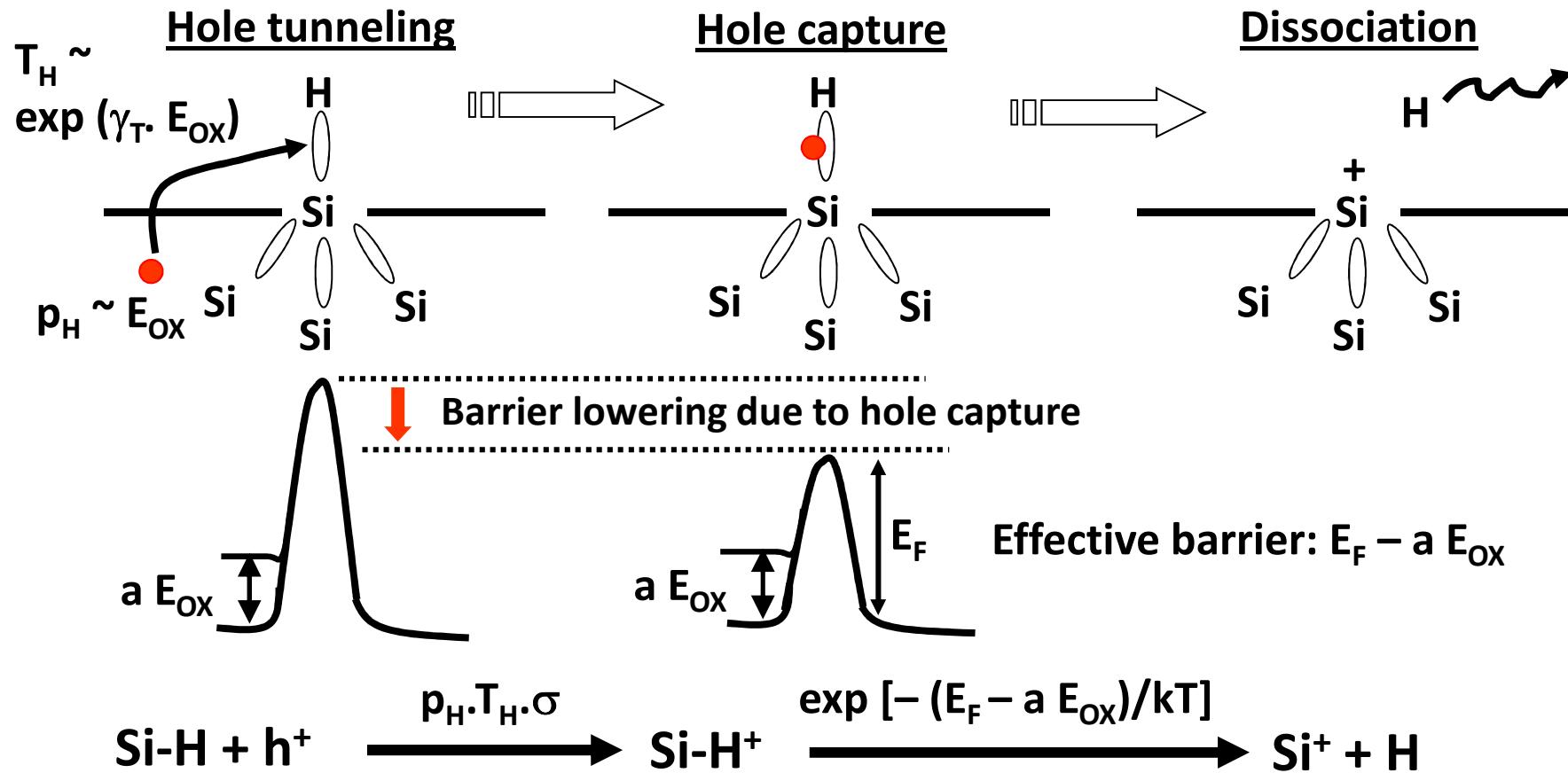
Short time dynamics controlled by Si-H bond breaking reaction

Mid time dynamics controlled by H to H₂ formation and diffusion of both species

Long time dynamics (time to failure) controlled by H₂ diffusion



Physics of Reaction (Breaking of Si-H Bonds)



Si-H dissociation reaction: $k_f \sim E_{ox} \exp(\gamma E_{ox}) \exp(-E_F/kT); \gamma = \gamma_T + a/kT$

Simplification by power law voltage dependence

Details: Islam,
IEDM'06



H – H₂ RD Model: Mathematical Formulation

Reaction

$$\frac{dN_{IT}}{dt} = k_f (N_0 - N_{IT}) - k_r N_{IT} N_H^{(0)}$$

H \leftrightarrow H₂ transformation

$$\frac{\delta}{2} \frac{dN_H^{(0)}}{dt} = D_H \frac{dN_H^{(0)}}{dx} + \frac{dN_{IT}}{dt} - \delta k_H [N_H^{(0)}]^2 + \delta k_{H2} N_{H2}^{(0)}$$

$$\frac{\delta}{2} \frac{dN_{H2}^{(0)}}{dt} = D_{H2} \frac{dN_{H2}^{(0)}}{dx} + \frac{dN_{IT}}{dt} - \frac{\delta}{2} k_H [N_H^{(0)}]^2 - \delta k_{H2} N_{H2}^{(0)}$$

Diffusion

$$\frac{dN_H}{dt} = D_H \frac{d^2 N_H}{dx^2} - k_H N_H^2 + k_{H2} N_{H2}$$

$$\frac{dN_{H2}}{dt} = D_{H2} \frac{d^2 N_{H2}}{dx^2} + \frac{1}{2} k_H N_H^2 - \frac{1}{2} k_{H2} N_{H2}$$

$$k_f = k_{f0} (V_G - V_T)^{\frac{3}{2} \Gamma_{IT}} e^{-\frac{E_{Akf}}{kT}}$$

$$k_r = k_{r0} e^{-\frac{E_{Akr}}{kT}}$$

$$D_H = D_{H0} e^{-\frac{E_{ADH}}{kT}}$$

$$D_{H2} = D_{H20} e^{-\frac{E_{ADH2}}{kT}}$$

$$k_H = k_{H0} e^{-\frac{E_{AkH}}{kT}}$$

$$k_{H2} = k_{H20} e^{-\frac{E_{AkH2}}{kT}}$$

Details: Islam, TED'07



H – H₂ RD Model Parameters

Constant parameters across all (type, EOT & N%) devices studied in this work

$$k_f = k_{f0} \left(V_G - V_T \right)^{\frac{3}{2}} e^{-\frac{E_{Akf}}{kT}}$$

$$k_r = k_{r0} e^{-\frac{E_{Akr}}{kT}}$$

$$D_H = D_{H0} e^{-\frac{E_{ADH}}{kT}}$$

$$D_{H2} = D_{H20} e^{-\frac{E_{ADH2}}{kT}}$$

$$k_H = k_{H0} e^{-\frac{E_{AkH}}{kT}}$$

$$k_{H2} = k_{H20} e^{-\frac{E_{AkH2}}{kT}}$$

Only 2 device dependent parameters –
 k_{f0} and Γ_{IT}

$$E_{Akf} = 0.175 \text{ eV}$$

$$k_{r0} = 9.9 \times 10^{-7}, E_{Akr} = 0.2 \text{ eV}$$

$$D_{H0} = 9.56 \times 10^{-11} \text{ cm}^2/\text{s}, E_{ADH} = 0.2 \text{ eV}$$

$$D_{H20} = 3.5 \times 10^{-5} \text{ cm}^2/\text{s}, E_{ADH2} = 0.58 \text{ eV}$$

$$k_{H0} = 8.56 \text{ cm}^3/\text{s}, E_{AkH} = 0.3 \text{ eV}$$

$$k_{H20} = 5.7 \times 10^{-5} \text{ cm}^3/\text{s}, E_{AkH2} = 0.3 \text{ eV}$$

$\delta \rightarrow \text{Si/SiO}_2$ interfacial layer thickness ($\sim 1.5 \text{ \AA}$)

NBTI Degradation Components

Generation of interface traps (RD): $\Delta V_{IT} = q/C_{ox} * \Delta N_{IT}$

2 device dependent parameters (k_{f0} & Γ_{IT})

Hole trapping in pre-existing bulk insulator traps:

$$\Delta V_{HT} = \frac{q}{C_{ox}} B (V_G - V_{T0} - \Delta V_T)^{\Gamma_{HT}} e^{-\frac{E_{AHT}}{kT}} (1 - e^{-(\frac{t}{\tau})^{\beta_{HT}}})$$

$E_{AHT} = 0.03\text{eV}$ & $\Gamma_{HT} = \Gamma_{IT}$ (all devices);

3 device dependent parameters (B, τ & β_{HT})

Generation (and subsequent trapping in) bulk insulator traps:

$$\Delta V_{OT} = \frac{q}{C_{ox}} C (1 - e^{-(\frac{t}{n})^{\beta_{OT}}})$$

$$n = \eta(V_G - V_T - \Delta V_T)^{\frac{-\Gamma_{OT}}{\beta_{OT}}} e^{+\frac{E_{AOT}}{kT\beta_{OT}}}$$

$\eta = 5*10^{12}$, $\beta_{OT} = 0.36$, $\Gamma_{OT} = 9$ & $E_{AOT} = 0.15\text{eV}$ (for all devices);

1 device dependent parameter (C)

Device Details – Prediction of NBTI

Table I SiON Wafer Details

Device	EOT (nm)	%N
D1	2.35	16.7
D2	1.4	22.6
D3	1.56	34.6
D4	1.45	43

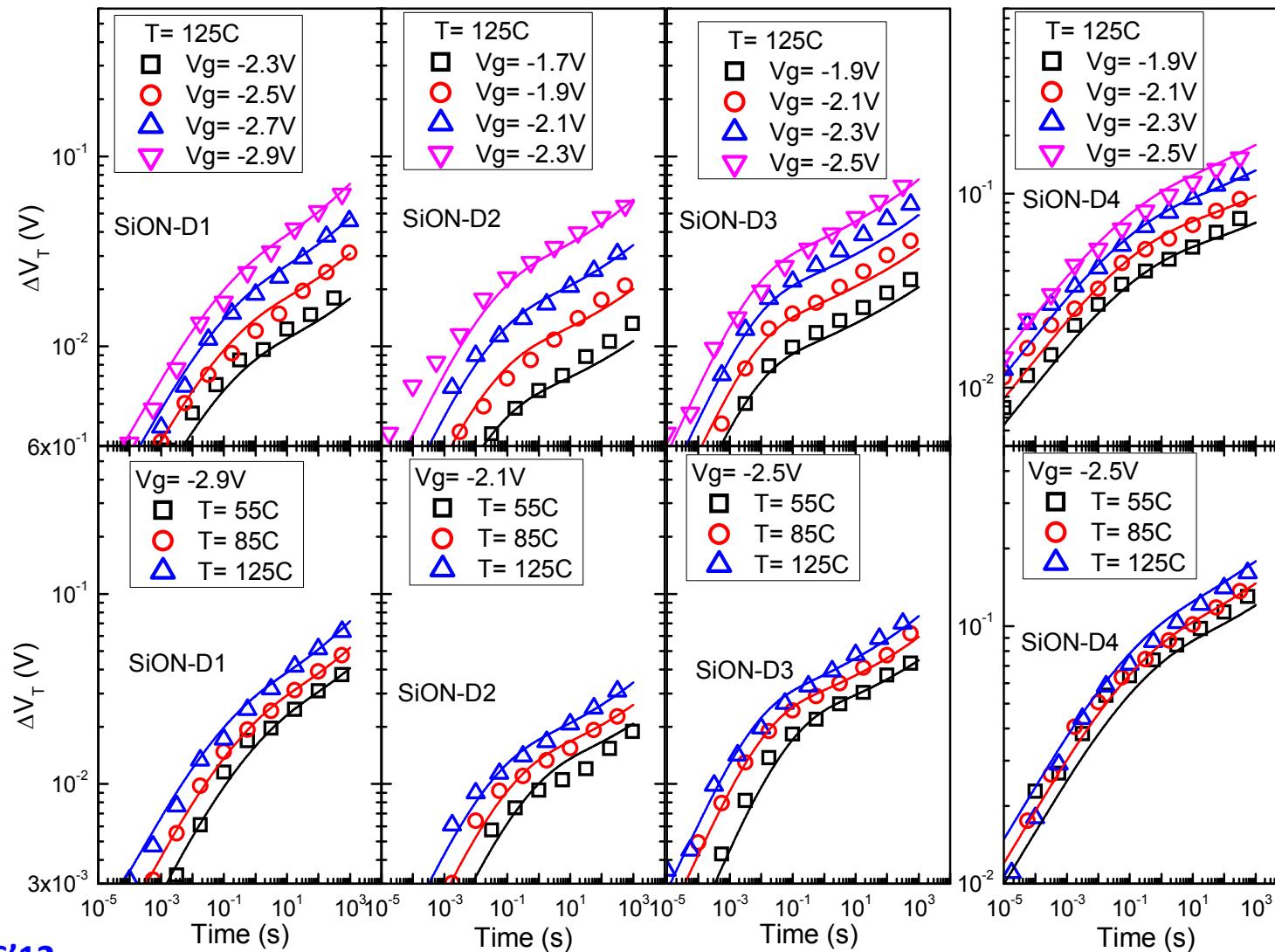
Type A
Type A
Type A/B
Type B

Table II HKMG Wafer Details

Device	HK	IL Thickness (nm)	HK Thickness (nm)
D6	HfSiON	1	2
D7	HfO ₂	1	2

Prediction of UF-OTF NBTI Degradation (SiON)

Prediction (lines) of stress V_G/T data for different (EOT, N%) devices



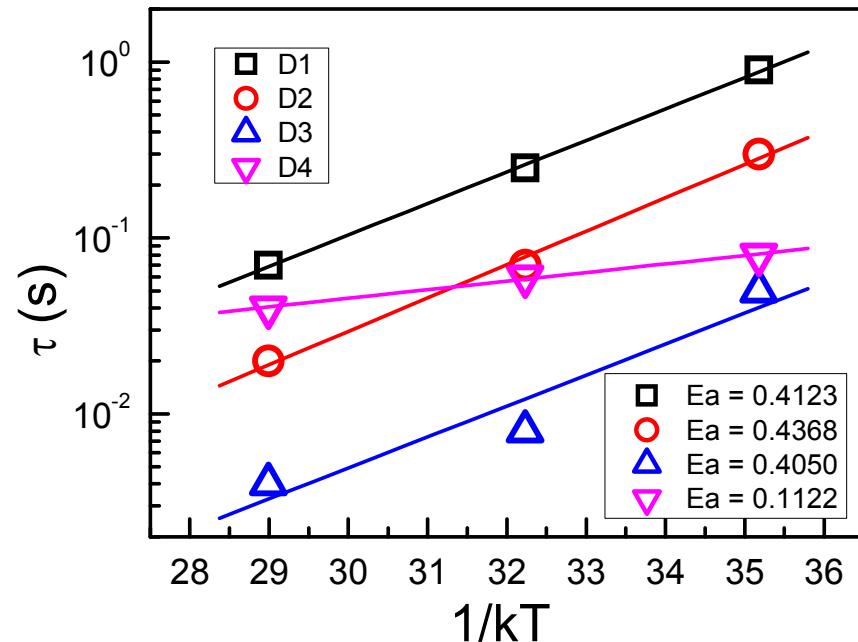
Device Dependent Parameters

Identical parameters for all stress V_G/T

Device	D1	D2	D3	D4
N %	17	23	35	43
EOT (Å)	23.5	14	15.6	14.5
Variable Parameters (not constant across devices)				
k_{f0}	1.1	10	28	350
$\Gamma_{NIT} = \Gamma_{NHT}$	4.3	4.3	3.5	2.2
B ($\times 10^{10}$)	1.1	4.2	7.5	85
C ($\times 10^{13}$)	2.6	16	13	100
β_{HT}	0.28	0.33	0.37	0.22

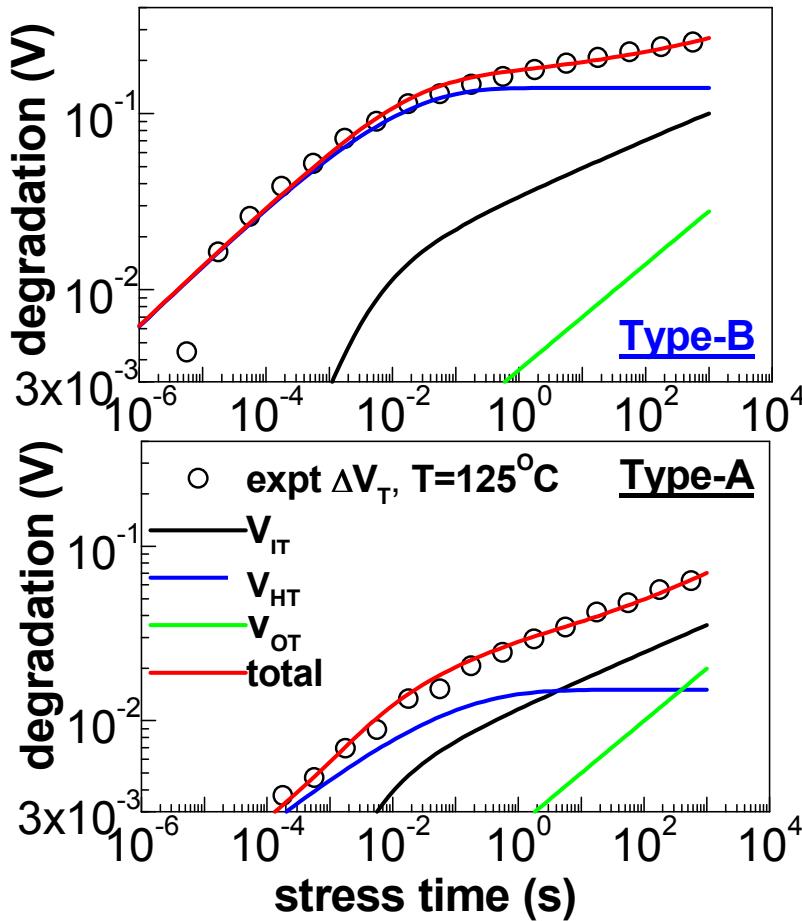
Hole trapping time constant (τ) shows T activation

Presence of shallow hole traps for heavily nitrided device (Type-B)



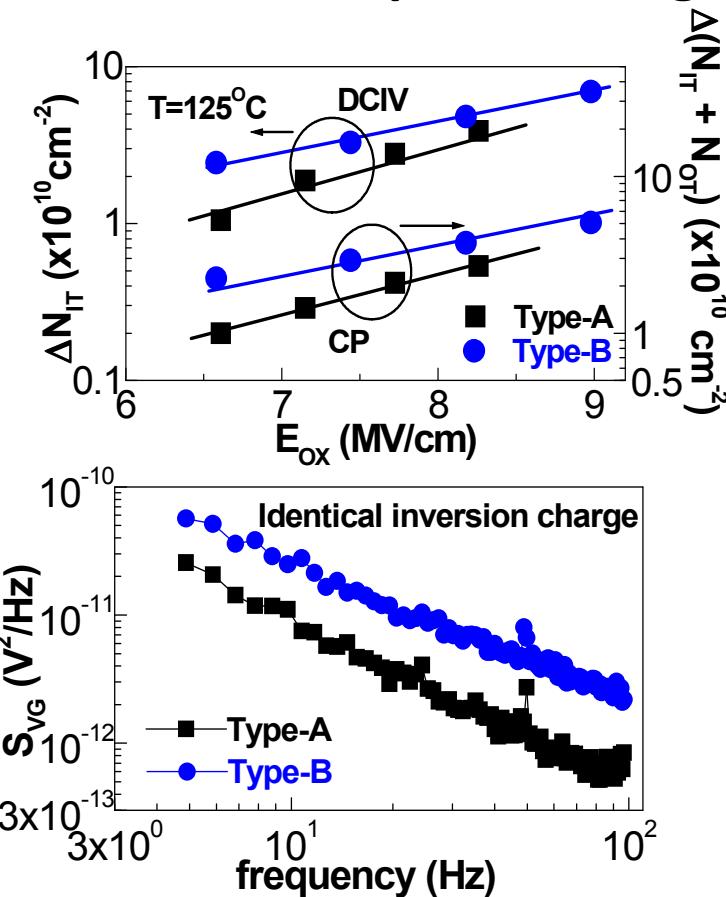
Prediction of NBTI Components (SiON)

Total ΔV_T (mobility corrected) = $q/C_{ox} * (\Delta N_{IT} + \Delta N_{HT} + \Delta N_{OT})$



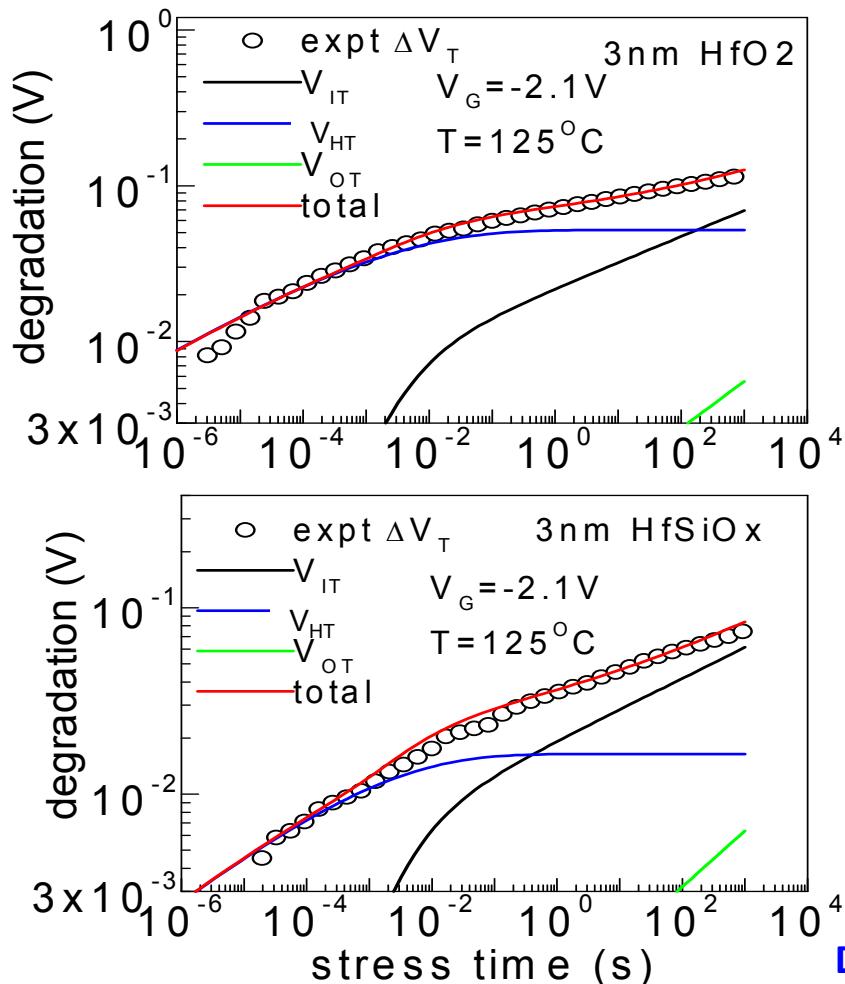
Large Type-A vs. Type-B difference primarily due to trapping in pre-existing traps

Small difference in generated traps, larger difference in pre-existing traps



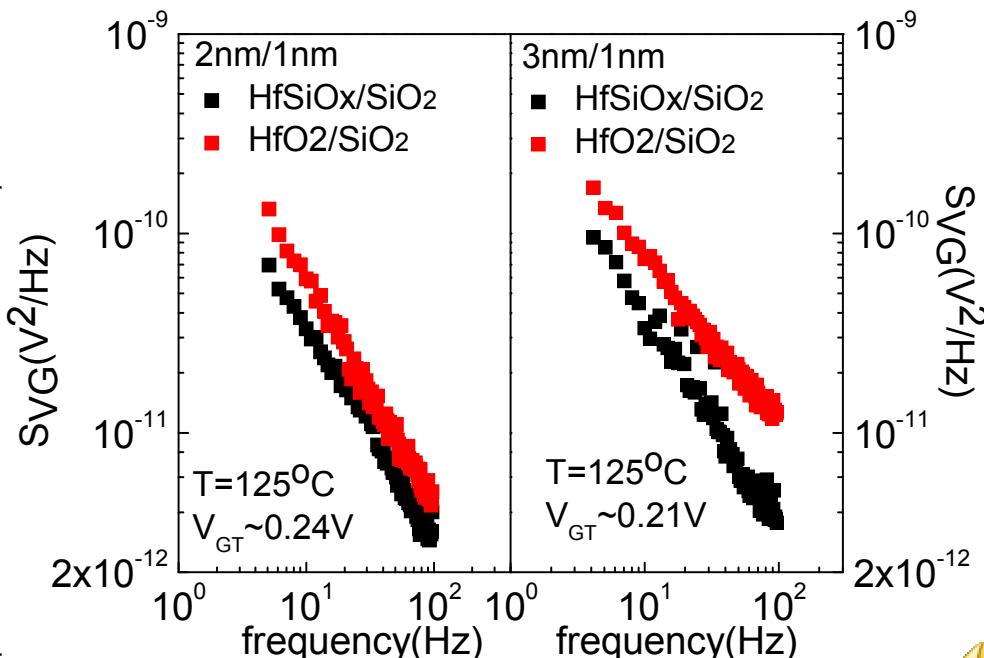
Prediction of NBTI Components (HKMG)

Total ΔV_T (mobility corrected) = $q/C_{ox} * (\Delta N_{IT} + \Delta N_{HT} + \Delta N_{OT})$



Large Hi-K Type-A (HfSiON) vs. Type-B (HfO_2 , un-optimized) difference primarily due to trapping in pre-existing traps

Recap: Higher flicker noise for HfO_2 compared to HfSiON



H – H₂ RD Model: Analytical Solution (Long time)

$$dN_{IT}/dt = k_F (N_0 - N_{IT}) - k_R \cdot N_H(x=0) N_{IT}$$

$$N_{IT} \ll N_0, dN_{IT}/dt \text{ small} \rightarrow N_H(x=0) N_{IT} = k_F N_0 / k_R$$

$$N_H^2(x=0) / N_{H2}(x=0) = \text{const.} (2H = H_2)$$

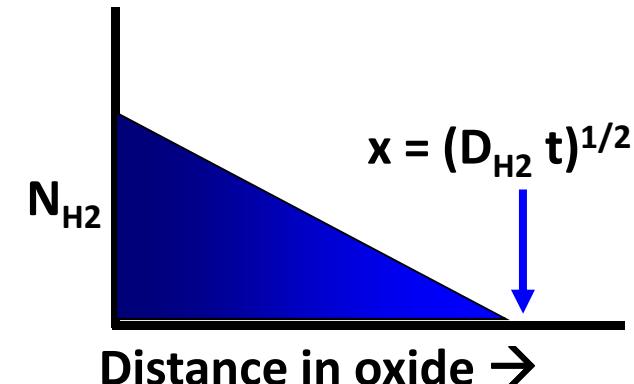
$$N_{IT}^2 = (k_F N_0 / k_R)^2 / N_{H2}(x=0)$$

$$dN_{H2}/dt = D_{H2} d^2N_H/dx^2 \rightarrow x = (D_{H2} t)^{1/2}$$

Generated $N_{IT} = 2 * \text{Total no. of released H molecules}$

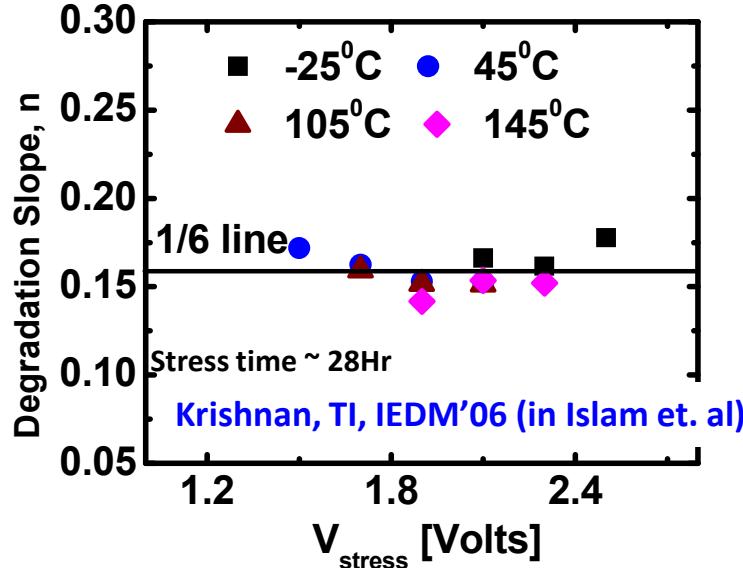
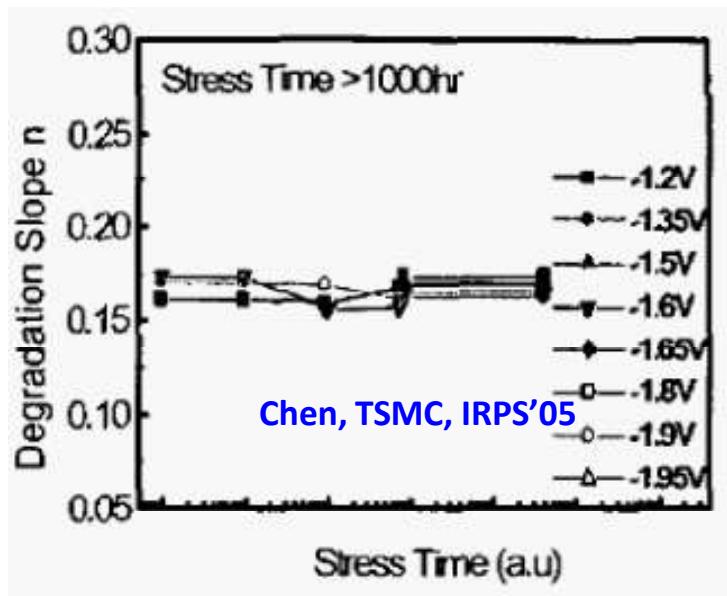
$$N_{IT} = 2 * \frac{1}{2} N_{H2}(x=0) (D_{H2} t)^{1/2}$$

$$N_{IT} = (k_F N_0 / k_R)^{2/3} (D_{H2} t)^{1/6}$$

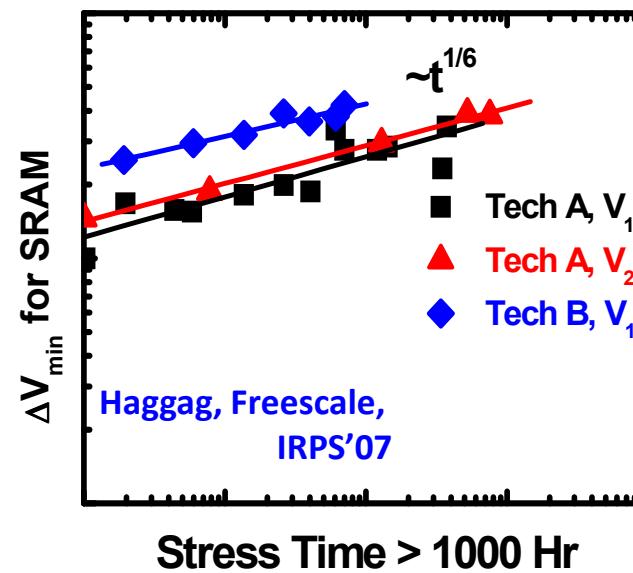


Prediction of 1/6 power law time exponent

Very Long Time Degradation



Universally observed long-time power-law time exponent of $n = 1/6$ in “production quality” devices & circuits



Important feature for prediction of degradation at end-of-life

Accurate prediction by RD model

Simplified NBTI Degradation Model (Long Time)

Power law ($n = 1/6$) time dependence of interface traps

$$\Delta V_{IT} = \frac{q}{C_{ox}} A (V_G - V_{T0} - \Delta V_T)^{\Gamma_{IT}} e^{-\frac{E_{AIT}}{kT}} t^{\frac{1}{6}}$$

$$E_{AIT} = \left(\frac{2}{3} (E_{Akf} - E_{Akr}) + \frac{E_{ADH2}}{6} \right)$$

Saturation of hole trapping

$$\Delta V_{HT} = \frac{q}{C_{ox}} B (V_G - V_T - \Delta V_T)^{\Gamma_{HT}} e^{-\frac{E_{AHT}}{kT}}$$

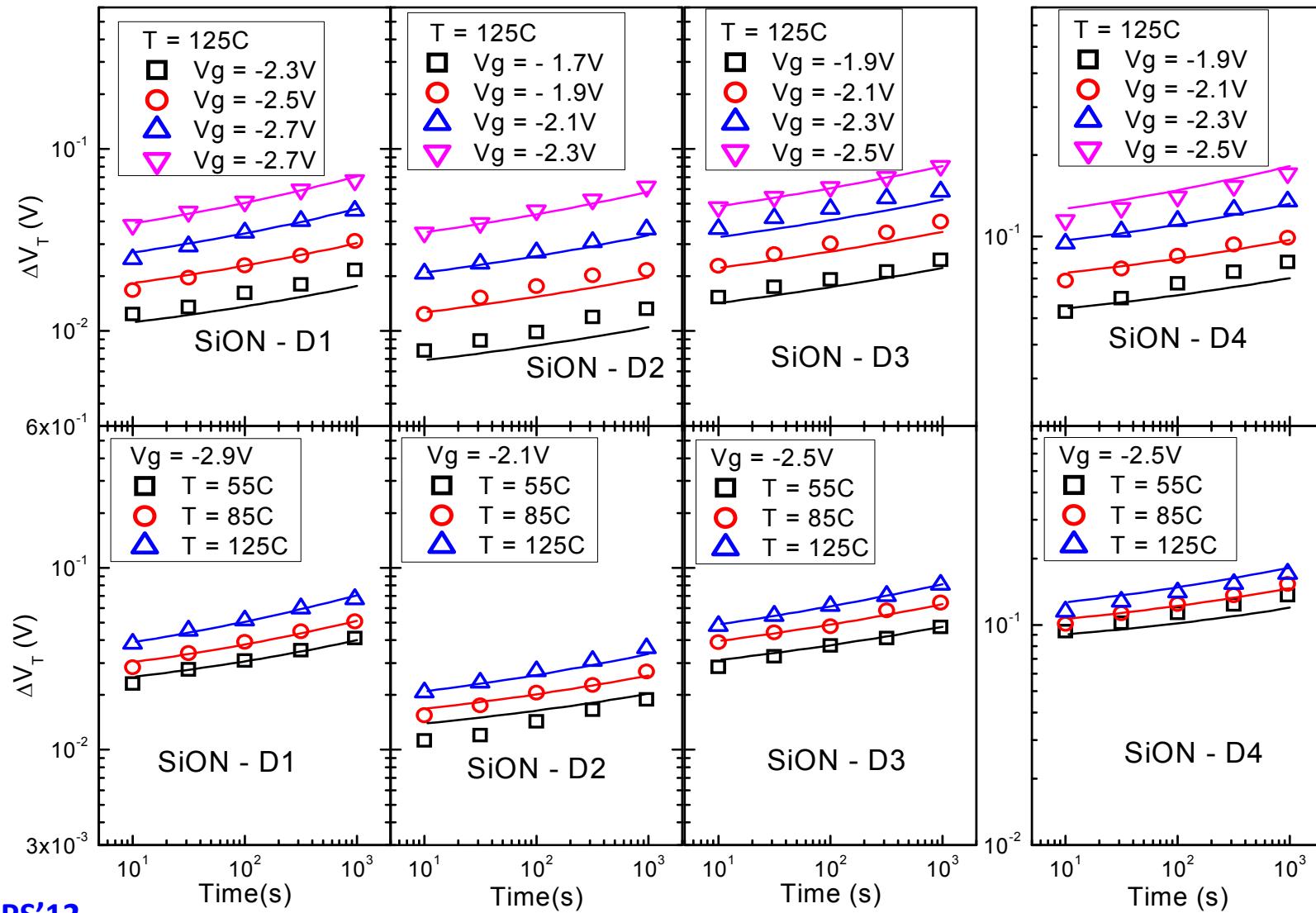
Bulk trap generation and subsequent hole trapping

$$\Delta V_{OT} = \frac{q}{C_{ox}} C \left(1 - e^{(-(\frac{t}{n})^{\beta_{OT}})} \right)$$

$$n = \eta (V_G - V_T - \Delta V_T)^{\frac{-\Gamma_{OT}}{\beta_{OT}}} e^{+\frac{E_{AOT}}{kT\beta_{OT}}}$$

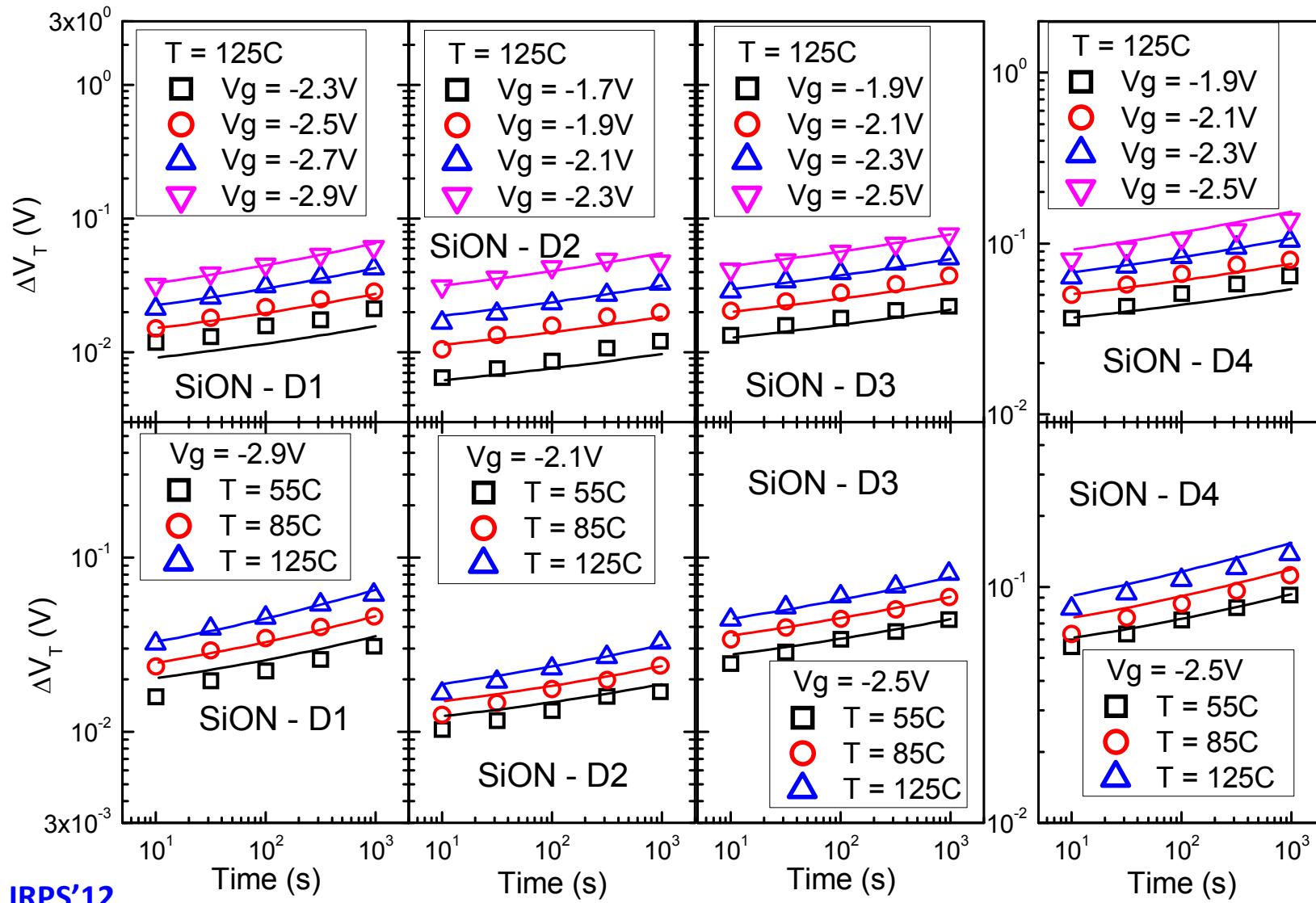
Prediction of UF-OTF NBTI Degradation (SiON)

UF-OTF measurement with t_0 delay of 1 μ s at different stress V_G/T



Prediction of OTF NBTI Degradation (SiON)

OTF measurement with t_0 delay of 1ms at different stress V_G/T



Device Dependent Parameters (SiON, Simple Model)

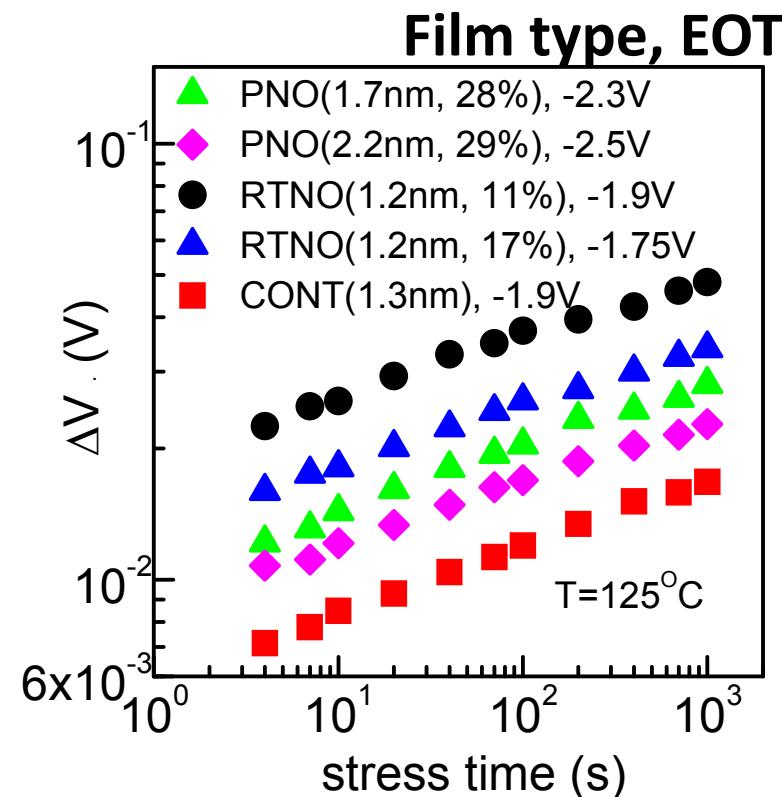
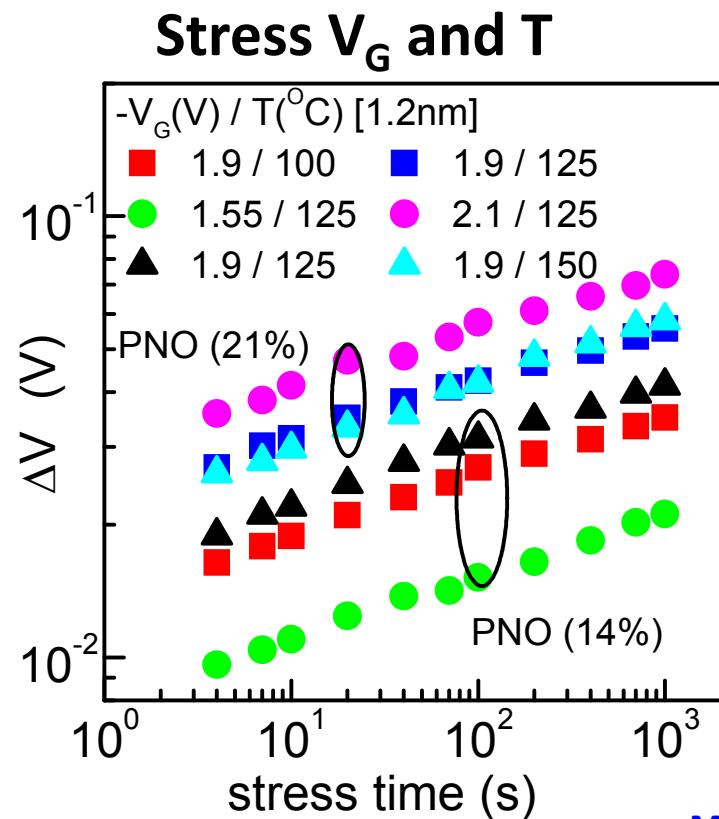
Prediction of V_G/T dependence by only 4 adjustable parameters

Device	D1	D2	D3	D4
N %	17	23	35	43
EOT (Å)	23.5	14	15.6	14.5
Variable Parameters (not constant across devices)				
A ($\times 10^{10}$)	2	8.5	20	90
B ($\times 10^{10}$) (UF-OTF, $t_0 = 1\mu s$)	1.1	4.2	7.5	85
B ($\times 10^{10}$) (OTF, $t_0 = 1ms$)	0.75	3.4	6	45
C ($\times 10^{13}$)	2.6	16	13	100
$\Gamma_{IT} = \Gamma_{HT}$	4.3	4.3	3.5	2.2
Fixed Parameters (constant across all devices)				
$E_{Akf} = 0.175eV$	$E_{Akr} = 0.2eV$	$E_{ADH2} = 0.58eV$	$E_{AHT} = 0.03eV$	
$\Gamma_{OT} = 9$	$E_{AOT} = 0.15 eV$	$\beta_{OT} = 0.36$	$\eta = 5 \times 10^{12}$	

Conventional OTF Measurement Results

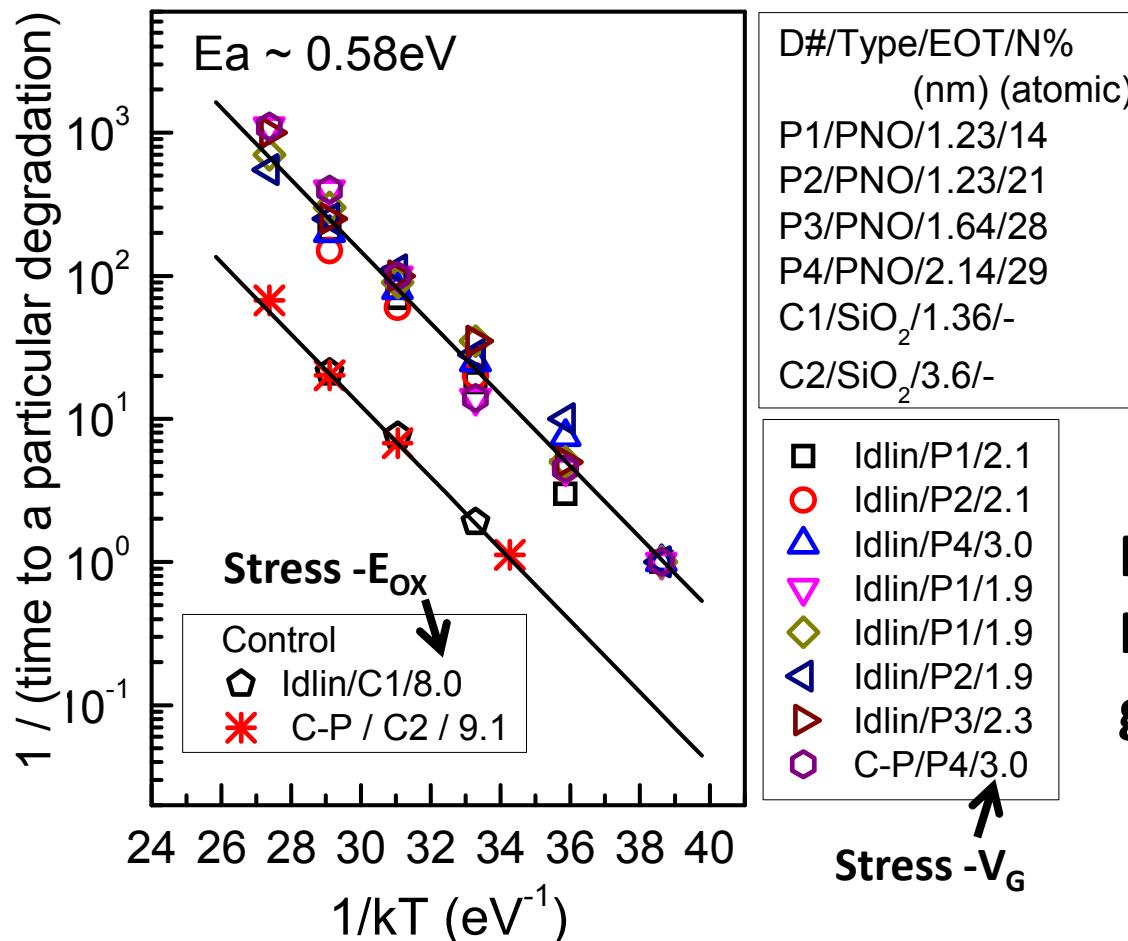
Power law time dependence of longer time data, with time exponent $n \sim 0.14\text{-}0.15$ for all stress bias and temperature

Relatively smaller hole trapping, NBTI dominated by generation of interface traps

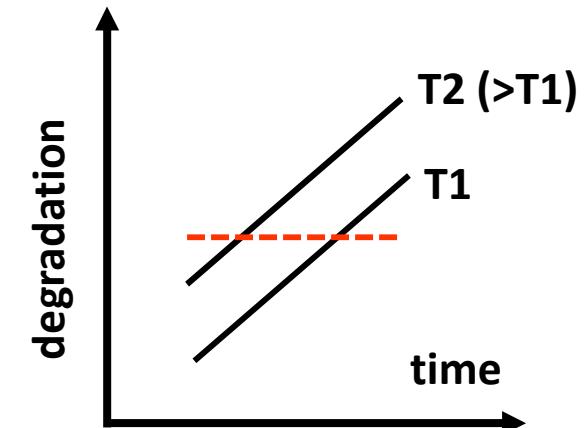


NBTI Temperature Activation

Time to reach a particular degradation
from OTF (1ms) I_{DLIN} and Charge Pumping



Mahapatra, IRPS'07



Identical T activation
across devices and
measurement methods

Negligible hole trapping,
NBTI dominated by
generation of interface traps

E_A refers to activation of
molecular H₂ diffusion

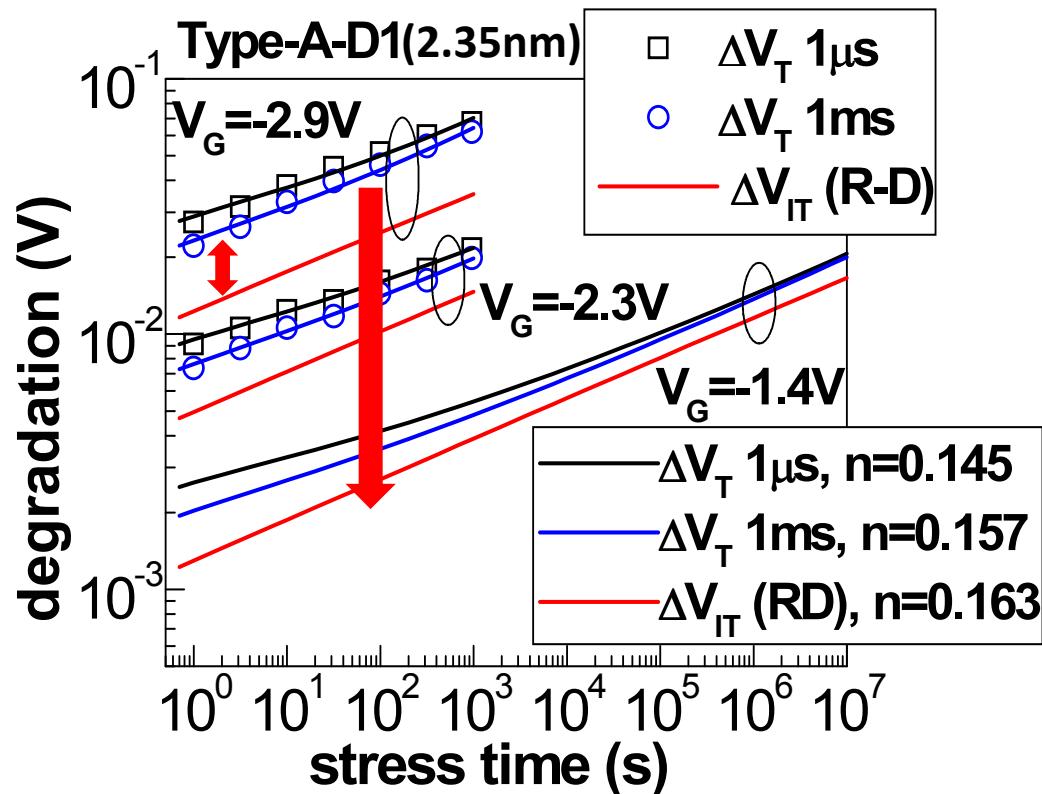
Reed, JAP'88



Voltage Acceleration & Projection

Black and blue lines: NBTI model, sum of N_{IT} , N_{HT} and N_{OT}

Type-A: Reduced hole trapping in pre-existing bulk traps



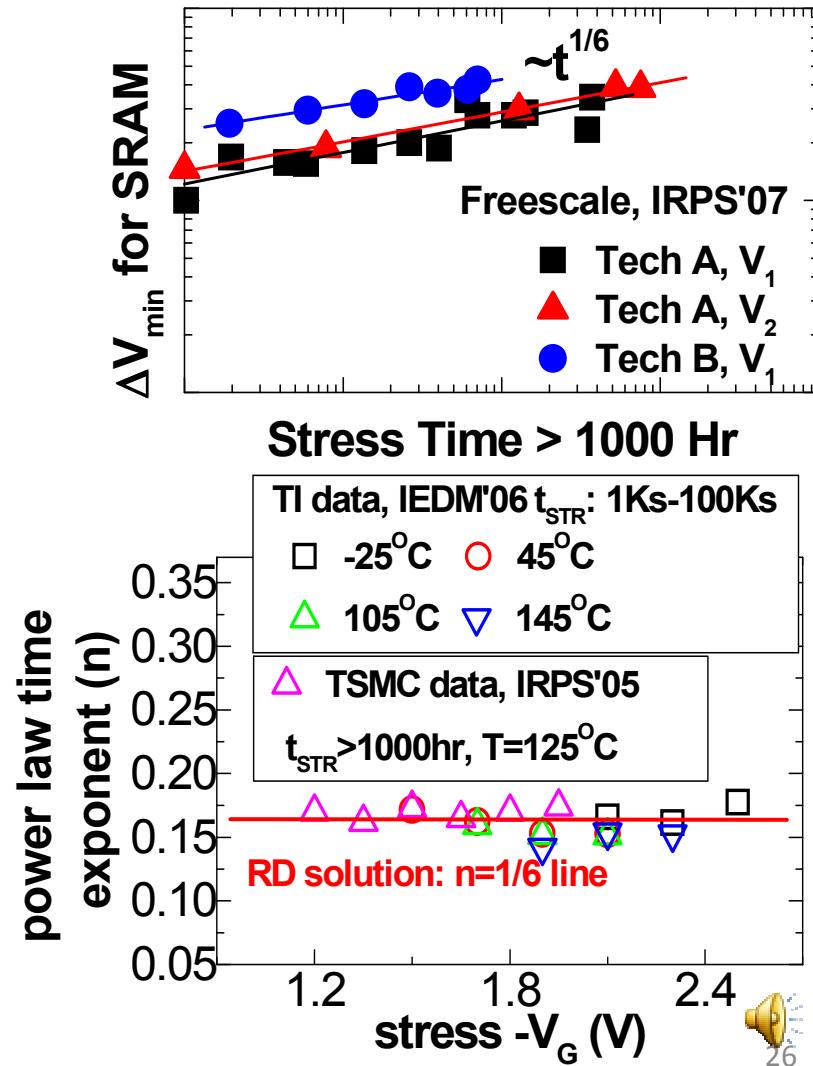
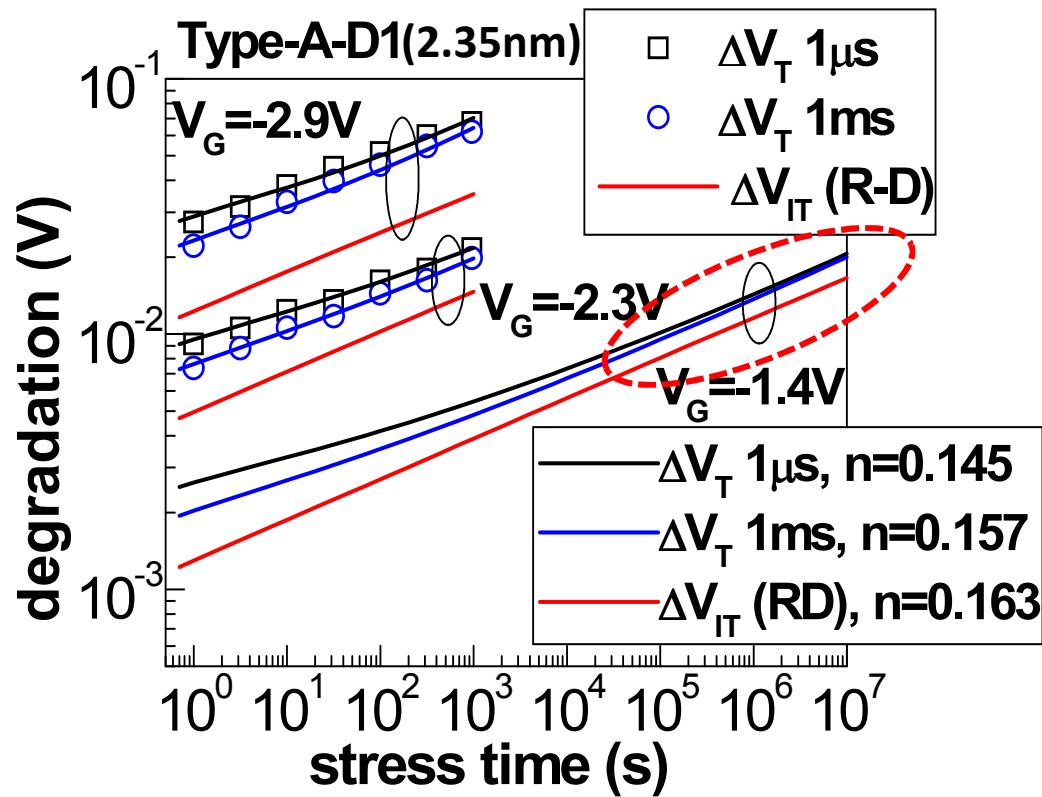
High stress V_G : Significant contribution from bulk trap generation – large difference between data and RD model

Lower stress V_G : Much reduced bulk trap generation (stronger V_G acceleration) – extrapolated measured data close to RD prediction

Projection: Long Time Degradation

Predicted very long-time power-law exponent at use V_G consistent with measured data

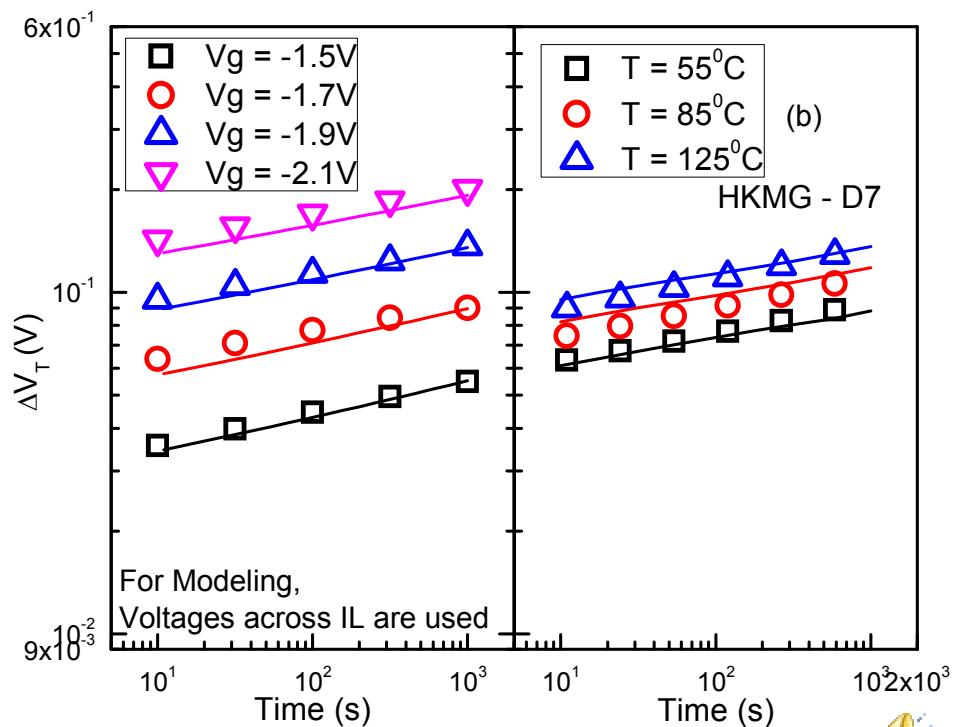
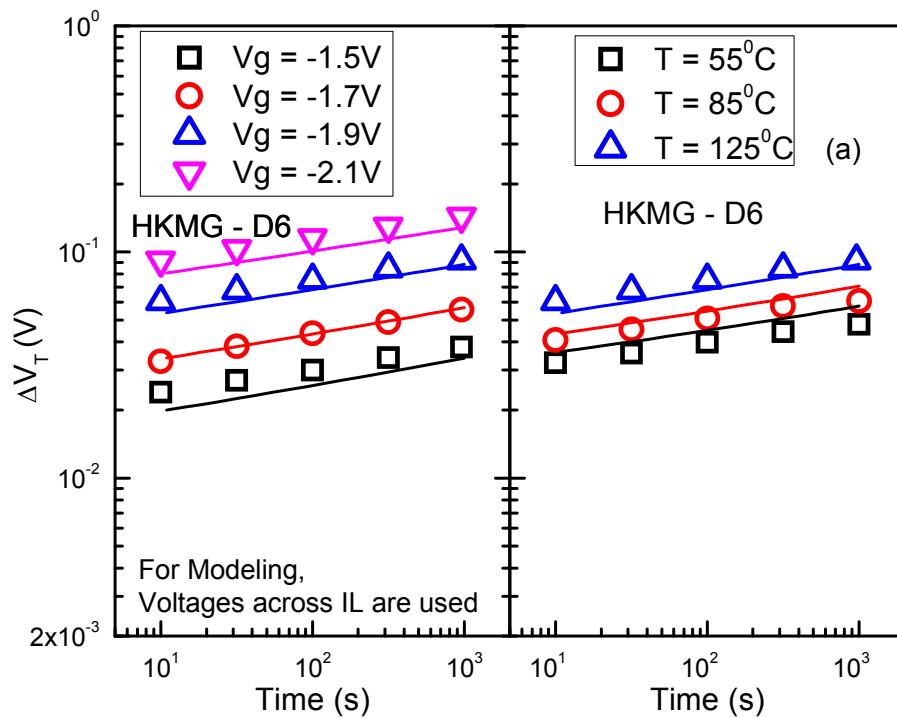
N_{IT} governed long-time NBTI at use condition



Prediction of UF-OTF NBTI Degradation (HKMG)

Prediction of long time NBTI degradation for different V_G/T in different HKMG stacks: SiON/HfSiON and SiON/HfO₂

Device D7 has higher N% in IL and show larger NBTI



Device Dependent Parameters (HKMG, Simple Model)

Prediction of V_G/T dependence by only 4 parameters

Device	D6 (SiON/HfSiON)	D7 (SiON/HfO ₂)
Variable Parameters (not constant across devices)		
A (x 10 ¹²)	3.1	5.5
B (x 10 ¹¹) (t ₀ = 1us)	3.5	8
C (x 10 ¹²)	3.1	3.1
$\Gamma_{IT} = \Gamma_{HT}$	4.5	4.5
Fixed Parameters (constant across all devices)		
$E_{Akf} = 0.195\text{eV}$	$E_{Akr} = 0.2\text{eV}$	$E_{ADH2} = 0.58\text{eV}$
$E_{AHT} = 0.03\text{eV}$		
$\Gamma_{OT} = 9$	$E_{AOT} = 0.15\text{ eV}$	$\beta_{OT} = 0.36$
		$\eta = 5 \times 10^{12}$

Summary – Model for NBTI DC Stress

NBTI stress transients at different stress V_G/T and different devices can be successfully explained using a RD model based framework with minimal adjustable parameters

Interface trap generation plays an important role, together with hole trapping in pre-existing and generated traps

The proposed physical mechanism is consistent with experimental results obtained from multiple methods

A simplified model can be developed to explain long time NBTI degradation with only 4 adjustable parameters

NBTI in HKMG devices is governed by SiON IL, and can be predicted using the model developed for SiON devices, with similar model parameters

Outline

Introduction, Basic NBTI signatures

Fast / Ultra-fast drain current degradation measurement

Estimation of pre-existing and generated defects

Transistor process / material dependence

Role of Nitrogen – Study by Ultrafast measurement

Predictive modeling

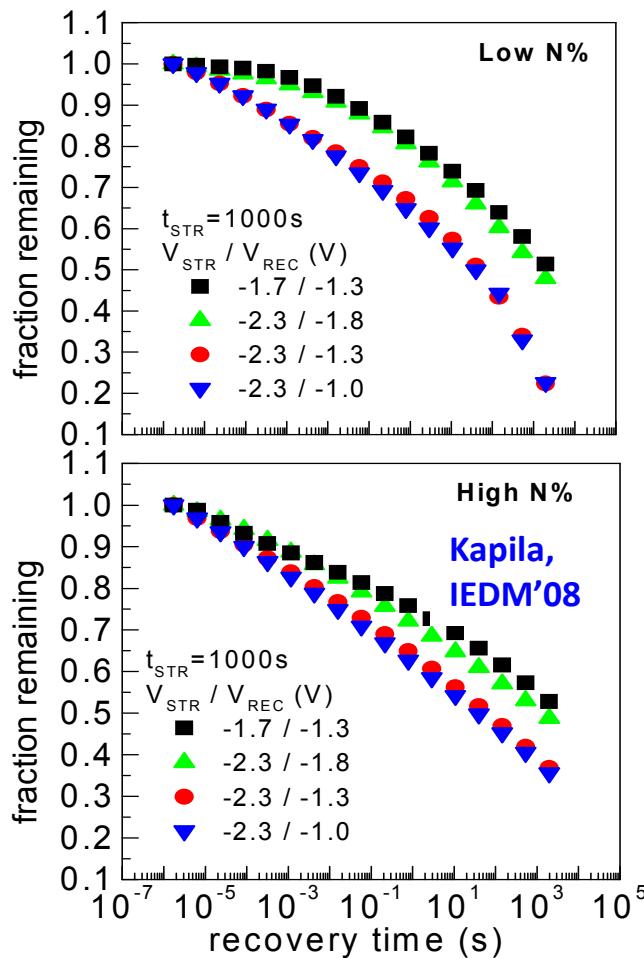
Stress

Recovery and AC effects ←

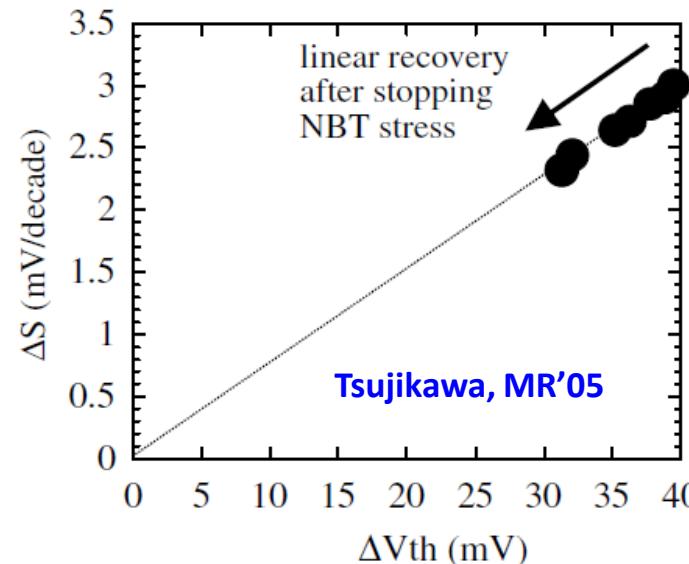
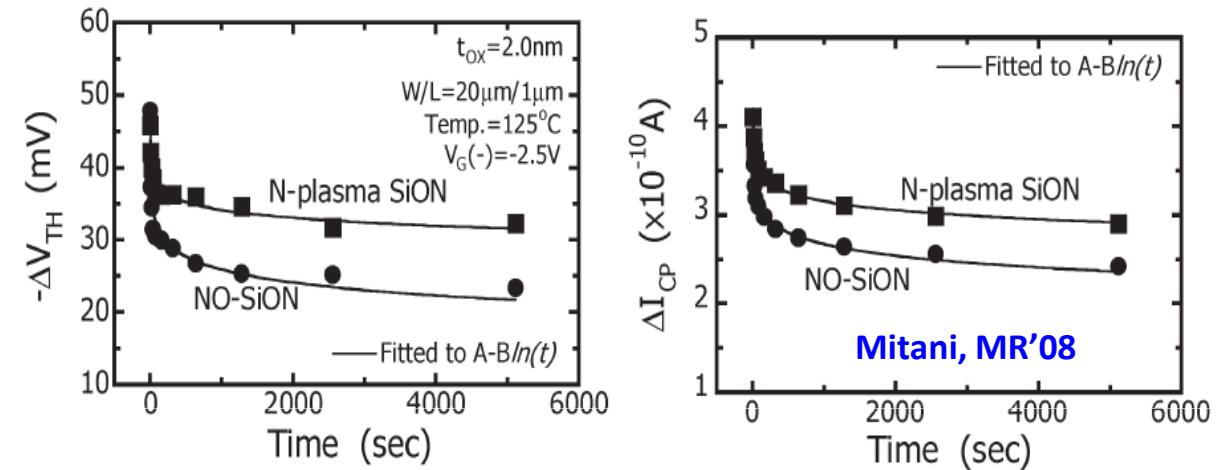
Conclusions / outlook

NBTI Experimental Signatures (1)

Strong gate insulator process dependence

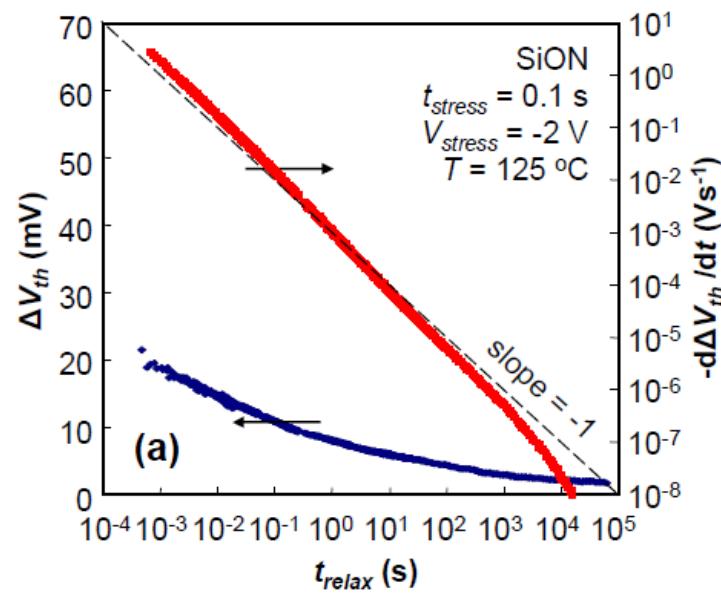
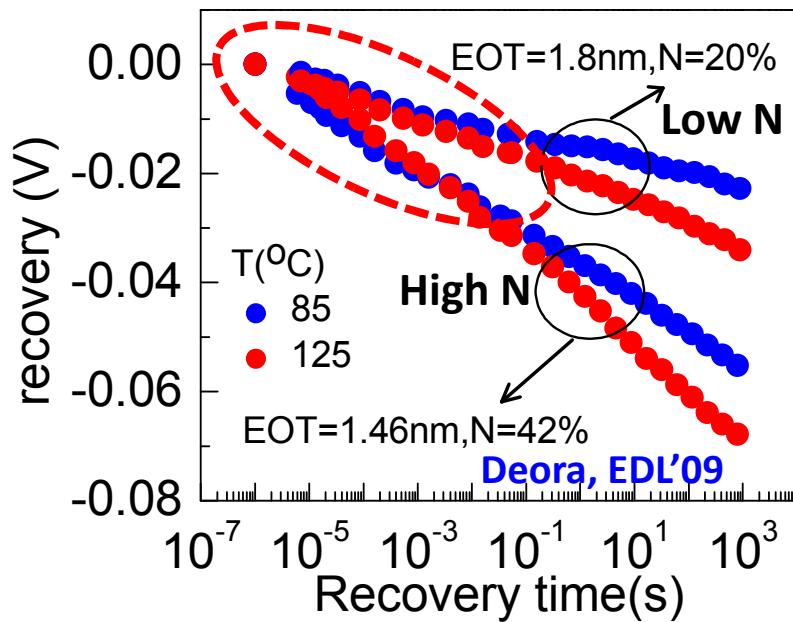


Strong evidence of recovery of generated traps



Strong
evidence of
recovery of
interface traps

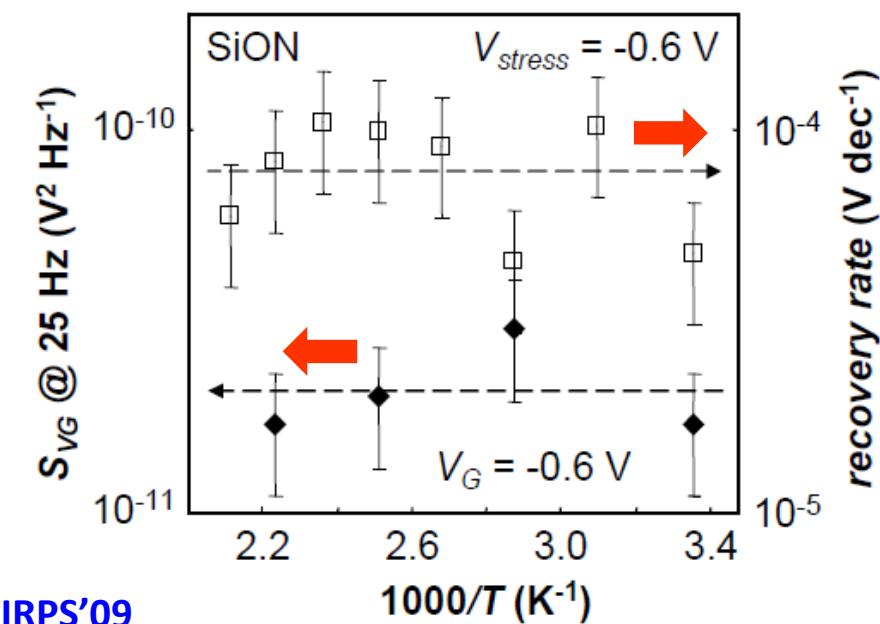
NBTI Experimental Signatures (2)



Short-time recovery shows weak T dependence

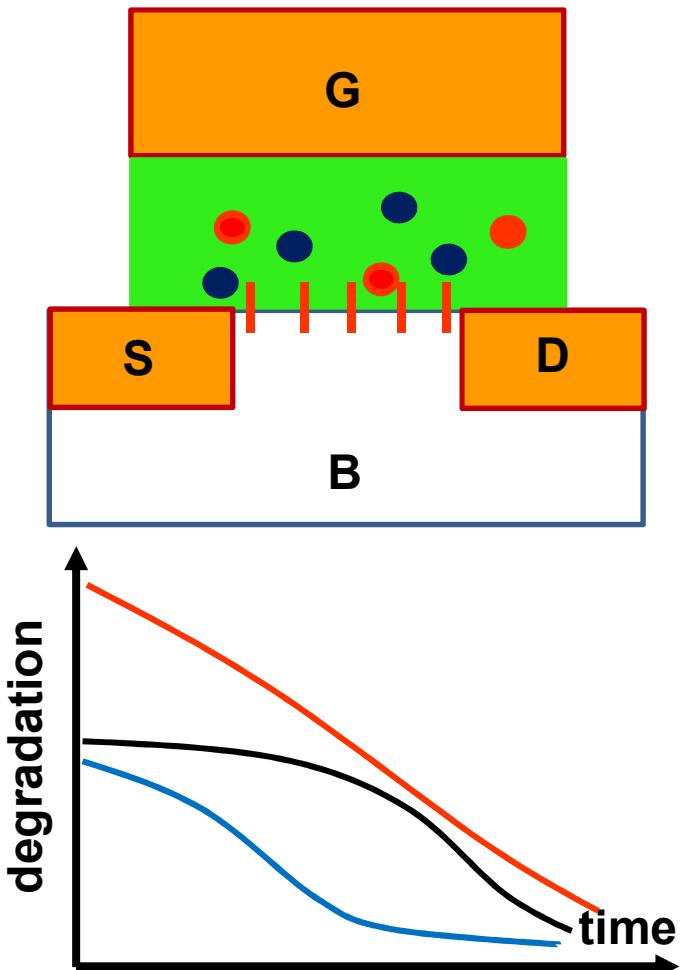
Short-time recovery rate slope ~ -1

Consistent with flicker noise (trapping-detrapping of holes)



NBTI Recovery Model

Interface trap passivation – re-formation of Si-H bonds at Si/SiON interface by back diffusion of Hydrogen (RD model) $\rightarrow \Delta V_{IT}$ reduction



Discharging of pre-existing bulk oxide traps

Detrapping of trapped holes in generated bulk oxide traps (traps do not recover)

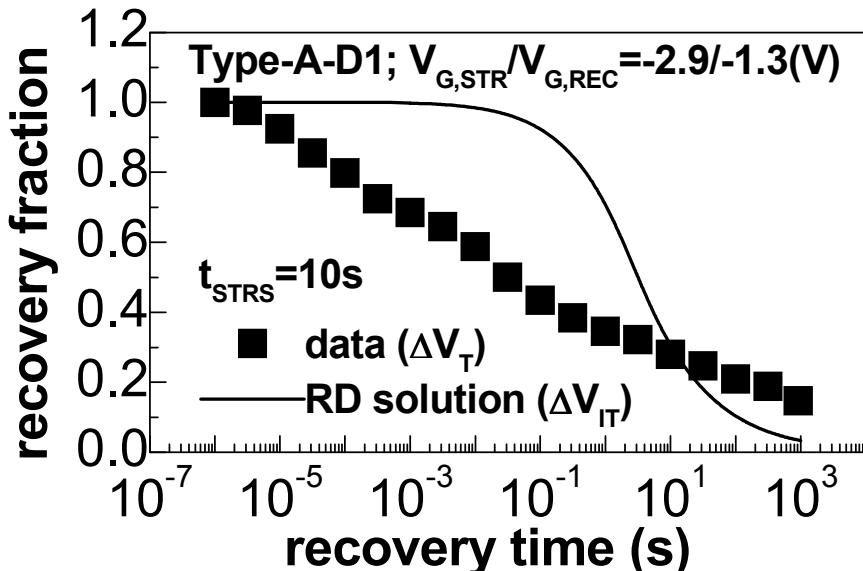
Both hole detrapping terms merged into one (fast detrapping) – analytical expression to account for multiple detrapping time constants

$$\Delta V_{HT} \sim \exp(-t/\tau)^{\beta}$$

Total recovery = sum of two components

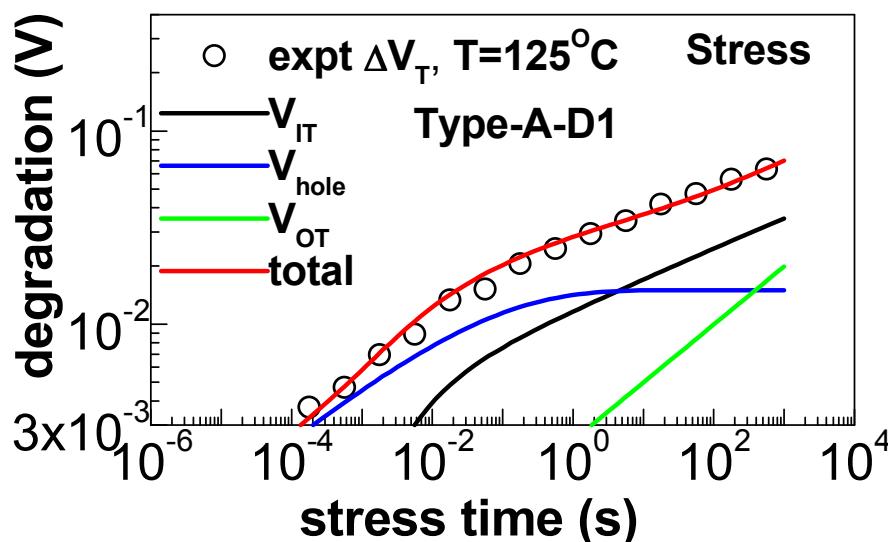
Mahapatra, IRPS'11

Prediction of Recovery Transients



RD model cannot explain NBTI recovery!

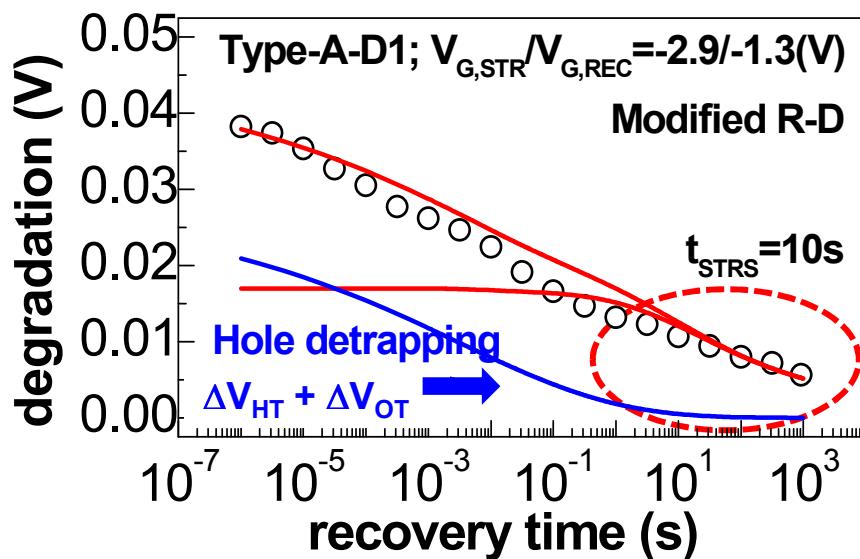
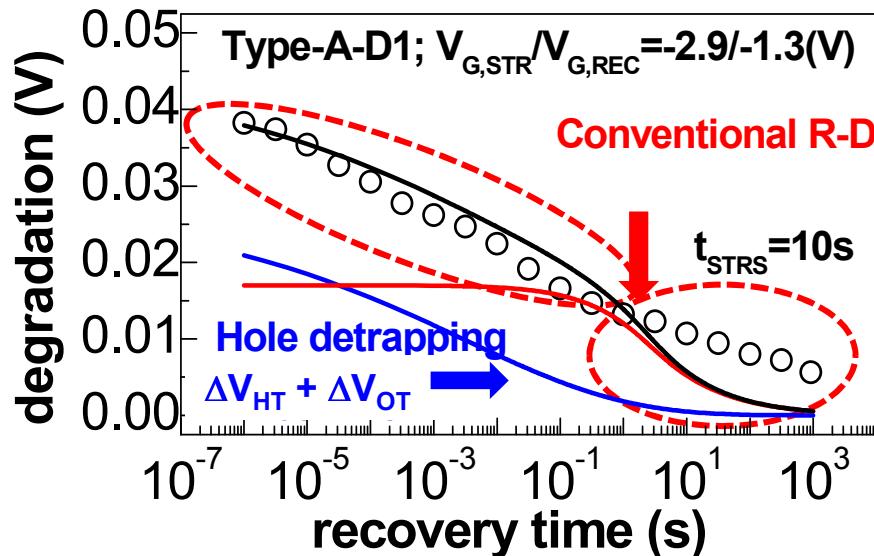
Recap: NBTI is interface trap generation (R-D) **plus** hole trapping in pre-existing and stress generated traps



Erroneous criticism when NBTI recovery is compared to recovery of interface traps (R-D solution)

Need to account for hole detrapping

Prediction of Recovery Transients



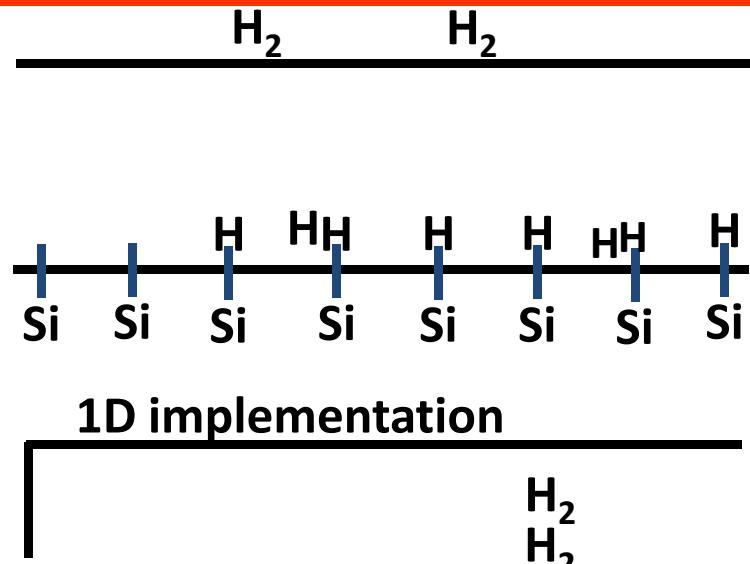
Early part of NBTI recovery due to fast hole detrapping from pre-existing and generated traps

Accurate prediction of start time of NBTI recovery as controlled by interface trap re-passivation (R-D solution)

Rate of recovery cannot be matched by conventional R-D solution

Rate of N_{IT} recovery accurately predicted by modified RD approach

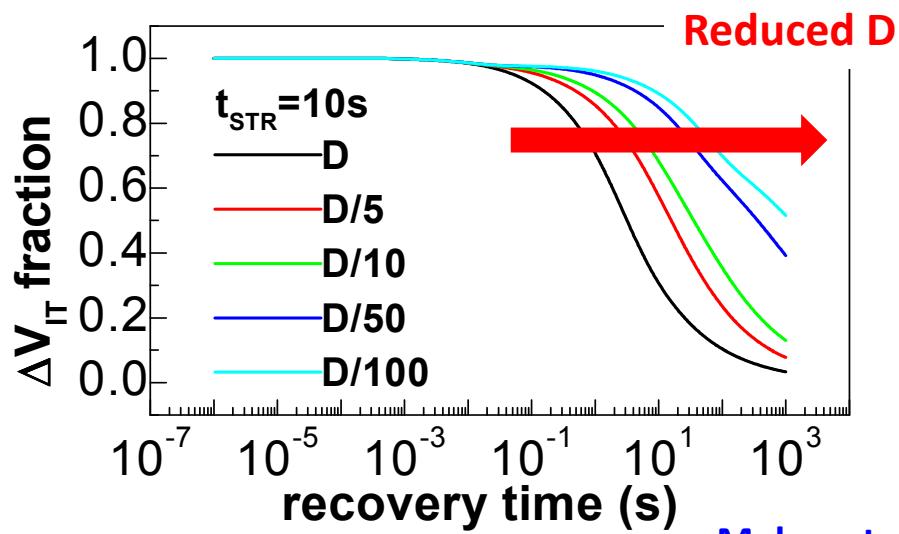
Modification of R-D Framework



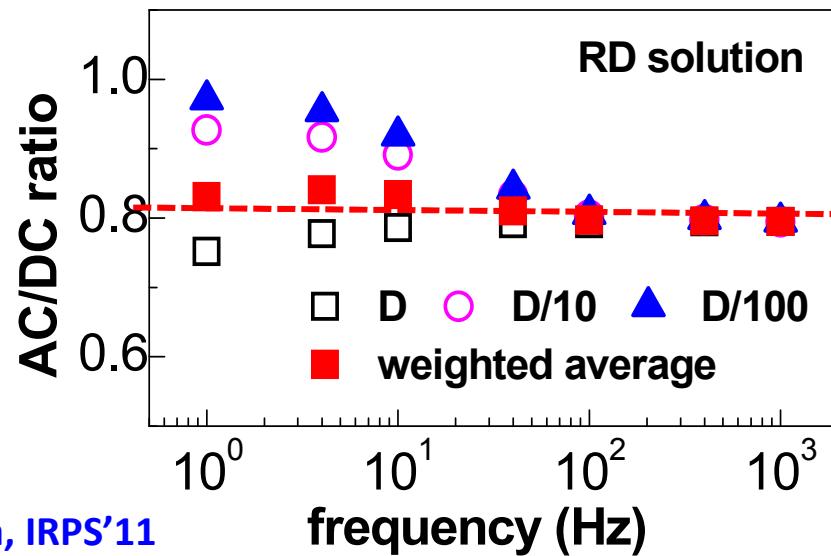
Simple 1D implementation by reduction of diffusivity during recovery

A weighted average predicts rate of long-time recovery

Consistent with NBTI frequency independence

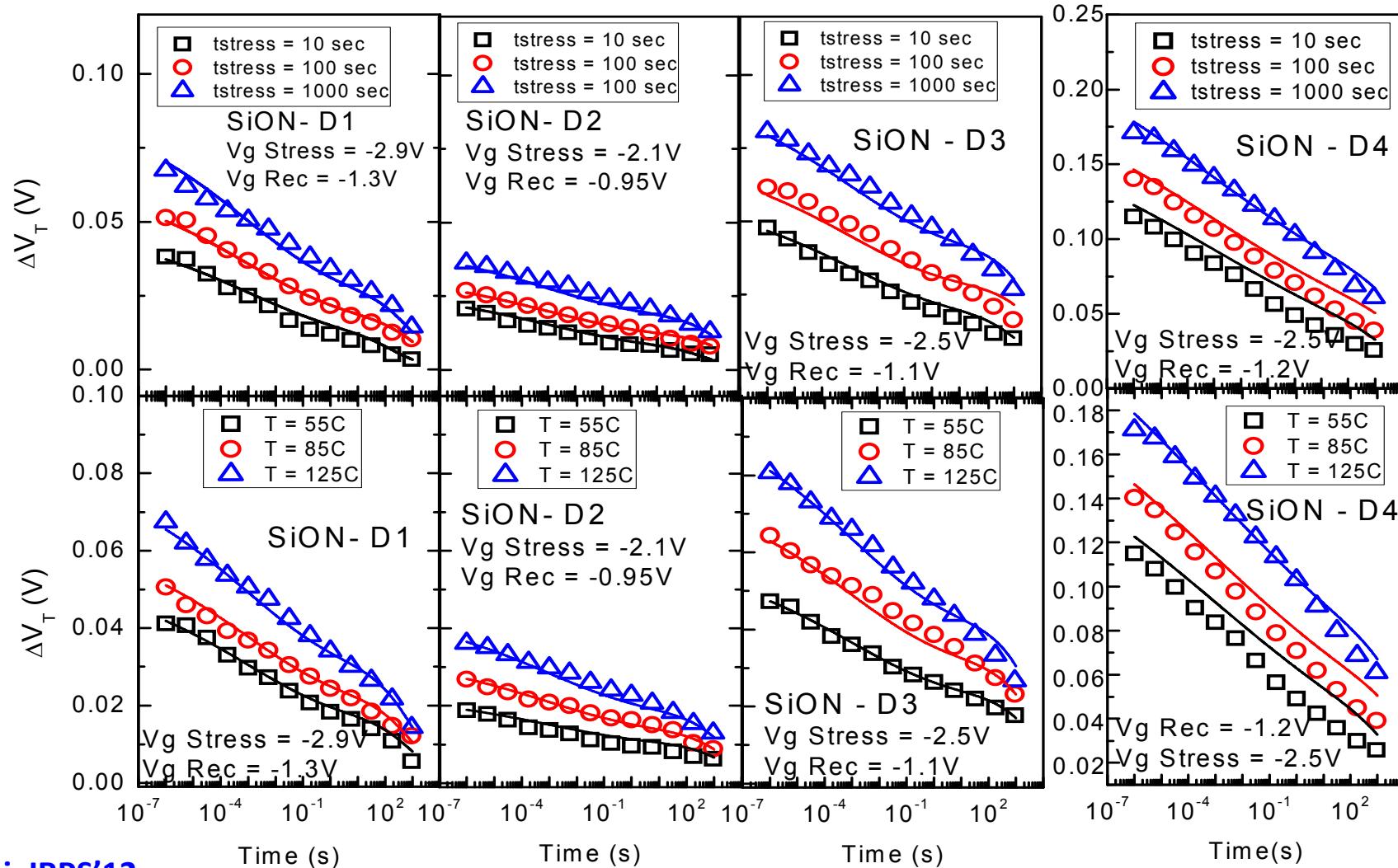


Mahapatra, IRPS'11



Prediction of NBTI Recovery – Material Dependence

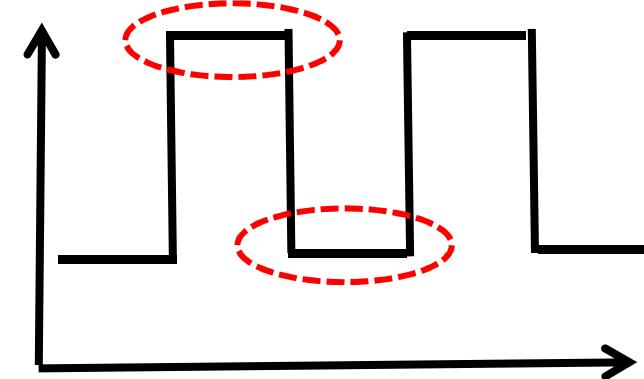
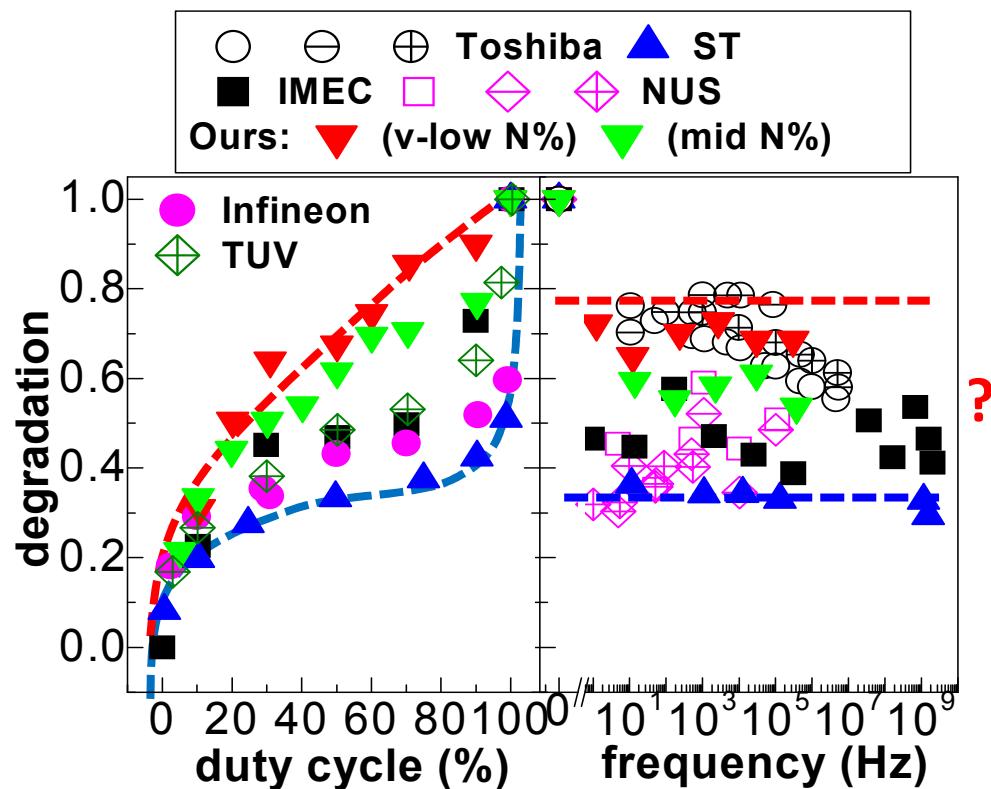
Modified RD framework can successfully predict recovery transients following different stress time & T



AC Degradation – Duty Cycle and Frequency

Recap: Measured data from different industry/academia

Normalized to DC → Large difference in “shape” of duty cycle dependence and AC/DC ratio → Modeling a challenge!

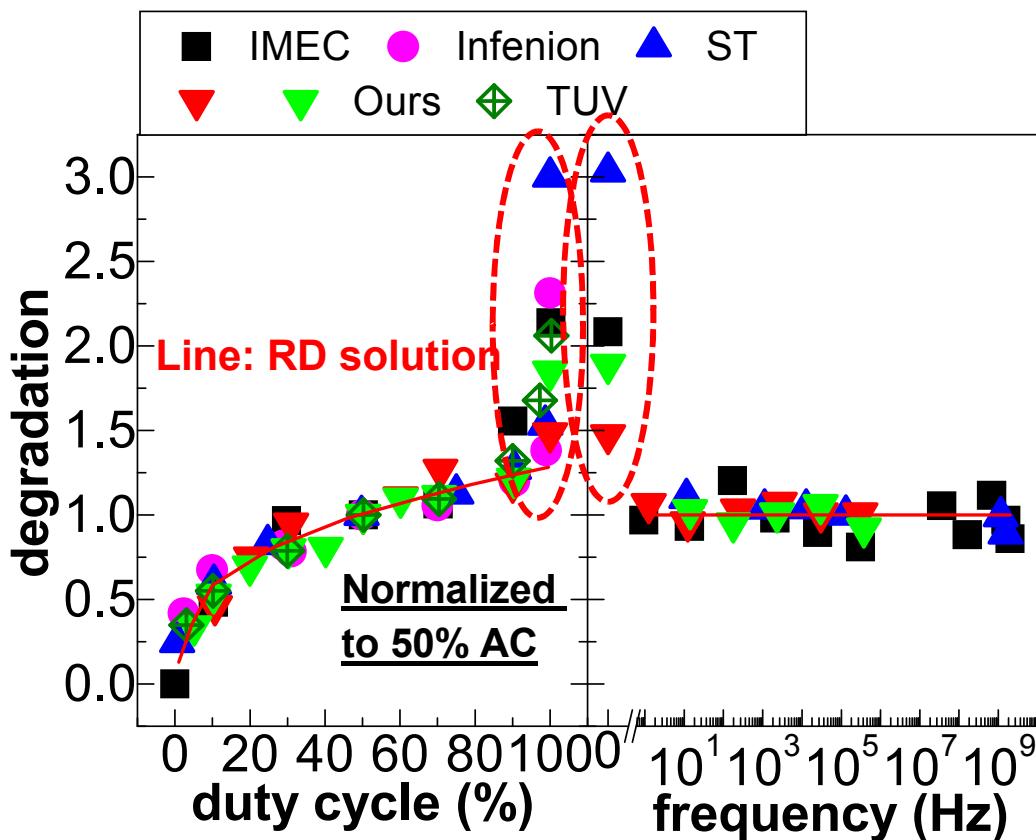


Hole trapping and interface trap generation

Hole detrapping and recovery of interface traps

Renormalization of AC Degradation

Measured data from different industry/academia



Re-normalized to 50% AC data

Universal relation versus duty cycle up to ~80%

Successful prediction by R-D model

Spread close to DC due to differences in AC-DC measurement methods and hole trapping

Prediction of Duty Cycle Dependence

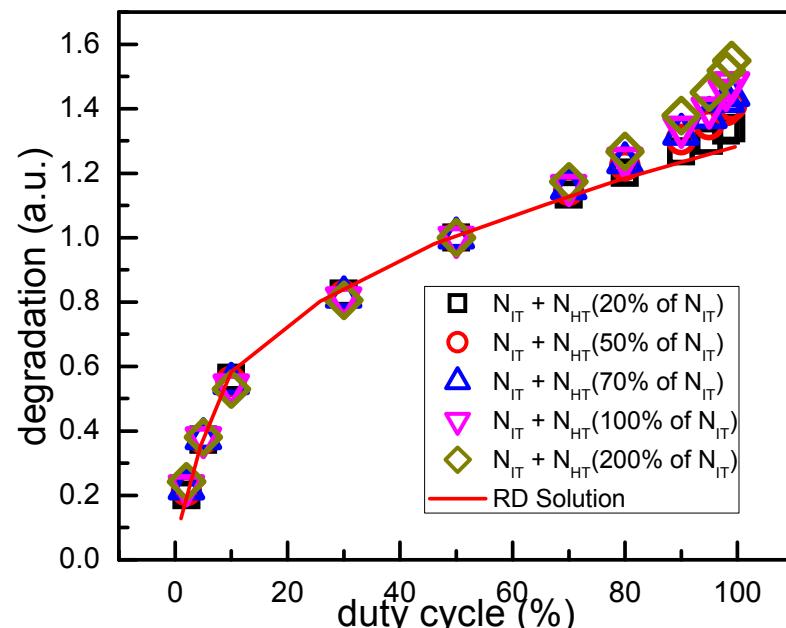
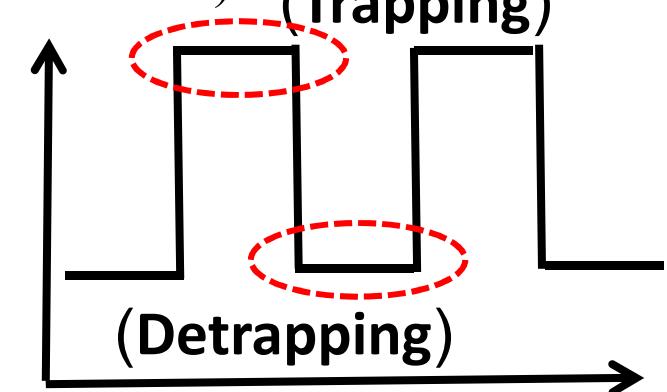
Solution of analytical models for hole trapping/detrapping

$$\Delta V_{HT} = \frac{q}{C_{ox}} B \left(V_G - V_{T0} - \Delta V_T \right)^{\Gamma_{HT}} e^{-\frac{E_{AHT}}{kT}} \left(1 - e^{-(\frac{t}{\tau})^{\beta_{HT}}} \right)$$
$$\Delta V_{HT} = B' e^{-\left(\frac{t}{\tau_r}\right)^{\beta_r}} \quad (B' \text{ obtained from end of stress})$$

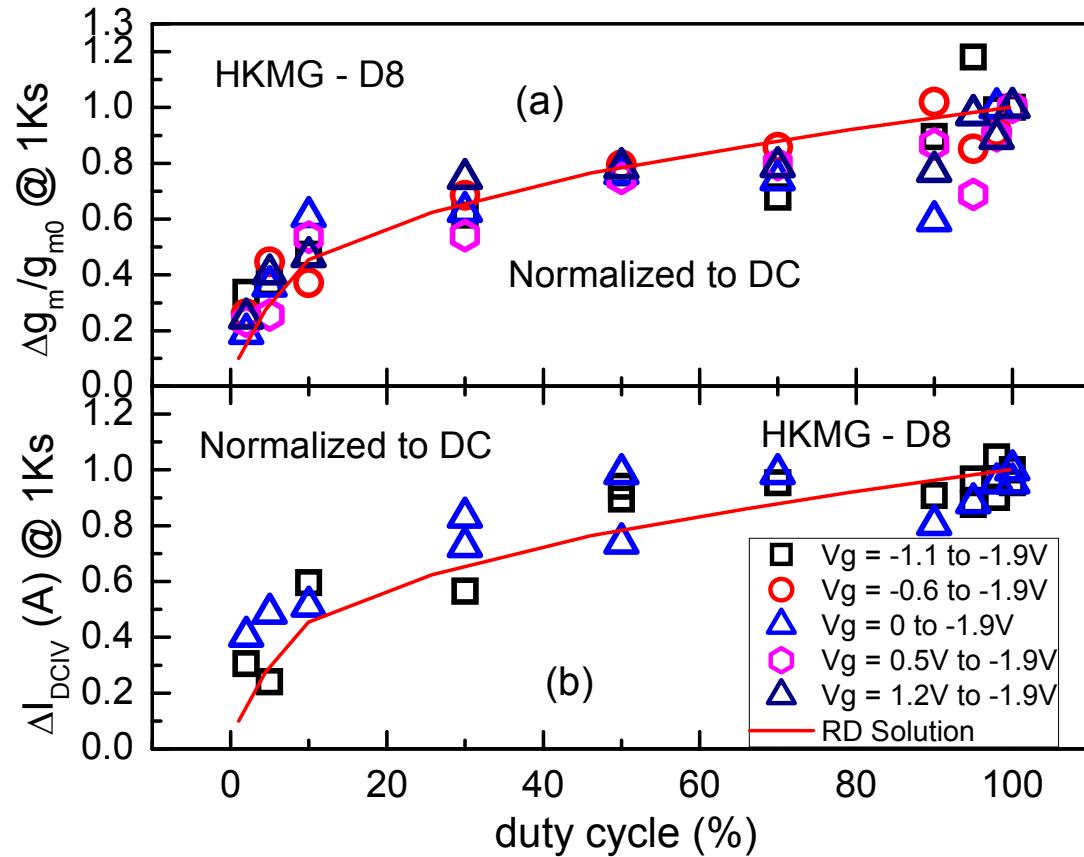
AC simulation of RD model for interface trap contribution

Up to ~80% duty cycle
dominated by interface traps
(fast hole trapping - detrapping)

Large spread close to DC due to
hole trapping contribution



Trap Generation – Duty Cycle Dependence

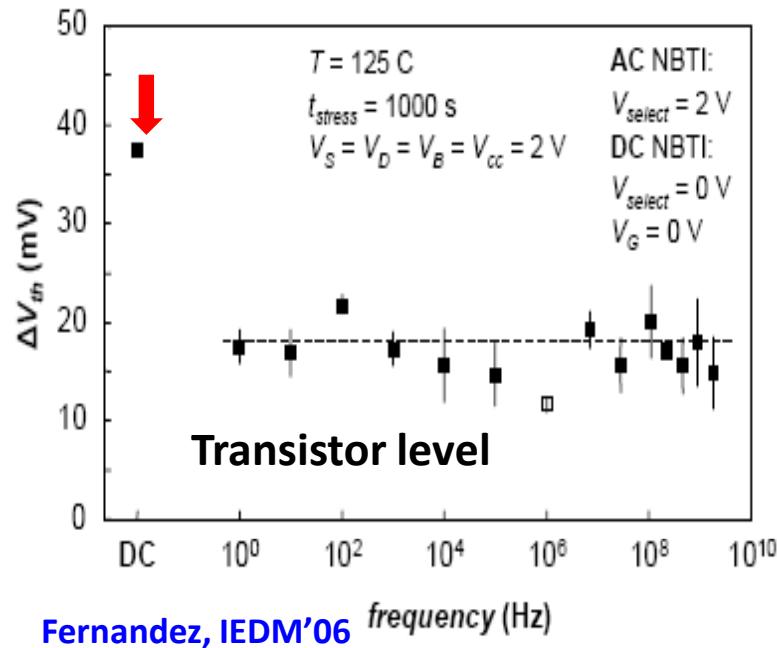


**Normalized to DC,
independent of
pulse low bias**

**Identical AC/DC
versus duty cycle for
both measurements**

**Prediction by RD
model solution of
interface traps**

NBTI Frequency (In)dependence

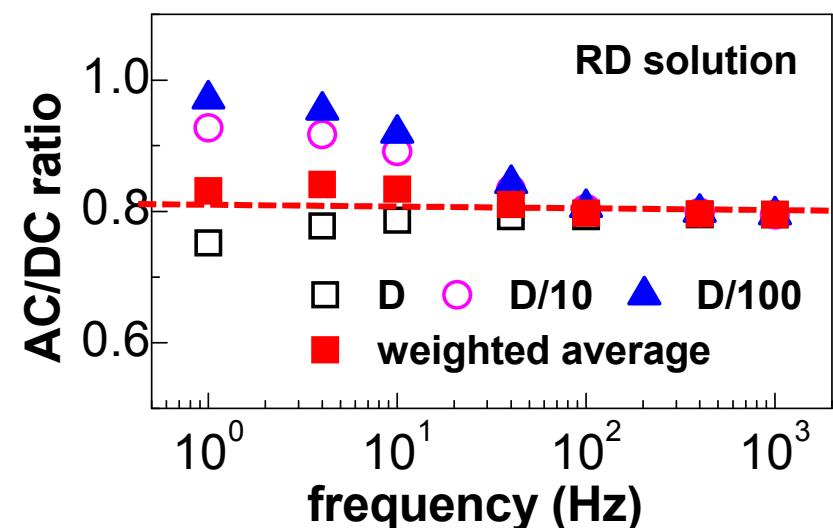
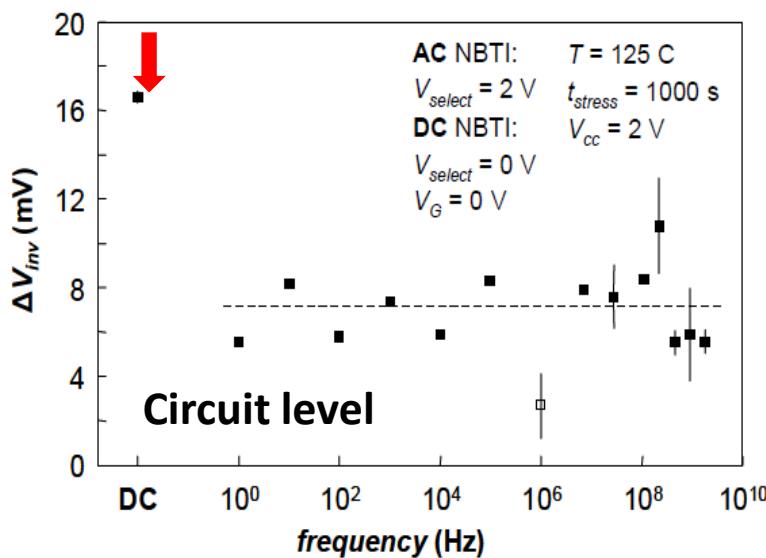


Careful experiments (on-chip circuitry to minimize high frequency reflection) shows frequency independent AC NBTI

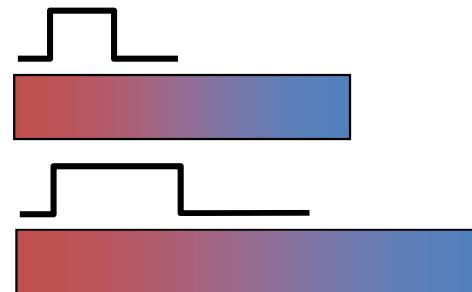
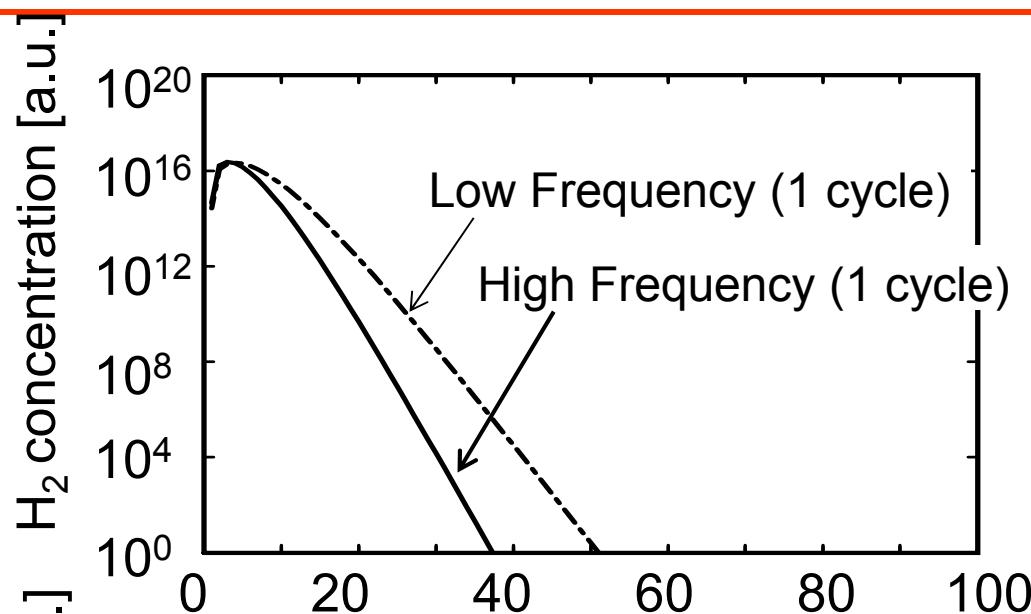
Similar observation in circuits (inverter max gain point shift)

Prediction by RD, but higher AC/DC ratio

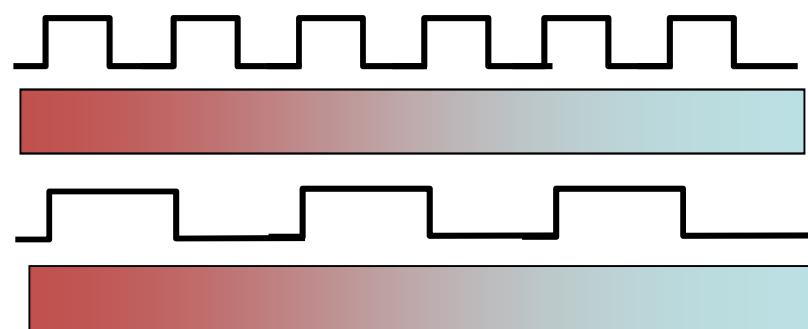
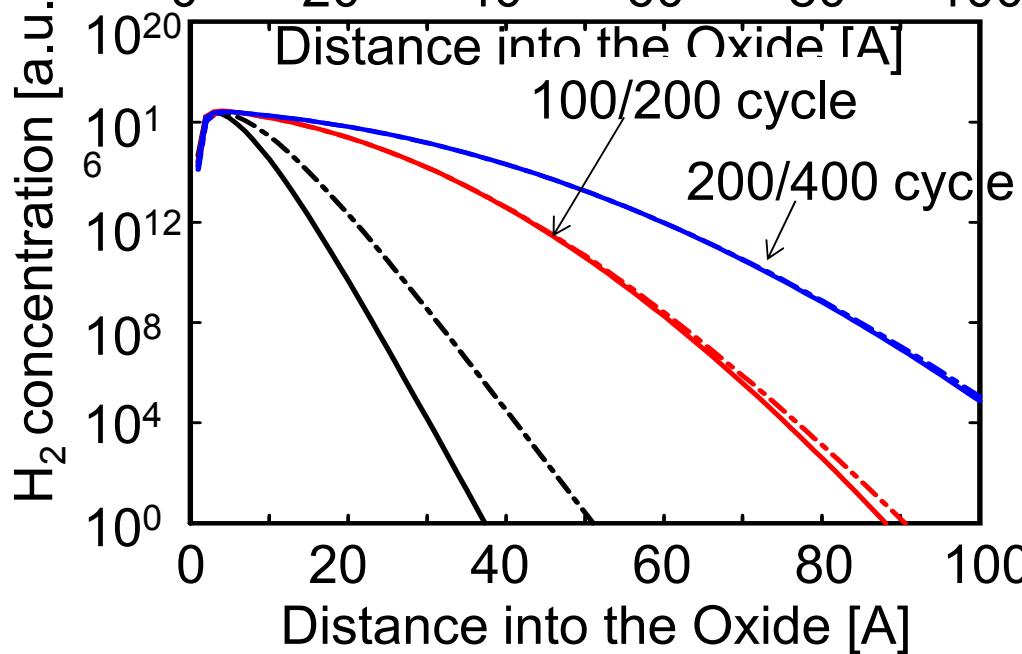
Additional hole trapping at DC stress



RD solution: NBTI Frequency Independence



Low f – greater distance of H_2 diffusion in oxide bulk



Multiple cycles – equal distance of H_2 diffusion in oxide bulk

Summary – Model for NBTI Recovery & AC Stress

NBTI recovery transients at different stress time & T and different devices can be successfully explained using a RD model based framework with similar parameters as stress

Early part of recovery dominated by hole detrapping, long time part by recovery of interface traps

The proposed physical mechanism is consistent with experimental results obtained from multiple methods

AC duty cycle dependence of degradation shows universality when properly normalized and can be successfully explained

Observed frequency independence of AC (~50% duty) degradation is an inherent outcome of RD model

Outline

Introduction, Basic NBTI signatures

Fast / Ultra-fast drain current degradation measurement

Estimation of pre-existing and generated defects

Transistor process / material dependence

Role of Nitrogen – Study by Ultrafast measurement

Predictive modeling

Conclusions / outlook ←

Conclusions

NBTI in both SiON and Hi-K/MG devices results in generation of interface traps plus hole trapping in pre-existing and generated bulk traps (mutually uncoupled components)

Pre-existing traps **primarily** responsible for NBTI material dependence; reduction by suitable processes

Bulk trap generation reduction at lower stress bias

Interface trap generation shows universality in long-time degradation, AC duty and frequency dependence, responsible for long-time NBTI failure

Reaction-Diffusion (R-D) model can fully explain NBTI stress, recovery and AC degradation once hole trapping contribution is identified and isolated



Outlook

NBTI in scaled IL HKMG stacks → Possible role of 2nd (IL/HK) interface

Develop physics based trapping model for large area device and correlation to flicker noise → Completion of macroscopic model

Extension of macroscopic trap generation model to small area device

Extension of macroscopic trapping model to small area device and correlation to RTN

Suitable addition of microscopic trap generation and trapping models to predict long time NBTI variability for small area devices