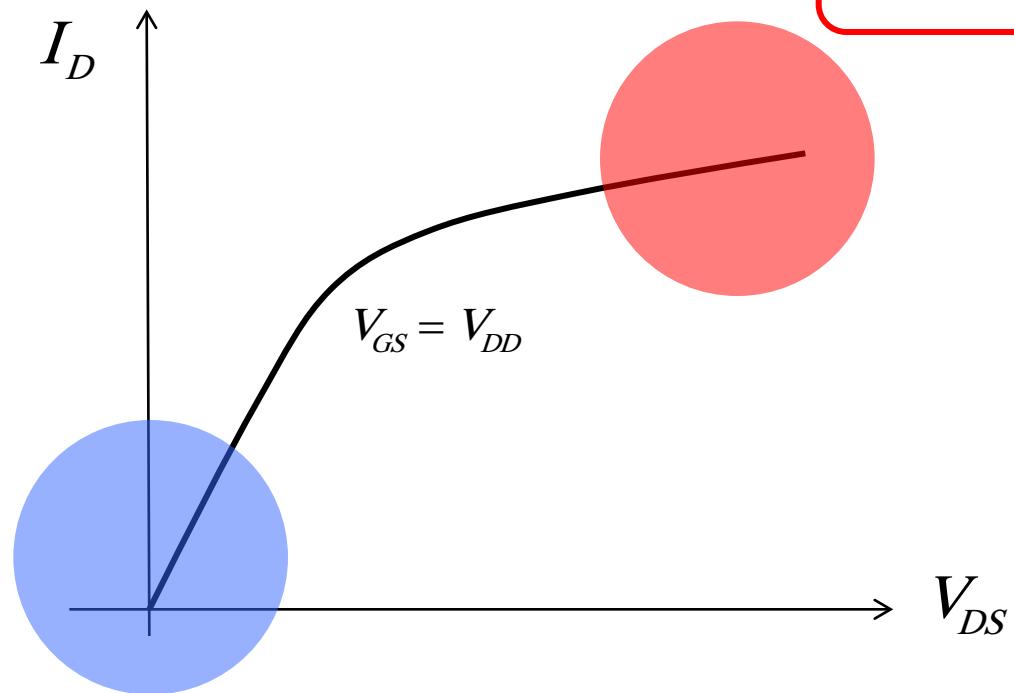


Lecture 7: Comparison to Experimental Results

Mark Lundstrom

Electrical and Computer Engineering
Network for Computational Nanotechnology
and
Birck Nanotechnology Center
Purdue University
West Lafayette, Indiana USA

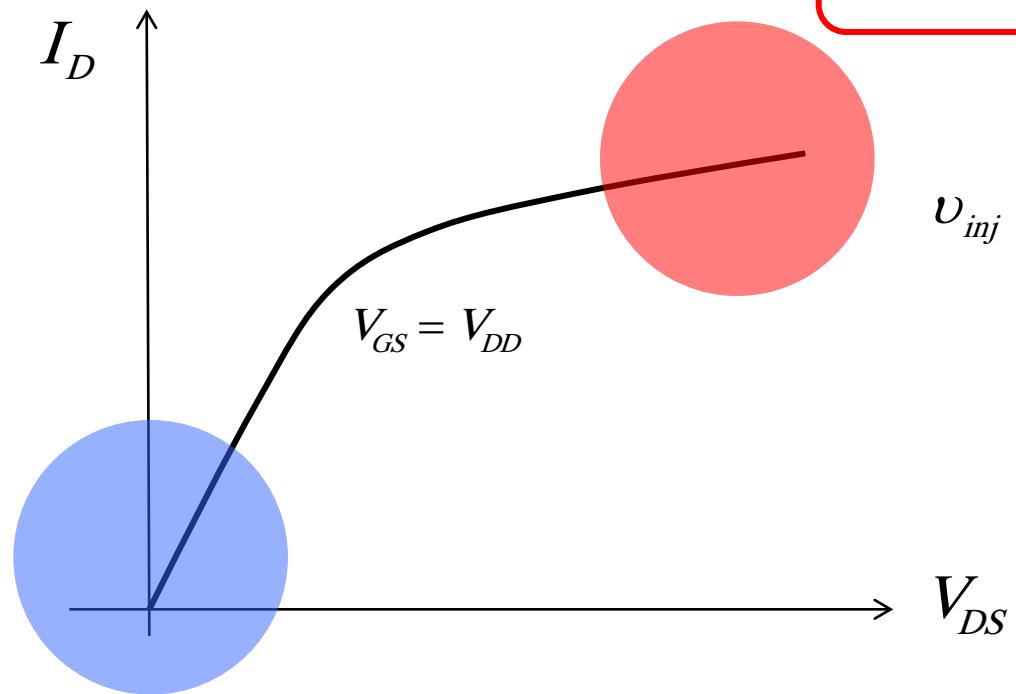
ballistic MOSFET (MB)



$$I_D = WC_{inv}v_T(V_{GS} - V_T)$$

$$I_D = WC_{inv} \frac{v_T}{2k_B T_L/q} (V_{GS} - V_T) V_{DS}$$

ballistic MOSFET (FD)



$$I_D = WC_{inv}v_{inj}(V_{GS} - V_T)$$

$$v_{inj} \equiv \sqrt{\frac{2k_B T_L}{\pi m^*}} \frac{\mathcal{F}_{1/2}(\eta_{F1})}{\mathcal{F}_0(\eta_{F1})}$$

$$I_D = WC_{inv} \frac{v_T}{2k_B T_L/q} \left(\frac{\mathcal{F}_{-1/2}(\eta_{F1})}{\mathcal{F}_0(\eta_{F1})} \right) (V_{GS} - V_T) V_{DS}$$

question

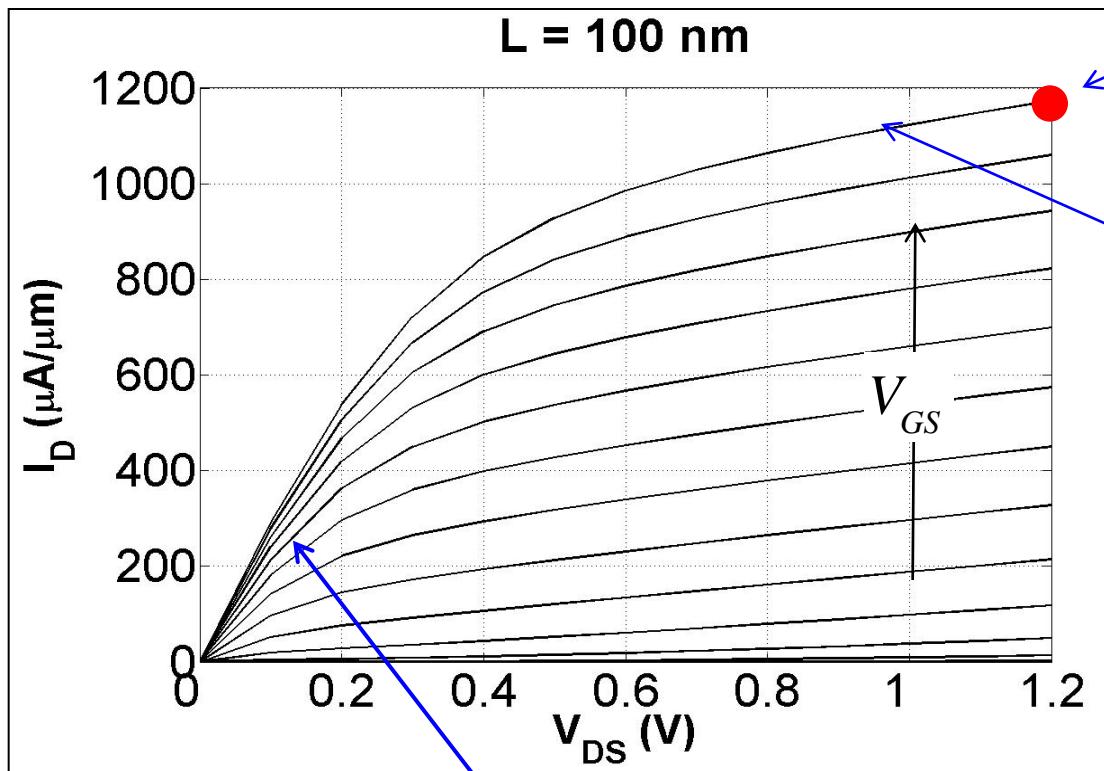
How close to the ballistic limit do modern MOSFETs operate?

2007 MOSFET parameters

- 1) Nitrided gate oxide ($k = 5$, $t_{inv} = 2.2$ nm)
- 2) Polysilicon gate
- 3) $V_{DD} = 1.2$ V
- 4) Not intentionally strained
- 5) All measurements at $T \sim 300$ K
- 6) Minimum mask channel length = 100 nm
Minimum physical channel length (SEM) = 85 nm

(Thanks to Shuji Ikeda of ATDF for supplying these devices in Dec. 2007)

ballistic MOSFET: linear region

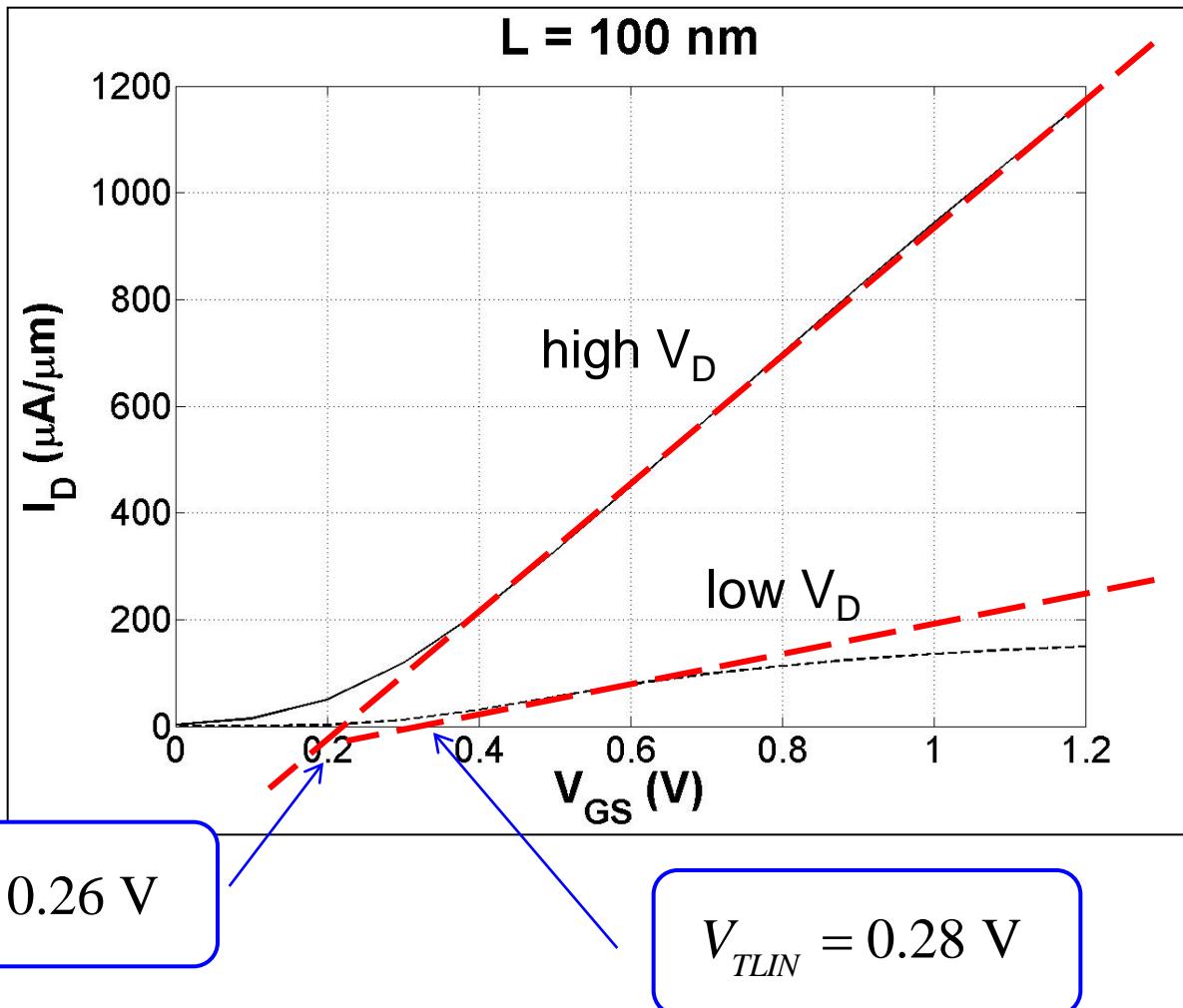


$$I_{ON} = 1120 \left(\mu\text{A}/\mu\text{m} \right)$$

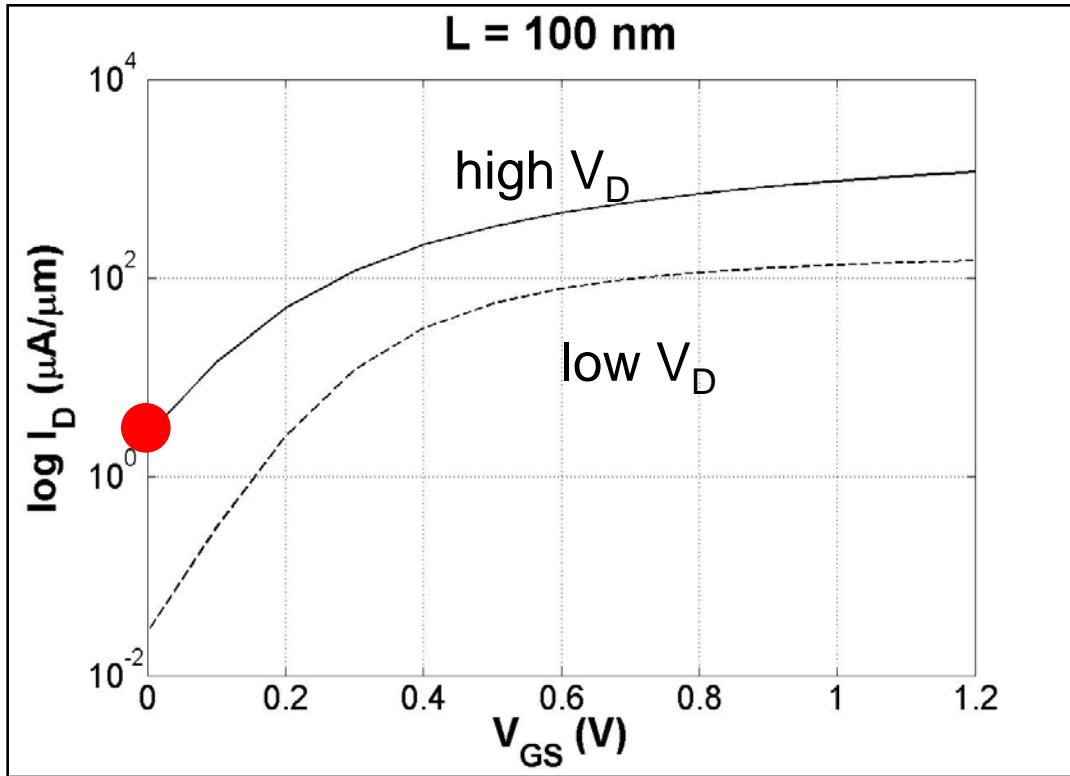
$$r_d = 4.7 \text{ } k\Omega - \mu\text{m}$$

$$R_{TOT} = \frac{V_{DS}}{I_D} = 340 \left(\Omega - \mu\text{m} \right)$$

transfer characteristics



$\log_{10} I_D$ vs. V_{GS}

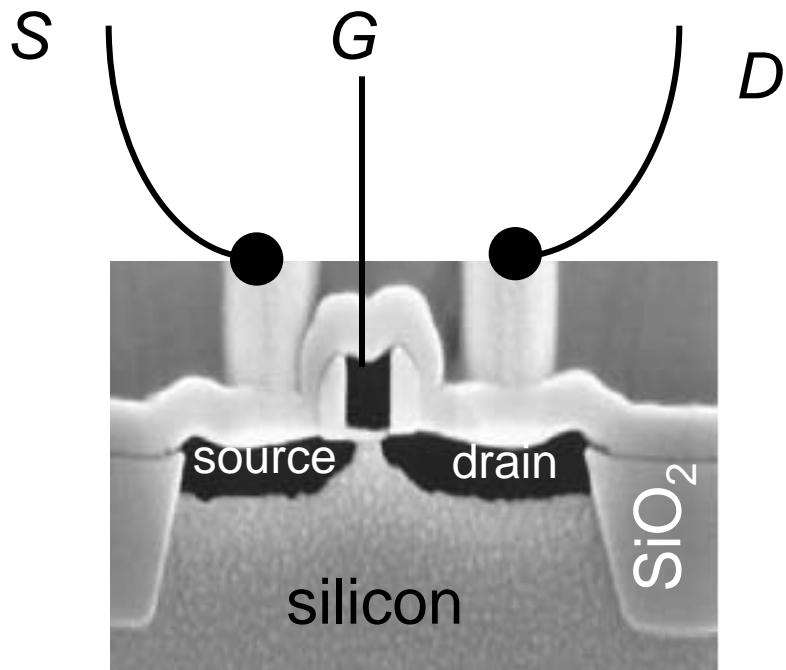


$$I_{OFF} = 0.95 (\mu\text{A}/\mu\text{m})$$

$$S = 110 (\text{mV/dec})$$

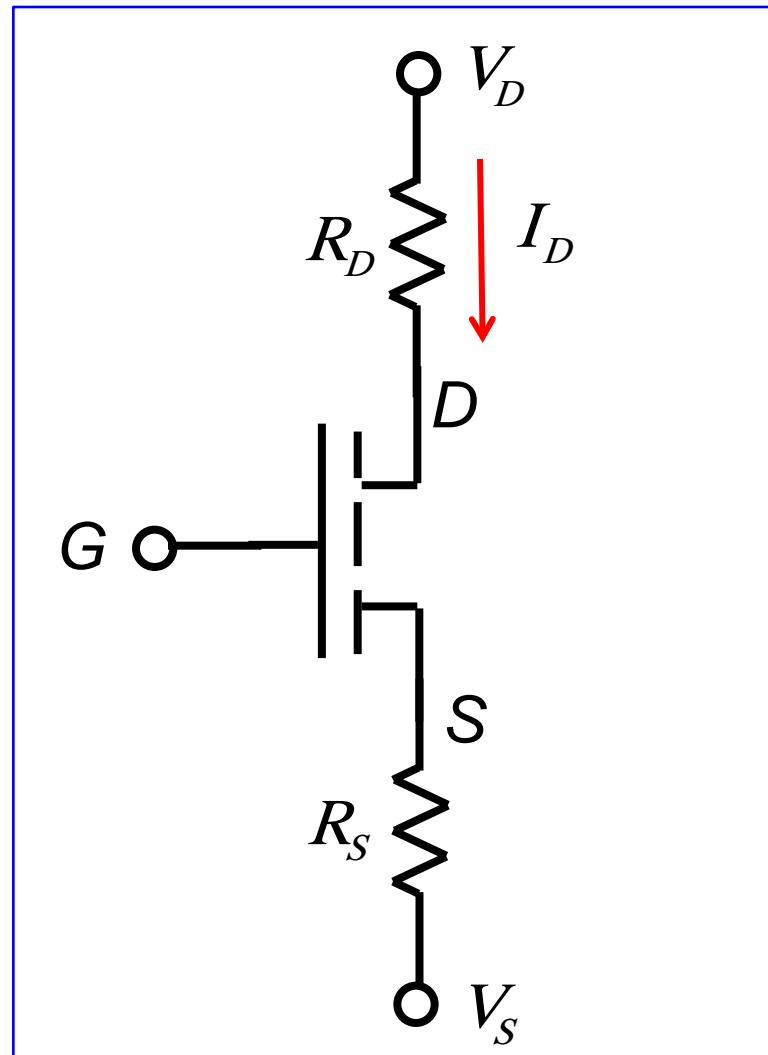
$$DIBL = 170 (\text{mV/V})$$

series resistance

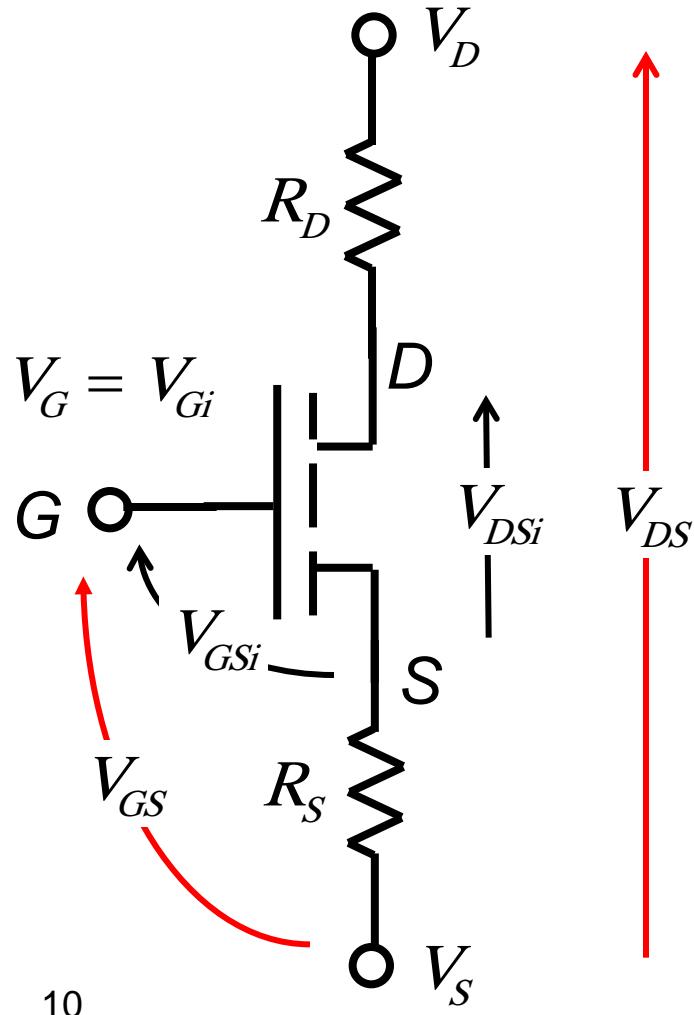


(Texas Instruments, ~ 2000)

The actual voltages applied to the terminals are larger than the voltages on the “intrinsic” device.



series resistance



$$V_{Gi} = V_G$$

$$V_{Di} = V_D - I_D(V_G, V_{Si}, V_{Di})R_D$$

$$V_{Si} = V_S + I_D(V_G, V_{Si}, V_{Di})R_S$$

Given the terminal voltages and the IV characteristic of the intrinsic device, we can solve these two equations for the intrinsic voltages.

on-current analysis (injection velocity)

$$I_{ON} = 1120 \text{ } (\mu\text{A}/\mu\text{m}) \quad \dashrightarrow \quad I_{ON} = WQ_n(0) \langle v(0) \rangle$$

$$R_s + R_D = R_{SD} = 200 \pm 50 \text{ } (\Omega - \mu\text{m})$$

$$V_{GSi} = 1.2 - I_{ON} R_s = 1.2 - 0.11 = 1.09V$$

$$C_{inv} = \frac{\kappa_{ox} \epsilon_0}{T_{inv}} = 1.54 \times 10^{-6} \text{ F/cm}^2$$

$$Q_n(0) = C_{inv} (V_{GSi} - V_{TSAT})$$

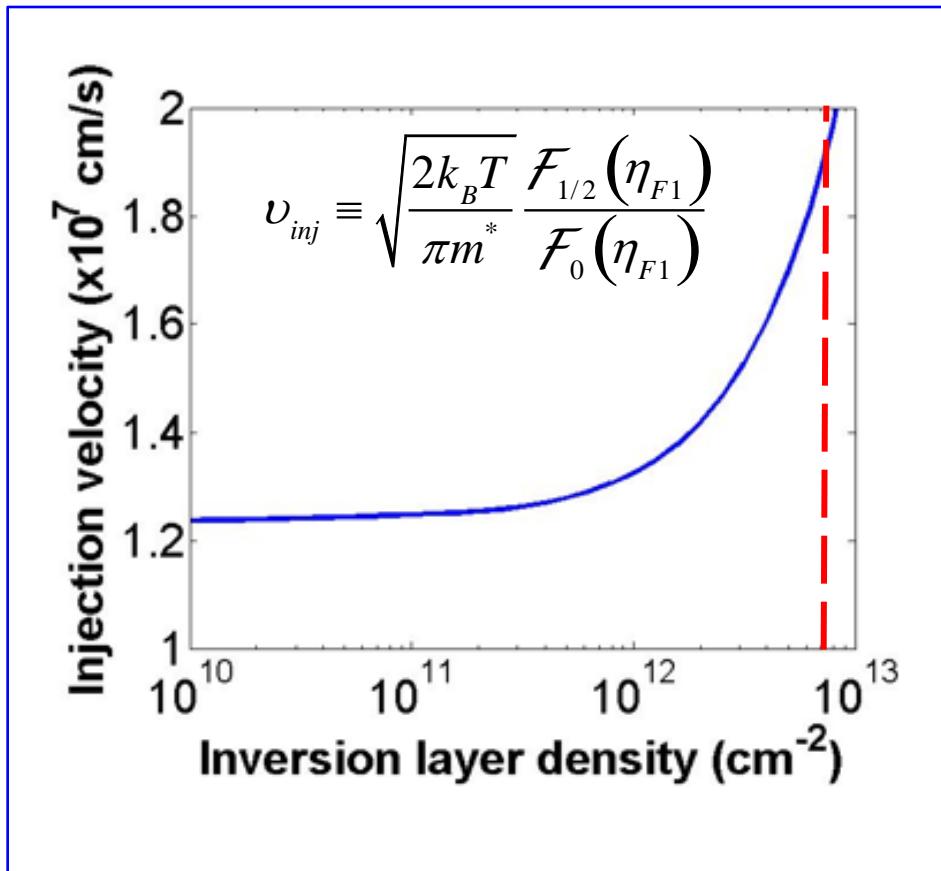
$$Q_I(0)/q ; 7.9 \times 10^{12} \text{ cm}^{-2}$$

$$\langle v(0) \rangle = \frac{I_{ON}}{WQ_n(0)}$$

$$\langle v(0) \rangle = 8.7 \times 10^6 \text{ cm/sec}$$

How close is this to the ballistic injection velocity?

comparison to ballistic limit



$$Q(0)/q \simeq 7.9 \times 10^{12} \text{ cm}^{-2}$$

$$\langle v_{inj} \rangle = 1.9 \times 10^7 \text{ cm/sec}$$

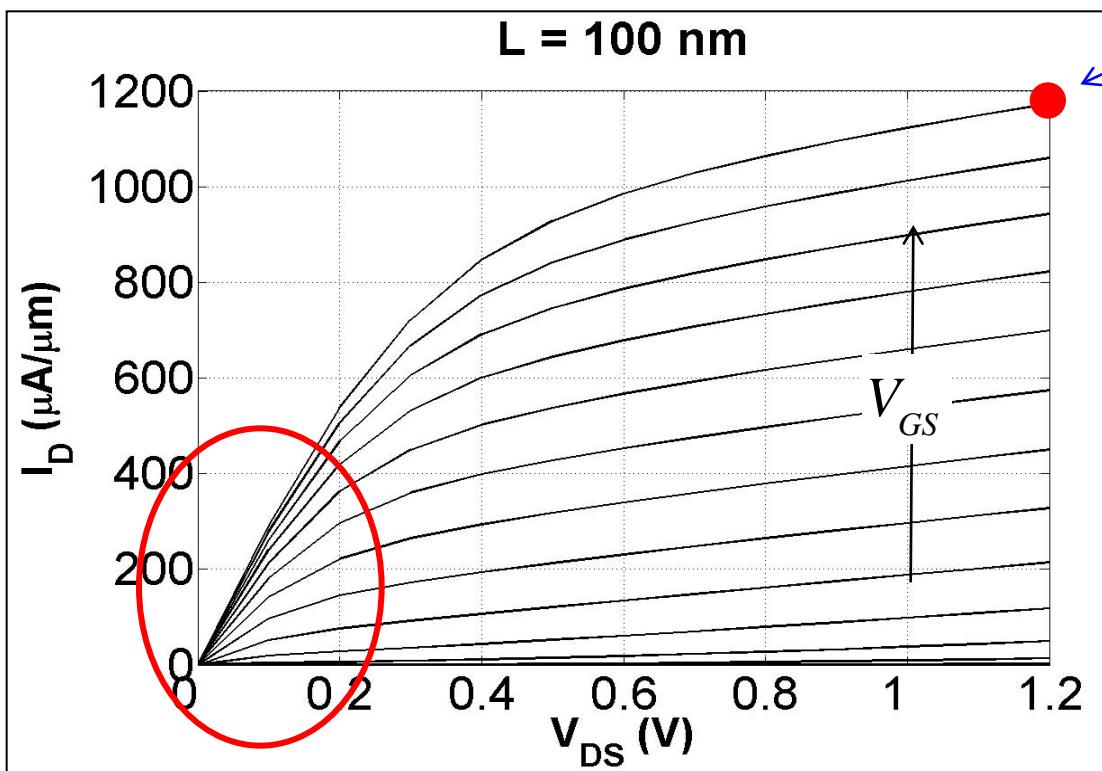
We know the actual velocity,

$$\langle v(0) \rangle = 8.7 \times 10^6 \text{ cm/sec}$$

so we can estimate the ballistic ratio:

$$B_{sat} = \frac{I_{ON}}{I_{ON}(\text{ball})} = \frac{\langle v(0) \rangle}{v_{inj}}$$

ballistic MOSFET: saturated region



$$I_{ON} = 1120 (\mu\text{A}/\mu\text{m})$$

$$B_{sat} = \frac{I_{ON}}{I_{ON}(\text{ball})} \approx 0.45$$

Modern Si MOSFETs operate at about one-half of the ballistic limit.

What about the linear region?

linear-current analysis (injection velocity)

$$R_{TOT} \left(V_{GS} = 1.2V \right) = \frac{V_{DS}}{I_D} = 340 \left(\Omega - \mu m \right)$$

$$R_{TOT} = R_{CH} \left(V_{GS} \right) + R_{SD}$$

$$R_{SD} = R_S + R_D \approx 200 \left(\Omega - \mu m \right)$$

$$R_{CH} \approx 140 \left(\Omega - \mu m \right)$$

$$V_{GSi} = V_{GS} - I_D R_S \approx V_{GS}$$

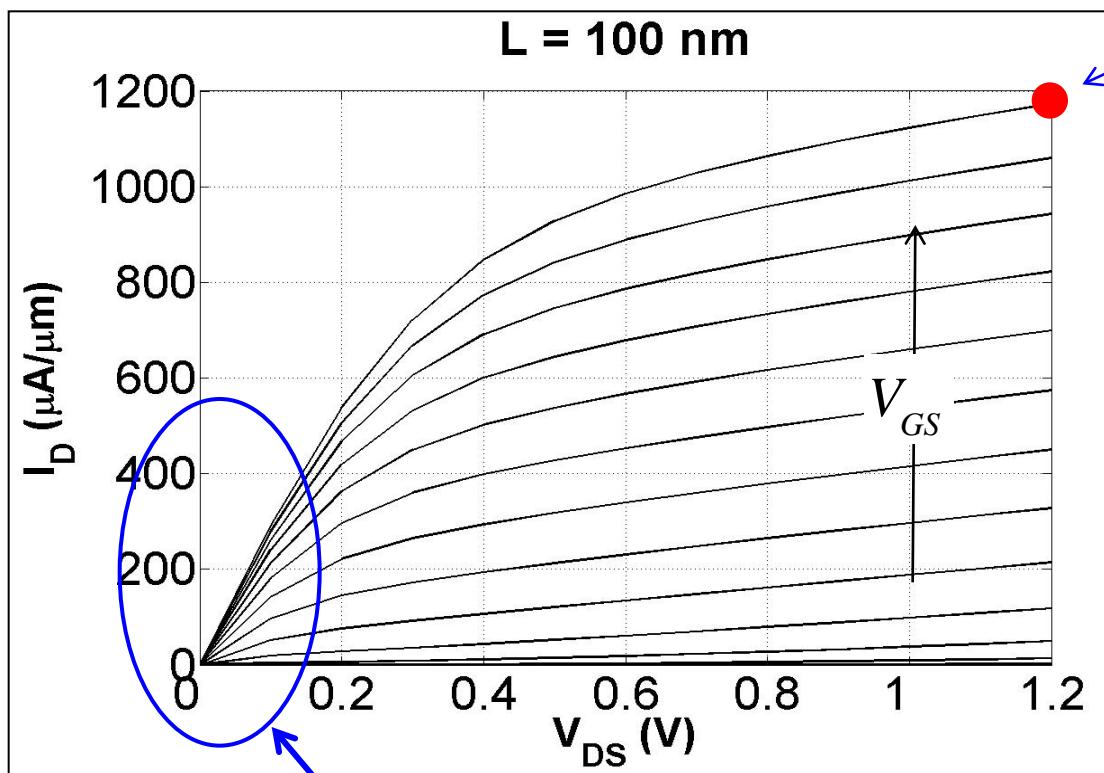
$$Q_n(0) = C_{inv} \left(V_{GSi} - V_{TLIN} \right)$$

$$Q_I(0)/q ; 8.9 \times 10^{12} \text{ cm}^{-2}$$

$$R_{CH}^{ball} = \frac{(2k_B T/q)}{Q_I(0) v_T} \frac{\mathcal{F}_0(\eta_{F1})}{\mathcal{F}_{-1/2}(\eta_{F1})} \approx 55 \Omega - \mu m$$

The measured channel conductance is less than one-third of the ballistic channel conductance.

ballistic MOSFET



$$I_{ON} = 1120 (\mu\text{A}/\mu\text{m})$$

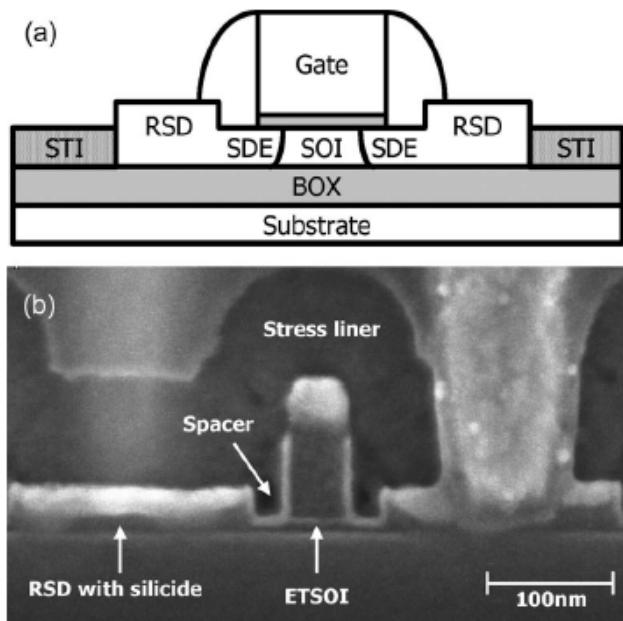
$$B_{sat} = \frac{I_{ON}}{I_{ON}(\text{ball})} \approx 0.45$$

Modern Si MOSFETs operate somewhat closer to the ballistic limit under high drain bias as compared to low drain bias.

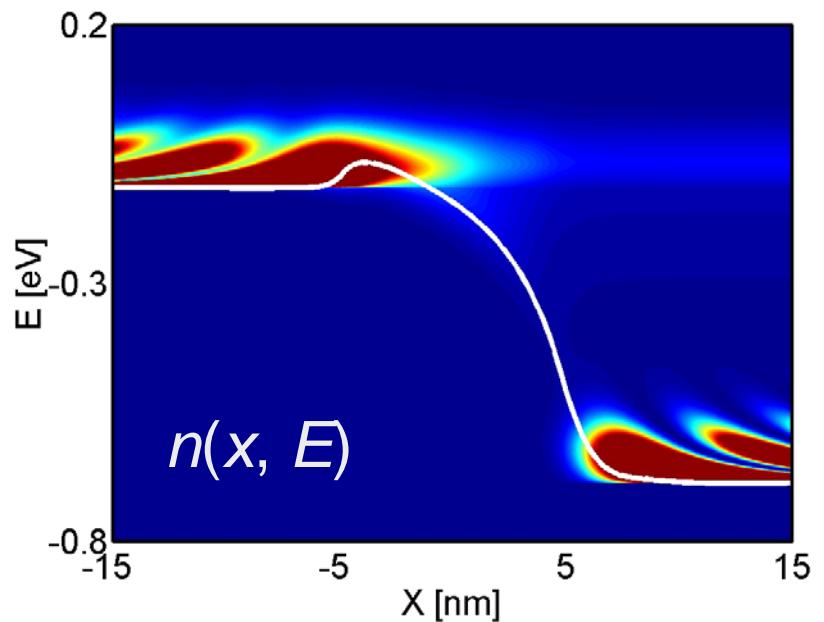
$$R_{CH} \approx 140 (\Omega - \mu\text{m}) \quad B_{lin} = \frac{I_{lin}}{I_{lin}(\text{ball})} \approx 0.35$$

comparison to numerical simulations

ETSOI



nanoMOS

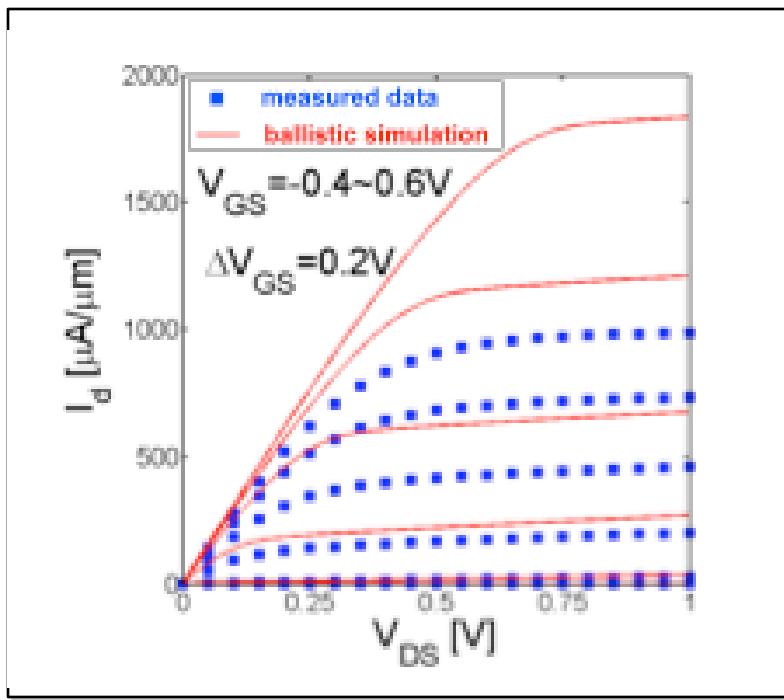


A. Majumdar, Z. Ren, S. J. Koester, and W. Haensch, Undoped-Body Extremely Thin SOI MOSFETs With Back Gates, *IEEE Trans. Electron Dev.*, **56**, 2270-2276, 2009.

www.nanoHUB.org

comparison with experiment: Silicon

$$L_G = 40 \text{ nm}$$



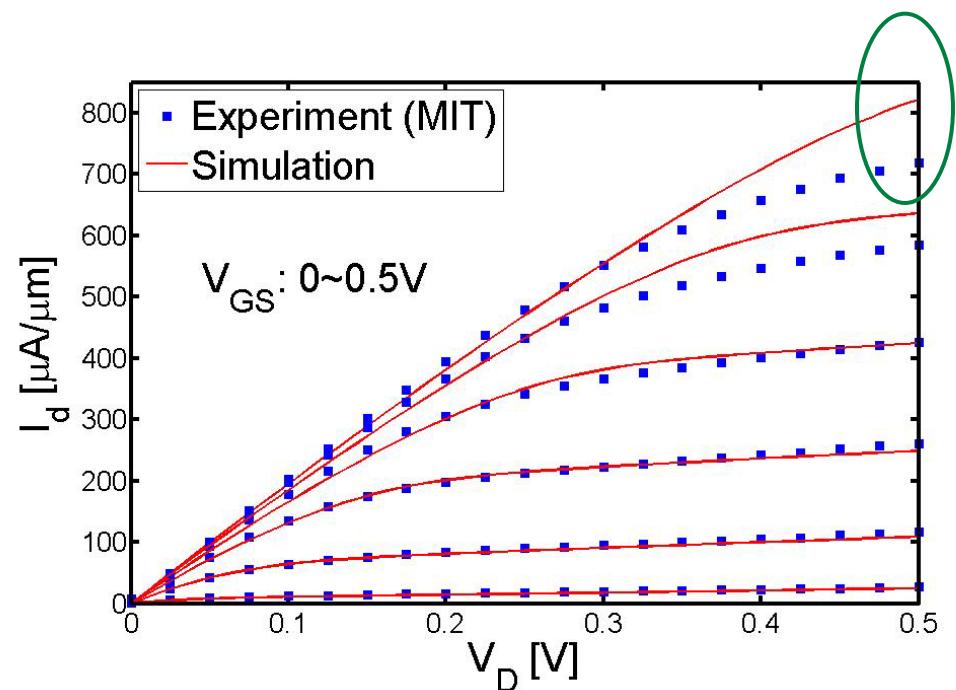
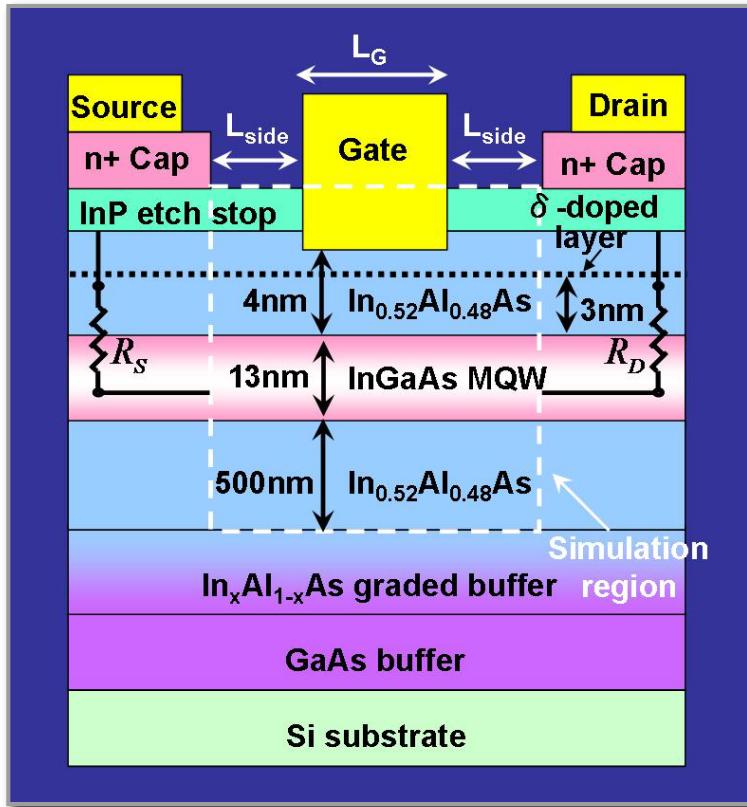
$$I_{Dlin} / I_{ballistic} \approx 0.2$$

$$I_{ON} / I_{ballistic} \approx 0.6$$

- Si MOSFETs deliver > one-half of the ballistic on-current. (Similar for the past 15 years.)
- MOSFETs operate closer to the ballistic limit under high V_{DS} .

A. Majumdar, Z. B. Ren, S. J. Koester, and W. Haensch, "Undoped-Body Extremely Thin SOI MOSFETs With Back Gates," *IEEE Transactions on Electron Devices*, **56**, pp. 2270-2276, 2009.

comparison with experiment: InGaAs HEMTs



Jesus del Alamo group (MIT)

wrap-up

Si MOSFETs operate at roughly half of the ballistic limit.

III-V FETs operate close to the ballistic limit.

To understand this, we need to discuss scattering.

But first, let's see if the theory of the ballistic MOSFET can be related to conventional (diffusive) MOSFET theory.

references

For some examples of analyzing experimental data, see:

A. Khakifirooz, O. M. Nayfeh, and D. Antoniadis, “A Simple Semiempirical Short-Channel MOSFET Current–Voltage Model Continuous Across All Regions of Operation and Employing Only Physical Parameters,” *IEEE Trans. Electron Dev.*, **56**, 1674-1680, 2009.

D.-H. Kim, J. A. del Alamo, D. A. Antoniadis and B. Brar, “Extraction of Virtual-Source Injection Velocity in sub-100 nm III-V HFETs,” Intern. Electron Dev. Meeting, 2009.

A. Khakifirooz and D. A. Antoniadis, “Transistor Performance Scaling: The Role of Virtual Source Velocity and Its Mobility Dependence,” Intern. Electron Dev. Meeting, 2006.