

Scientific Overview: Nanoscale Transistors

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electronics and the world today

The 20th Century was transformed by **electronic and microelectronic** device technologies.

The 21st Century will be transformed by **nanoelectronic** device technologies – especially transistors.

The nanotransistor is the first true, active nanodevice.

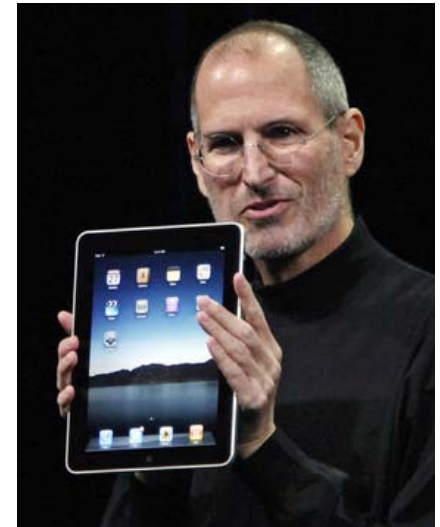
transistors (and IC's) created the modern world



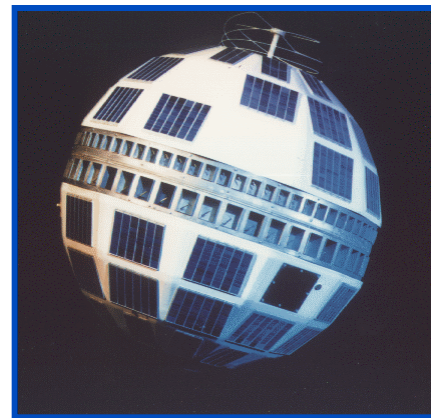
supercomputers



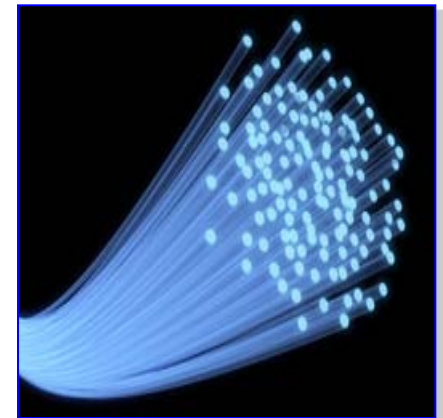
PCs



cell phones



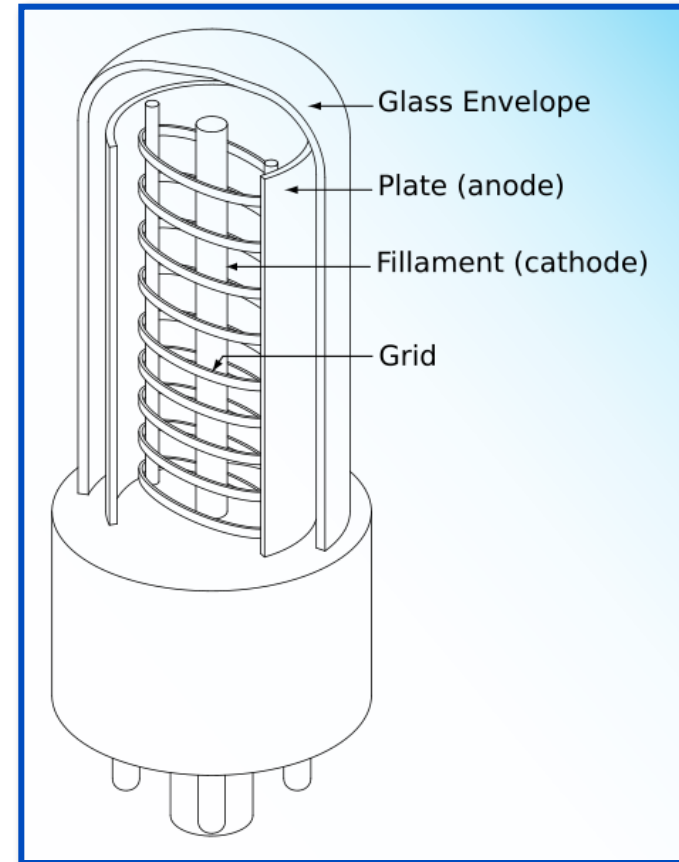
communication satellites



fiber optics / Internet

vacuum tube electronics

Vacuum Tube



Edison effect (Edison, 1883)
cathode rays (Thompson, 1897)
diode (Fleming, 1904)
triode (De Forest, 1905)

http://en.wikipedia.org/wiki/Vacuum_tube

what vacuum tubes enabled

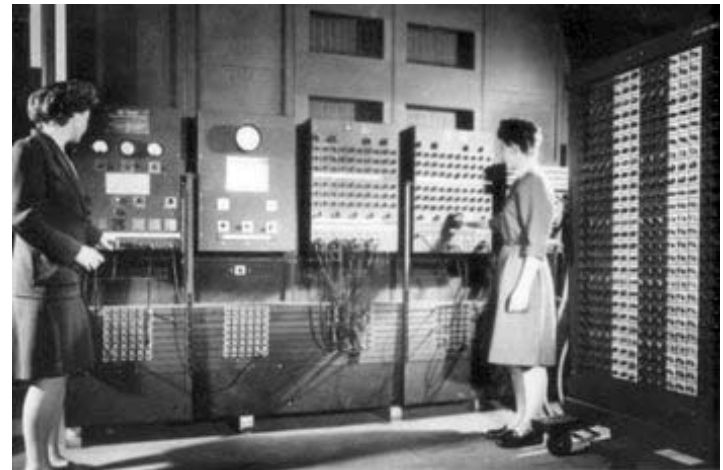
“Golden age of radio”
1935 - 1950



<http://history.sandiego.edu/GEN/recording/images5/radio11.jpg>

ENIAC

(1945, Mauchly and Eckert, U Penn)

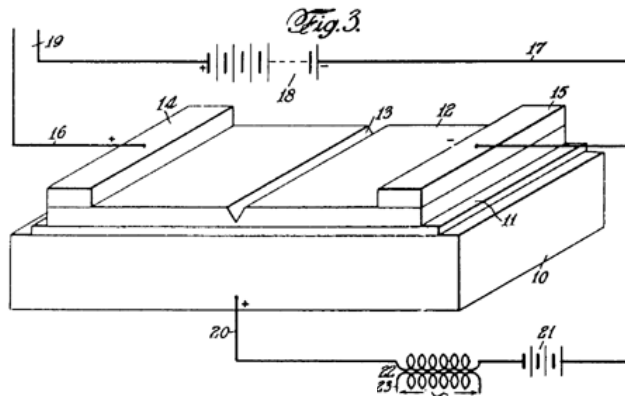


<http://en.wikipedia.org>

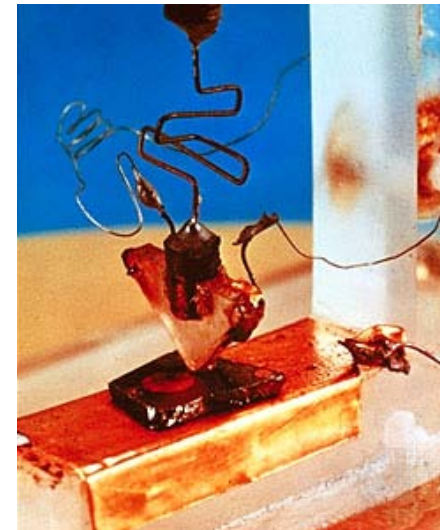
17,468 vacuum tubes
1000 sq. feet of floor space
30 tons
150 KW
~50 vacuum tubes / day

invention of the transistor

Field-Effect Transistor
Lillienfeld, 1925
Heil, 1935



Bardeen, Shockley,
and Brattain, 1947



IV characteristics: real current sources

transistors



Sony TR-63
6-transistor
shirt pocket radio
1957



<http://www.sony.net/Fun/SH/1-6/h2.html>

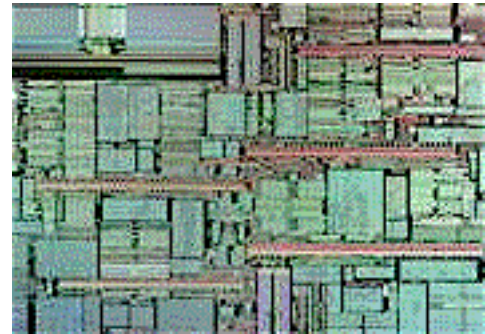
invention of the integrated circuit

integrated circuit



Kilby and Noyce (1958, 1959)

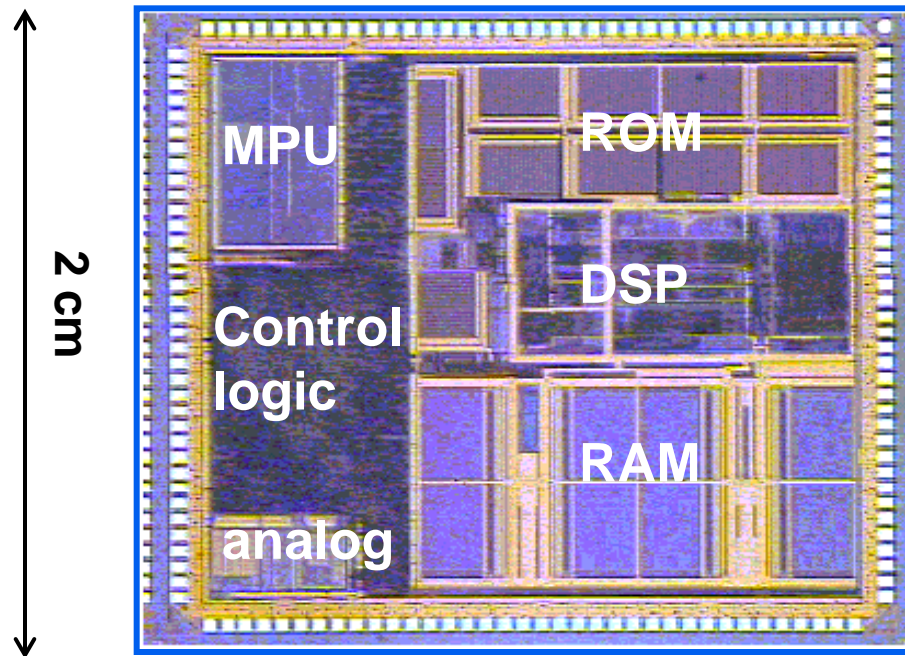
Intel 4004



Hoff and Faggin (1971)

~2200 transistors

modern CMOS chip



Texas Instruments cell phone chip

pH sensing with ISFETs

$$\text{pH} = -\log_{10} [\text{H}^+]_B$$

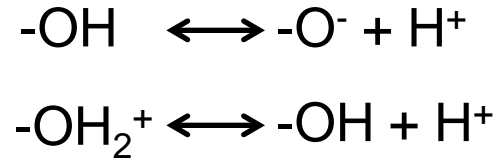
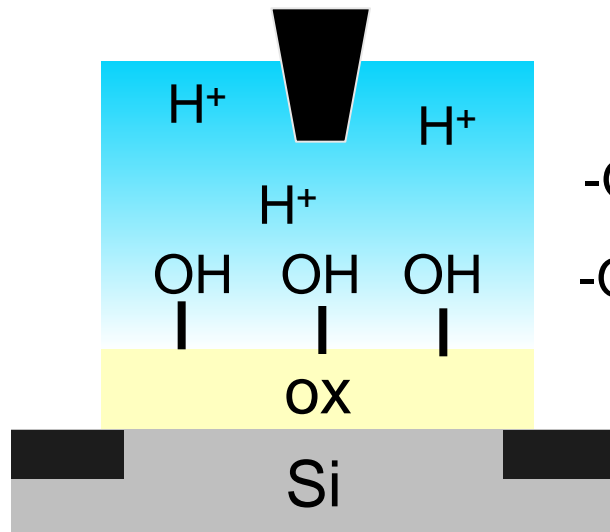
Acid

Alkaline



3 4 5 6 7 8 9 10 11

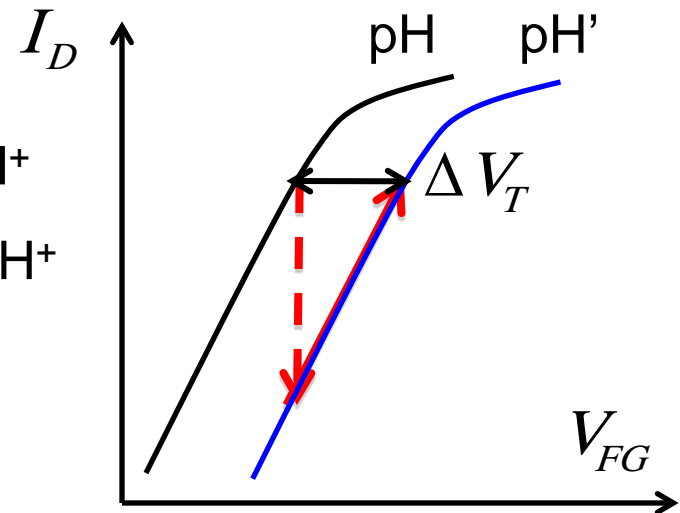
fluid gate



$$[\text{H}^+]_B = 10^{-7} \text{ M} \rightarrow \text{pH} = 7$$

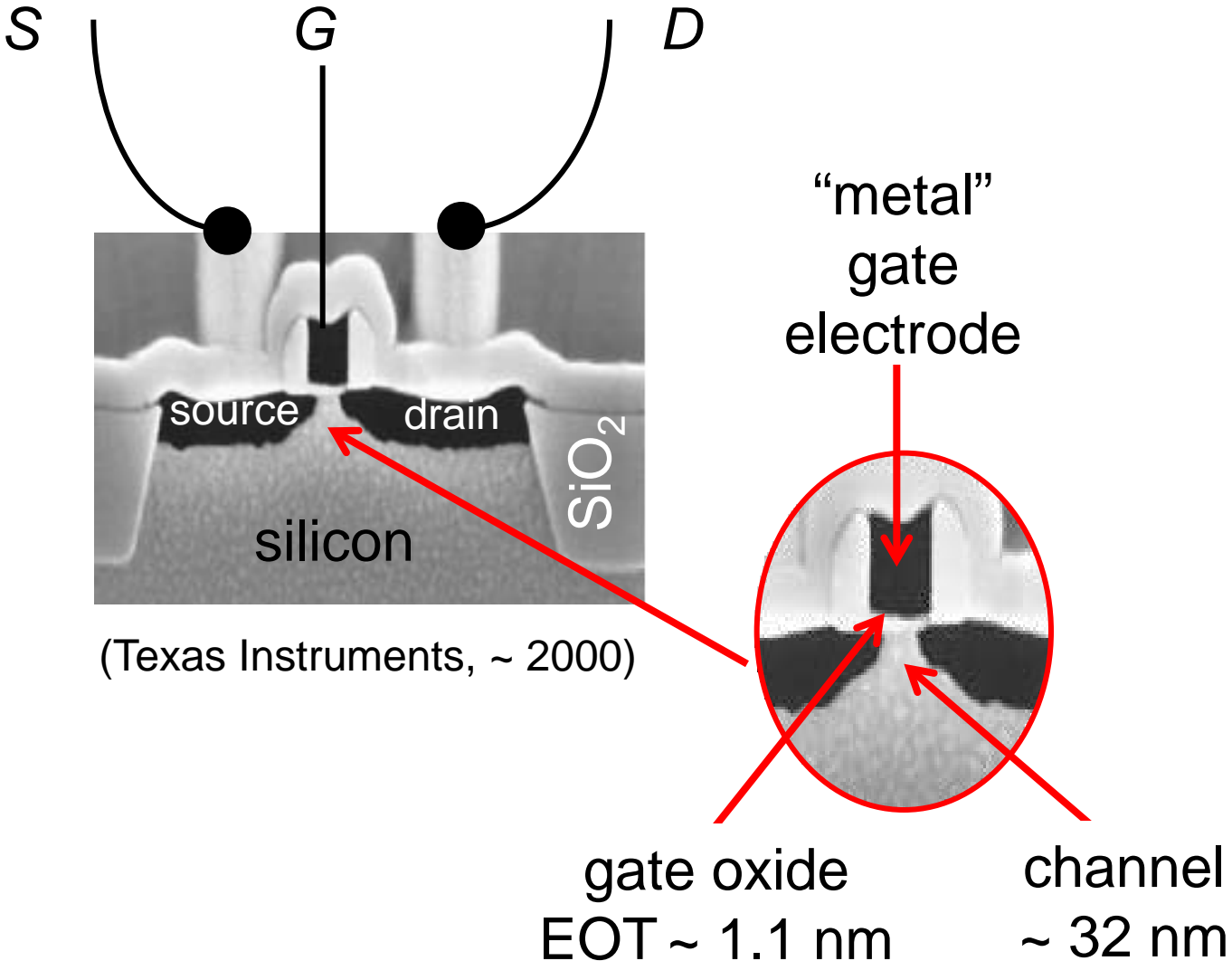
Human blood: $7.35 < \text{pH} < 7.45$

pH out of range is fatal!

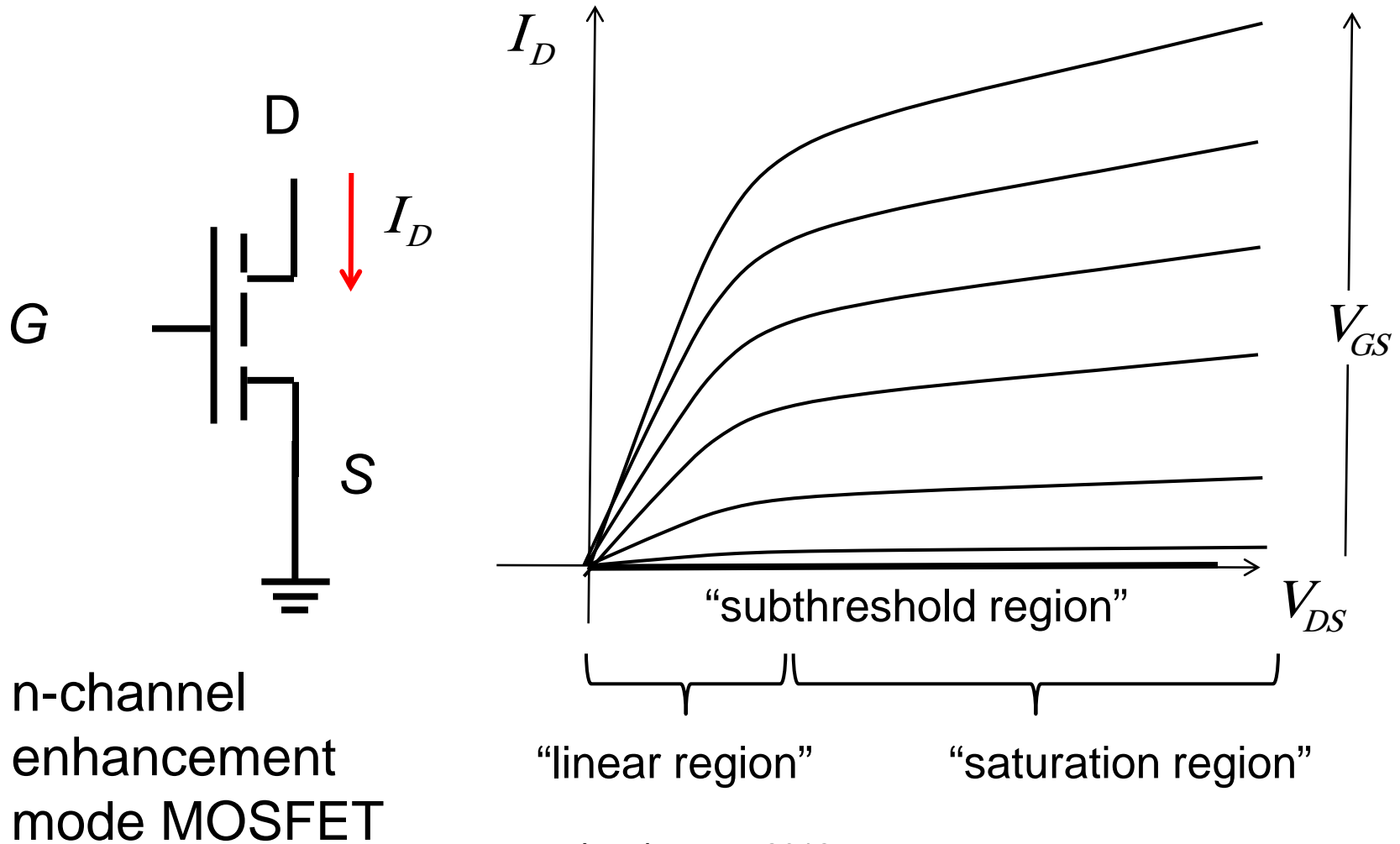


$$\text{pH sensitivity} = \Delta V_T / \Delta \text{pH} < 59 \text{ mV/pH (Nernst limit)}$$

nanoscale MOSFETs



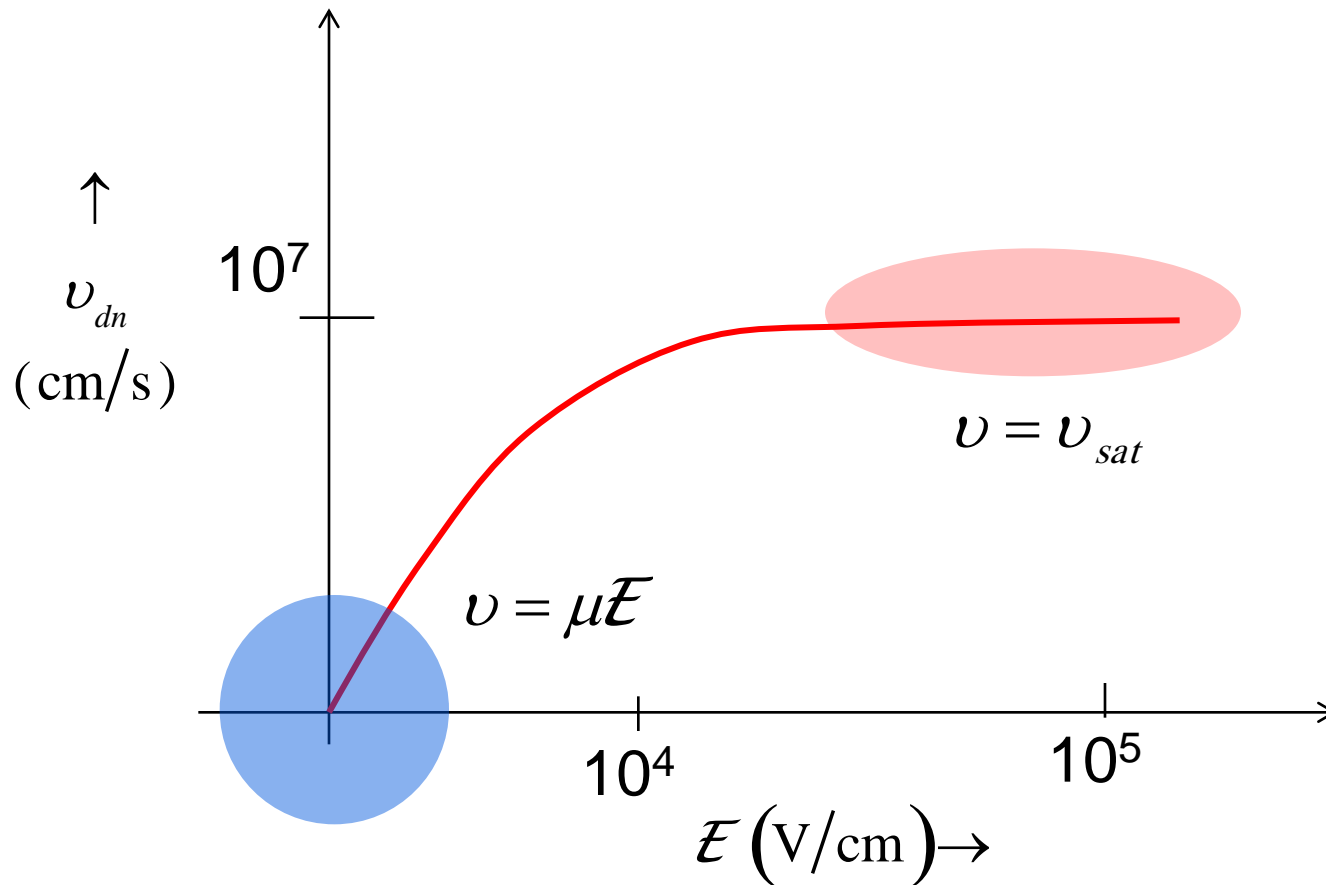
IV characteristics: transistors



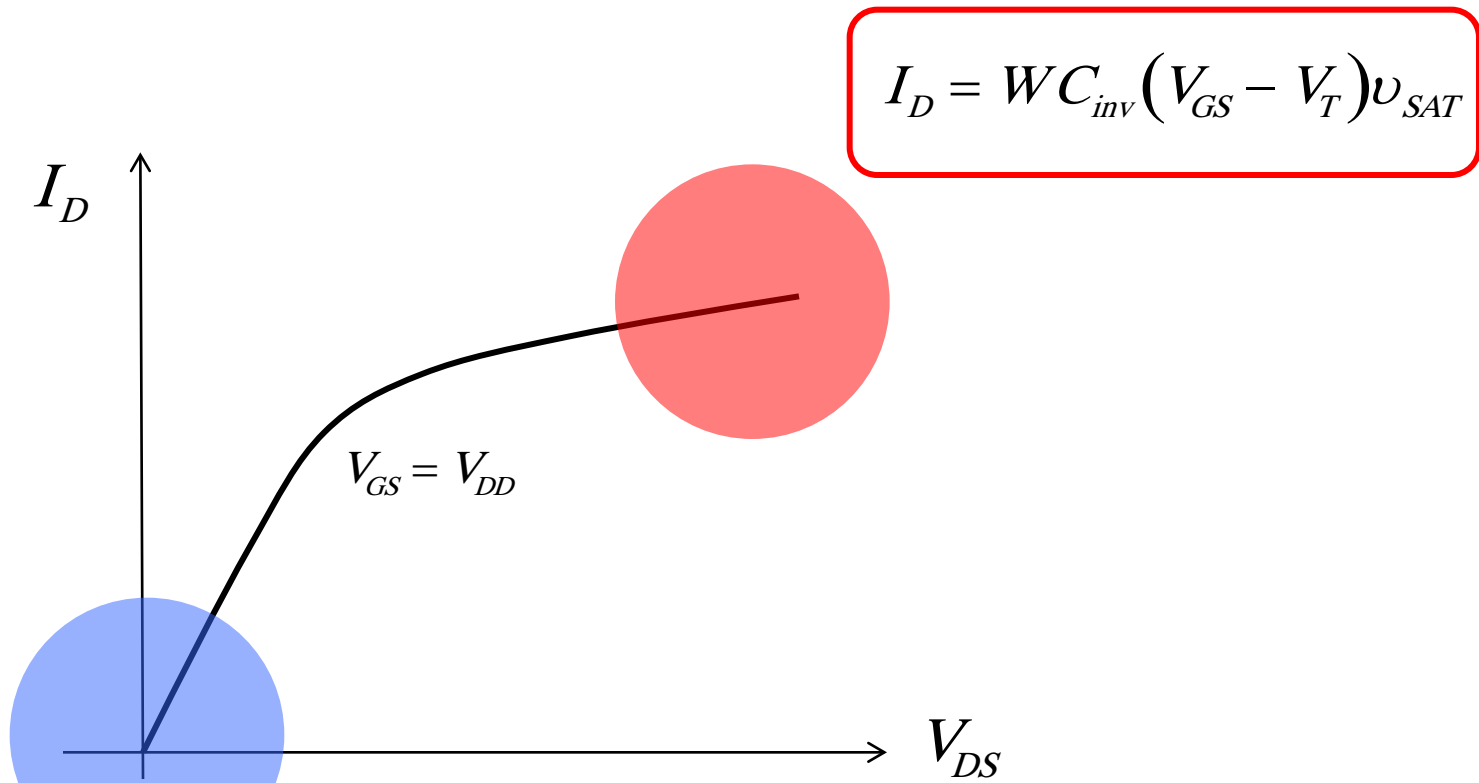
physics of MOSFETs

S.R. Hofstein and F.P. Heiman, "The Silicon Insulated-Gate Field-Effect Transistor," *Proc. IEEE*, pp. 1190-1202, Sept. 1963.

“drift-diffusion” carrier transport



traditional model of a MOSFET



$$I_D = WC_{inv}(V_{GS} - V_T)v_{SAT}$$

$$I_D = \frac{W}{L}\mu_{eff}C_{inv}(V_{GS} - V_T)V_{DS}$$

transport in a “long” device

$$v_x = \mu_n \mathcal{E}_x$$

$$D_n = \frac{v_T \lambda}{2}$$

$$\frac{D_n}{\mu_n} = \frac{k_B T}{q} = 0.026$$

$$v_T \approx 10^7 \text{ cm/s}$$

$$\mu_{\text{eff}} \approx 300 \text{ cm}^2/\text{V-s}$$

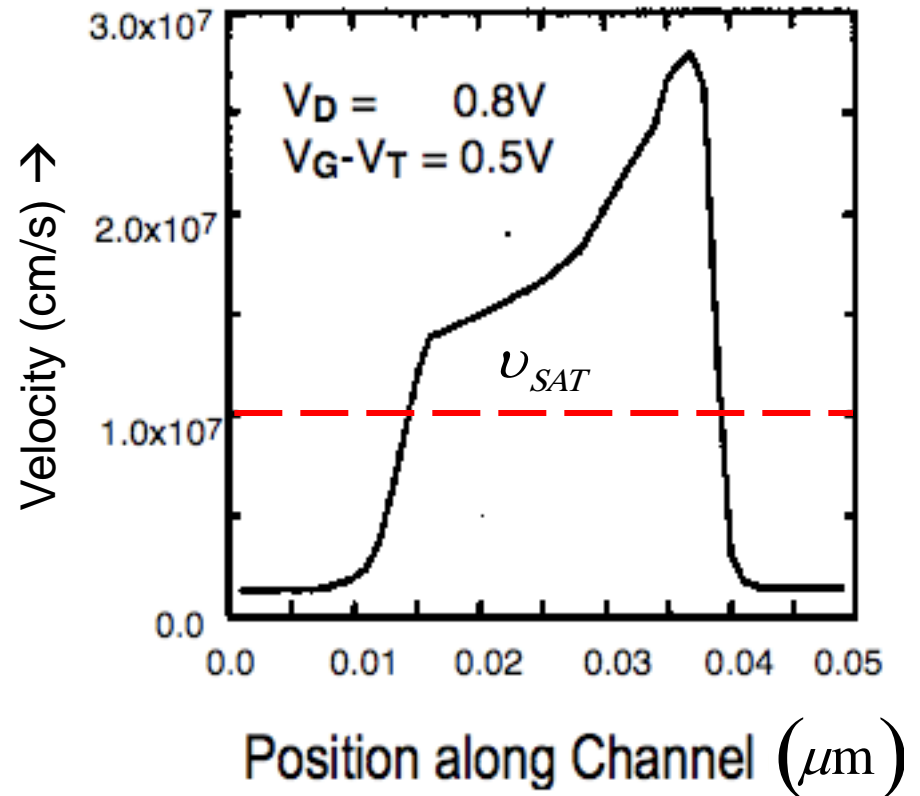
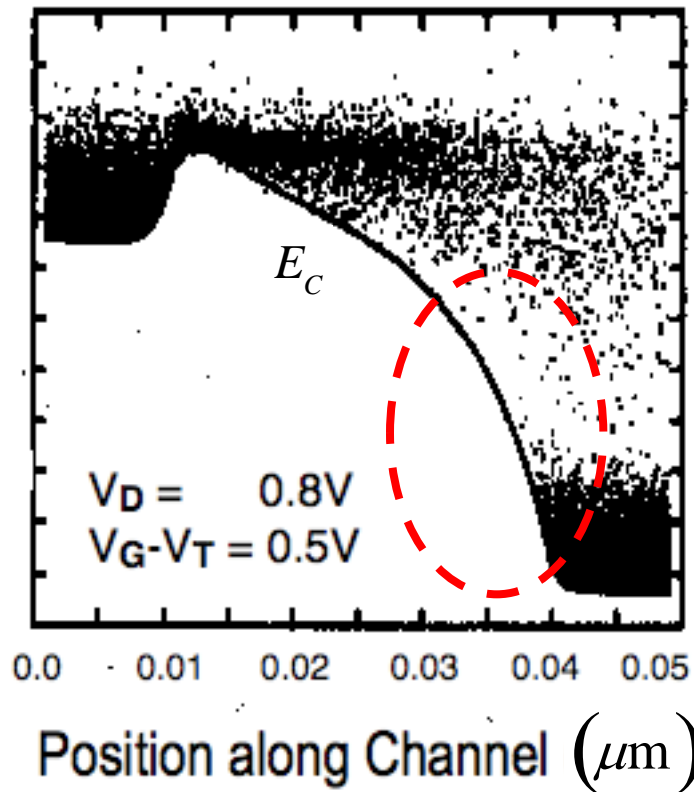
$$\lambda \approx 16 \text{ nm}$$

$$D_n \approx 8 \text{ cm}^2/\text{s}$$

$$v_x = \mu_n \mathcal{E}_x$$

$$L \gg \lambda$$

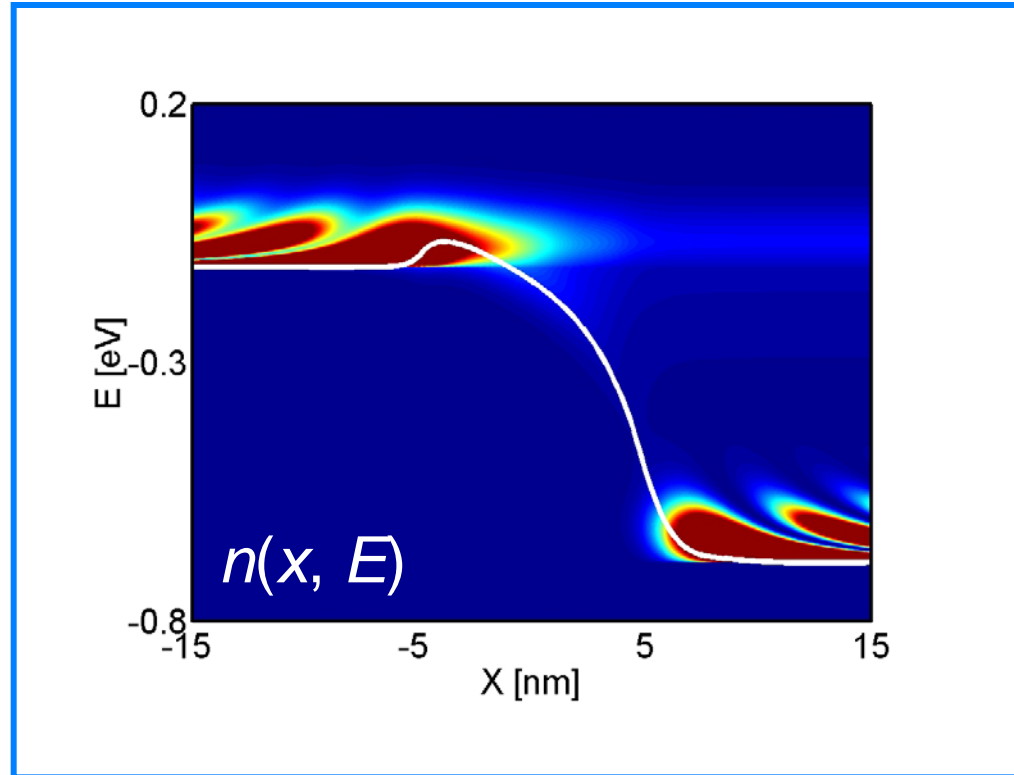
semiclassical transport in nanoscale MOSFETs



D. Frank, S. Laux, and M. Fischetti, Int. Electron Dev. Mtg., Dec., 1992.

quantum transport

$L = 10$ nm



nanoMOS (www.nanoHUB.org)

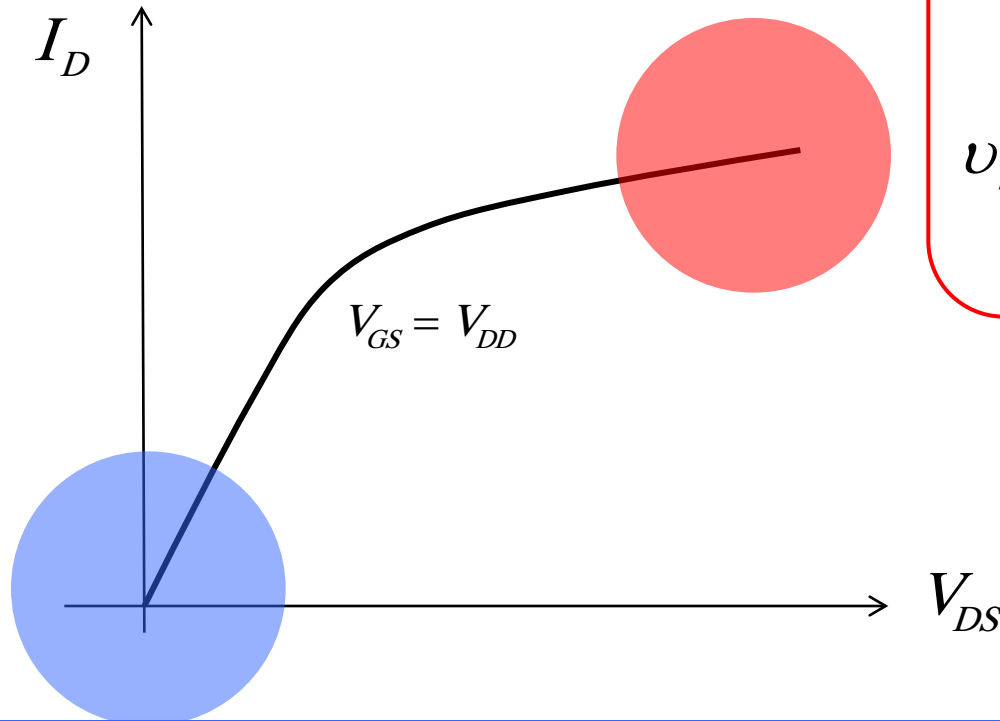
simple models and sophisticated simulations

“It is nice to know that the computer understands the problem,” said Wigner when confronted with the computer-generated results of a quantum-mechanics calculation.

“But I would like to understand it, too.”

(<http://www.cs.utah.edu/~crj/quotes.html>)

microscale → nanoscale MOSFETS

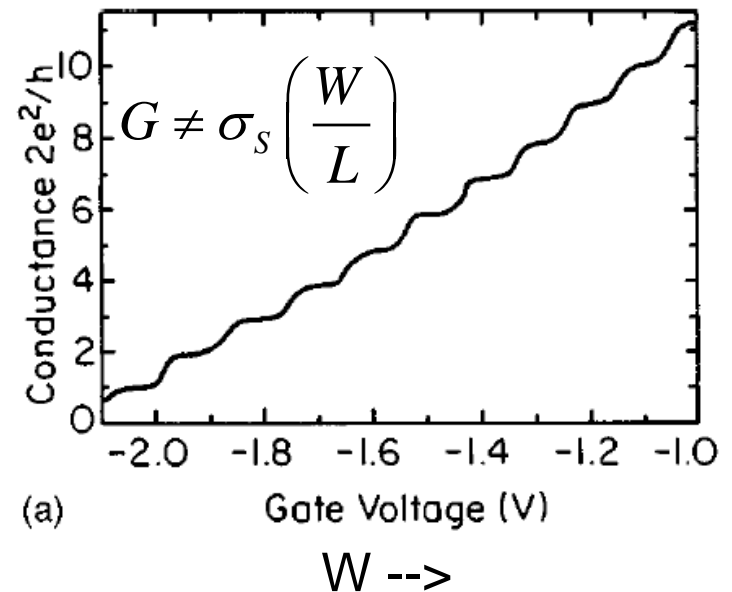
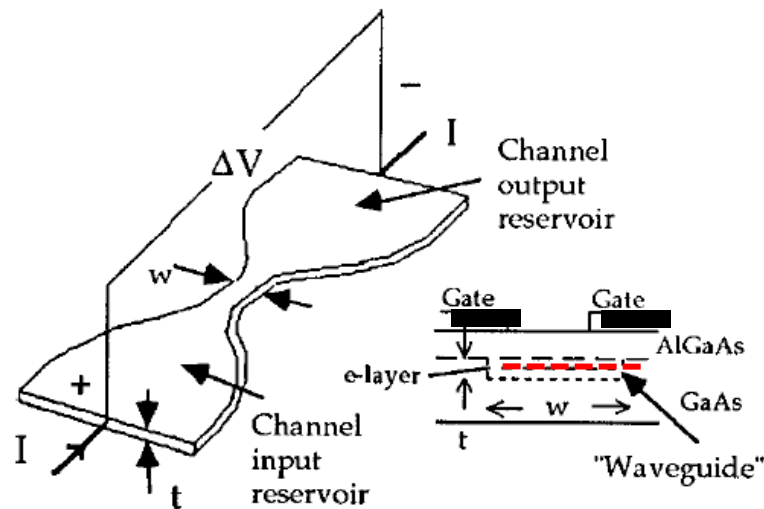


$$I_D = WC_{inv}v_{SAT}(V_{GS} - V_T)$$

$$v_{SAT} \rightarrow \left(\frac{T_{sat}}{2 - T_{sat}} \right) v_{inj}^{ball}$$

$$I_D = \frac{W}{L} \mu_{eff} C_{inv} (V_{GS} - V_T) V_{DS} \quad \mu_{eff} \rightarrow \mu_{app} \quad 1/\mu_{app} = 1/\mu_{eff} + 1/\mu_B$$

ballistic transport and quantized conductance



B. J. van Wees, et al. *Phys. Rev. Lett.* **60**, 848–851, 1988.

$$G = \frac{2q^2}{h} T(E_F) M(E_F)$$

Landauer approach

- 1) conductance is quantized
- 2) upper limit to conductance

goals

- Develop a simple, physical understanding of nanoscale MOSFETs.
- Understand why the functional form of the IV characteristic is so similar to MOSFETs of 50 years ago.
- Briefly examine the limits of device scaling.

lectures

- 1) Introduction to transistors
- 2) The traditional model
- 3) The MOSFETs as a barrier controlled device

- 4) MOS electrostatics
- 5) Carrier transport

- 6) Ballistic model
- 7) Comparison to experiments
- 8) Relation to traditional model
- 9) Scattering and transmission
- 10) Scattering model

- 11) MOSFET limits and possibilities

nanoHUB-U short course Fall 2012

