PUBLICATIONS from our group related to p-Bits for Probabilistic Spin Logic (PSL)

**Review Article**


**Bayesian Inference and Optimization**

- B.Behin-Aein, V.Diep and S.Datta “A Building Block for Hardware Belief Networks”, *Scientific Reports, 6*, 29893 (2016).

**Invertible Boolean Logic**


**Implementation with Micro-controllers**


**FPGA Implementation**


**Embedded MRAM based implementation: SPICE simulation**

Emulating qubits using p-bits


Experimental Results